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Adaptive-Speed/CAV Algorithm in a CD-ROM Drive to Accomplish High Data Transfer Rate and Low Power Consumption

Sorin G. Stan, Henk van Kempen, Chi-Cheng Steve Lin, M.-S. Mason Yen and Wai W. Wang

I. INTRODUCTION

The performance of a CD-ROM drive is mainly given in terms of data transfer rate, average access time, CPU (central processing unit) utilization, and power consumption. The first two and the latter performance indicators purely characterize the drive itself whilst the CPU utilization measures the processor load on the host computer. The CPU utilization is primarily determined by the data path firmware and the device driver installed on the host side and it is not of interest for this paper.

In order to optimize the performance of a CD-ROM drive, a trade-off has always been made between high data transfer rate and low access time. However, the need for miniaturization as well as the boost of the drive speeds have triggered the optimization towards power consumption. The current drawn from the power supply can be reduced especially when continuous data access is performed. In this paper, an adequate algorithm is reported which is able to automatically distinguish between continuous playback and continuous access so that the drive performance can be optimized separately for high data transfer rate, low access time and low power consumption, respectively.

II. CLV, CAV AND QUASI-CLV MOTOR CONTROL

As the data on a CD-ROM disc is uniformly distributed along a continuous spiral, the constant linear velocity (CLV) control remains the only way to drive the spindle motor such that a maximum average data transfer rate can be achieved. However, it has been shown [1] that pure CLV becomes obsolete when the pickup head moves very fast from one location to another along the disc radius. Alternatively, there are two solutions: either (i) implementing a full constant angular velocity (CAV) motor control or (ii) controlling the spindle motor within a quasi-CLV loop, as reported in the mentioned paper.

When compared with CAV control, the quasi-CLV solution does not worsen the averaged data transfer rate of the system whilst still preserving the access performance of the CAV mode. Moreover, the quasi-CLV can serve as basic control loop for an adaptive-speed CD-ROM drive [1]. Such a drive features a real-time adjustment of the disc rotational frequency dependent on the read-out position of the optical pickup unit. By adequately choosing the speed profile between the inner and outer disc radii, an optimal trade-off between data throughput and access time can be achieved.

III. THE ADAPTIVE-SPEED/CAV TRADE-OFF

The quasi-CLV control will always brake or accelerate the spindle motor while seeking outwards, respectively during inward-oriented seeks. This behavior is nothing but a consequence of using the real disc speed to drive the motor and compare it with a fixed reference.

An adaptive-speed CD-ROM system will further adjust this reference as dependent on the subcode timing. How-
ever, during continuous data access, the motor speed neither arrives at a steady-state nor reaches its target quasi-CLV velocity. Since the motor driver is driven in saturation, additional power dissipation will take place.

At high and very high disc speeds, e.g., 20X, 24X, or 30X, the heat generated in the spindle-motor loop (namely in the motor driver and motor itself) reaches excessive values. The power dissipation becomes therefore an important design parameter. As a matter of fact, we are not interested in maximizing the data transfer rate during continuous data access. It follows that a less aggressive motor control should be employed during this operating mode and the best choice is still the CAV.

Based upon the above discussions, the essence of CD-ROM performance optimization presented within this paper consists of 1) adaptive-speed profile based on quasi-CLV control during continuous playback and 2) CAV mode during continuous seek. The adaptive-speed profile should be optimized such that a maximum of the average bit rate is obtained while reading out data from the inside to the outside of the disc. An example of such optimization which leads to a cubic overspeed\(^3\) profile is presented in [3]. Further, the adaptive-speed profile can also be refined for a short sequence of data seeks when the CAV control does not need being switched on.

IV. CIRCUIT FUNCTIONAL PRINCIPLE

The equipment used in this study is a 20X CD-ROM drive made by Acer Peripherals, Inc., which is based on Philips key components. The block diagram of the spindle motor control loop is illustrated in Fig. 1. For a long sequence of seeks when the CAV control loop is desired, the motor will be driven by a tacho circuit or merely by an open-loop voltage. Due to heavy competition within the CD-ROM industry, the open-loop control is a preferred solution as long as the margin for overspeed tolerance can be maintained.

V. FIRMWARE ALGORITHM FOR SWITCHING BETWEEN ADAPTIVE-SPEED AND CAV

In order to detect the continuous-seek operating mode of the drive, a suitable algorithm should be provided. This algorithm will interfere with the motor control loop, switching on the CAV mode if necessary. Nevertheless, a certain convention should also be agreed upon such that a clear separation can be made between different repetitive seek-playback patterns. Any other operating modes which do not fulfill the requirements for continuous seek (e.g., short seeks or continuous playback) are flagged accordingly and the adaptive-speed motor control is kept active.

The detection mechanism monitors some variables related both to the seek length and to the number of seeks performed within a certain time. The number of long-range seeks is recorded by a counter and, if enough seeks have been performed within a given time interval, the counter will be reset and the CAV mode will be switched on. In addition, depending on whether or not a read command has been issued by the host system, the algorithm keeps either the CAV mode or the adaptive-speed mode activated. This decision depends upon not only the size of the available empty buffer (which can still be used by the data path at that particular moment) but also on the amount of data to be transferred towards the host system. This approach ensures an optimized flow of data through the host interface by monitoring the data flow coming from the bit engine. The disc speed can thus be regulated and better correlated with a given strategy for the buffer management. This algorithm is implemented at the data-path firmware level and its corresponding flow chart is illustrated in Fig. 2 and Fig. 3.

VI. EXPERIMENTAL RESULTS

The efficiency of the proposed algorithm has been tested by monitoring the heat dissipation in the spindle motor and the power consumption of the motor driver. Table 1 shows that the power dissipation is significantly reduced when the CAV motor control is activated during continuous access which leads to a lower temperature of the spindle motor.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Regular mode</th>
<th>Adaptive-speed/CAV mode</th>
</tr>
</thead>
<tbody>
<tr>
<td>Spindle motor temperature</td>
<td>60 °C</td>
<td>41 °C</td>
</tr>
<tr>
<td>Power consumption (motor driver)</td>
<td>4.26 W</td>
<td>1.79 W</td>
</tr>
</tbody>
</table>

Table 1. Efficiency of the adaptive-speed/CAV algorithm after 40,000 continuous seeks at the ambient temperature of 23 °C.
Fig. 2. Flowchart (1/2) of the switching algorithm implemented at the data-path level.

Fig. 3. Flowchart (2/2) of the switching algorithm implemented at the data-path level.
VII. DISCUSSION

For the CD-ROM system reported in this paper, the adaptive-speed profile is bordered by the overspeeds $N_{in} = 10X$ at the inner disc radius and $N_{out} = 20X$ at the outer disc radius, respectively. The profile is optimized [3] such that the maximum overspeed $N_{max} > N_{out}$ which can be handled by the system is never exceeded at the end of a random outward-oriented third-stroke seek. The braking possibilities of the spindle motor are also taken into account.

On the other hand, the overspeeds $N_{cav}$ which occur during continuous-seek are simply radius-dependent and are situated along a pure CAV profile. They satisfy the relation

$$N_{out} \frac{R_{in}}{R_{out}} \leq N_{cav}(R) = \frac{2\pi f_{rot} R}{v_{a}} \leq N_{out} \quad (1)$$

where the disc radius $R$ varies between $R_{in}$ and $R_{out}$ corresponding to the start and respectively end of the program area and $f_{rot}$ is the disc rotational frequency. Obviously, when $f_{rot}$ is properly chosen with respect to all tolerances in the system, the maximum allowed overspeed $N_{max}$ will never be exceeded during whatever length of a seek.

VIII. CONCLUSION

An algorithm to distinguish the continuous-seek mode from other operating modes is proposed so that a CAV control of the spindle-motor loop can be activated while performing continuous seeks. The data transfer rate of the CD-ROM system during continuous seek is of less importance but the substantial decrease in power consumption within the motor loop represents an important achievement.

REFERENCES


Sorin G. Stan was born in Braşov, Romania, on April 16, 1964. He received a M.Sc. (EE) degree from the Transylvania University at Braşov in 1988. He worked after graduation at Tohan Mechanical Company, in Romania, where he was involved in various technical projects for the defense industry, especially on the area of measurement systems, data acquisition techniques and electro-hydraulic control. He also received a Master of Electronic Engineering degree from the Eindhoven International Institute of the Eindhoven University of Technology, The Netherlands, in 1992. He joined afterwards Philips Electronics N.V. as a member of the Optical Recording Laboratory of Philips Laser Optics, in Eindhoven. Since 1992 he has carried out development work on CD-ROM systems, specifically aimed to increase the data transfer rate and decrease the access time of the drives. His current research interests include the decoding and servo circuits for CD and DVD systems. Mr. Stan is also pursuing his Ph.D. study at the Eindhoven University of Technology. He is the author and coauthor of several patents and scientific papers.

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Wai W. Wang was born in Taiwan on April 4, 1957. He received his BS Electro-Physics and MS Electro-Optics degrees from National Chiao Tung University of Taiwan in 1980 and 1984, respectively. He also received a MSEE degree from University of New Mexico, Albuquerque, New Mexico, U.S.A. in 1986. In 1987, he joined Optotech Inc. at Colorado Springs, Colorado, USA for optical head and disk drive development. In 1990, he was with Applied Solar Energy Corporation for Laser diode array development. In 1991, he continued his optical recording media research at University of Colorado and received his Ph.D. degree in 1993. During his Ph.D. study, he also worked in Lightwave Corporation at Colorado Springs, Colorado, for MO disk drive development until he joined Acer Peripherals, Inc., Taiwan, in March 1994. Since then he has been involved at Acer in optical recording product development. His research interests include servo control, media, and optical pick-up. Dr. Wang was the principle author for more than 12 technical publications and patents.