Aggregation of Component-Based Grid-Feeding DER and Load Models for Simulation of Microgrid Islanding Transients

M.H. Roos*, P.H. Nguyen†‡, J. Morren†, J.G. Slootweg†
*Department of Electrical Engineering
Eindhoven University of Technology, Eindhoven, the Netherlands
Email: m.h.roos@tue.nl
†Asset Management,
Enexis Netbeheer, ’s-Hertogenbosch, the Netherlands
‡Sustainable Energy Systems Group,
Luxembourg Institute of Science and Technology, Belvaux, Luxemburg

Abstract—The reliability of distribution networks can be improved by islanding of part of the network when a short-circuit fault occurs. The voltage and frequency stability during transient can be evaluated with time-domain simulations using component-based DER and load models. However, simulations with this type of models have a relatively high computational burden which constrains periodical stability analysis of microgrids. This paper proposes an aggregation methodology which significantly decreases the computation time of fault-initiated islanding simulations with component-based models, while maintaining the nonlinear dynamics. The methodology is first validated by directly comparing the original and equivalent models during large voltage and frequency transients. Secondly, a fault-initiated islanding case study of a residential distribution network is performed. For both validations, a sensitivity analysis of model parameters and voltage variations is performed. The results indicate the methodology preserves the dynamics of DERs and loads, while significantly reducing the computational time.

Index Terms—Dynamic equivalent model, microgrids, islanding, distributed generation, power system transients.

I. INTRODUCTION

The increasing societal dependence on electrical energy and the occurrence of natural disasters fuels the need for more reliable and resilient power (distribution) networks [1]. The reliability of distribution networks can be increased by fault-initiated islanding (FII), which forms islanded microgrids in case of a contingency such as a short-circuit fault [2], [3]. During the FII transient, the voltage and frequency can significantly deviate from their nominal values due to interactions between the phase-locked loop of distributed energy resources (DERs) and the load [4], and the lack of voltage and frequency control. Therefore the stability of the microgrids should be evaluated to enable FII, especially during and right after the FII transient [5]. The stability analysis should be performed periodically due to the variability of generation, network structure and load in microgrids.

To analyze the stability with time-domain simulations, accurate DER and load models are required which represent the nonlinear behavior during transients. The dynamics of different subsystems are strongly coupled in microgrids [6] requiring comprehensive DER and load models. Component-based models can model the behavior of DERs and load devices with a very high level of detail, accurately describing the electrical, mechanical, controllers and protection systems [7], [8]. Although component-based models can describe the behavior of the DERs and loads with high accuracy, these models increase the required computation time which constrains the ability to periodically perform FII stability analysis.

The computation time can be reduced by aggregation of the DER and load models. Three main methodologies for load aggregation have been described in the literature: modal analysis, coherency based aggregation and system identification [8]. System identification techniques are not suitable for detailed component-based models. Modal analysis requires linear models which neglects important nonlinear dynamics.

During coherency based aggregation, models with similar dynamics are replaced by an equivalent model. Coherency based methods have been mostly used for aggregation of synchronous and induction machines [9]–[11]. Aggregation of power electronic load models has been performed by [12], [13], however until now only the steady-state behavior of these aggregated models have been validated.

In this paper, a component-based DER and load model aggregation methodology is proposed which reduces the computation time of simulations of FII microgrids significantly, while preserving the dynamic behavior for accurate stability analysis. The methodology is validated by direct comparison between the original and aggregated models, and a FII case
study in a modified version of the CIGRE European LV residential distribution benchmark network.

The contributions of this paper are:

1) The aggregation methodology for component-based DER and load models which decreases computation time of FII microgrid simulations while maintaining accurate dynamics.

2) Verification of the accuracy of component-based load and DER device equivalencing under parameter variation and voltage differences.

3) Verification of the accuracy and reduction of computational burden of the methodology with a FII case study under different network impedance, and DER and load models parameter variations.

In the next section the aggregation methodology is described, the numerical validation of the methodology is discussed in section III, the results are shown in section IV and discussed in section V, and finally in section VI conclusions are given.

II. AGGREGATION METHODOLOGY

The methodology consists of several steps to transform a network consisting of nodes and branches into a reduced network with a common node to which the grid-feeding DERs and load devices are connected. An overview of the aggregation methodology is shown in Fig. 1. First, the network nodes are classified based on the connected DERs and load devices, which are classified based on the different device types. Second, a Kron reduction is applied to eliminate the interior and empty nodes while maintaining the boundary and device nodes. Third, a common node is created to which all grid-feeding DERs and detailed load models are connected instead of the network nodes. The power weighted average (PWA) of the devices is used to create equivalent current sources at the network nodes and an equivalent voltage source at the common node. Final, dynamic equivalent models are created for each type of device based on the device classification.

A. Classification

Network nodes are classified into four categories: boundary, device, interior and empty. Nodes to which the point of common coupling (PCC) and/or grid-supporting DERs are connected are classified as boundary nodes. Nodes to which grid-feeding DERs and/or detailed load models are connected are device nodes. Nodes to which only constant impedance loads are connected are interior nodes, and nodes without DERs or load devices are empty nodes.

Devices are classified into different categories based on the type of device to cluster devices with similar structure to allow the development of accurate equivalent models. Additionally, classification can be performed based on electrical, mechanical, controller, and protection system parameters. A larger set of classes can increase accuracy, however this results in more equivalent models, increasing the computation time.

B. Network Reduction

1) Kron reduction: Kron reduction is a network reduction methodology which reduces the number of nodes and constant impedance loads in the network. The boundary and device nodes are critical for voltage stability analysis as they contain all detailed, nonlinear models. Therefore, these nodes are preserved and Kron reduction is utilized to eliminate interior and empty nodes. Since the load connected to boundary and detailed load nodes does not impact the Kron equivalent network impedances, the DERs and detailed loads are neglected during Kron network reduction.

To perform Kron reduction, the network admittance matrix $Y_{Net}$ is build as described by equation 1. Where $N$ is a set of boundary and detailed load nodes, $M$ is a set of interior and empty nodes, $K$ is a set of detailed load nodes, $P$ is a set of device types and $Q$ is a set of devices of each type. Matrices $Y_{NN}$, $Y_{NM}$ and $Y_{MM}$ contain the node admittance and admittance between nodes of set $N$, $N$ and $M$, and $M$ respectively.

$$Y_{Net} = \begin{bmatrix} Y_{NN} & Y_{NM} \\ Y_{TN} & Y_{MM} \end{bmatrix}$$  \hspace{1cm} (1)

The Kron reduced admittance matrix is determined by using equation 2 [14].

$$Y_{kron} = Y_{NN} - Y_{NM} Y_{MM}^{-1} Y_{NM}^T$$  \hspace{1cm} (2)

2) Creation of common node: Before equivalent models can be developed for each type of device, the models should be connected to the same node. Therefore, a common node is created to which all grid-feeding DER and detailed load models are connected. The common node is fed by a voltage source which provides the PWA of the voltage of the device nodes as shown in Fig. 1. The device nodes are replaced by empty nodes with current sources which absorb or inject the PWA of the current of the device models connected to the common node.
The PWA of the voltage ($V_{eq}$) is determined by equation 3, while the PWA of the current injected at node $k$ ($I_{eq}^k$) is determined by equation 4. Where $V_k$ is the voltage at node $k$, $S_k$ is the total apparent power absorbed by the devices at node $k$, $S_{p,k}$ is the total apparent power absorbed by devices of type $p$ connected to node $k$.

$$V_{eq} = \sum_{k=1}^{K} V_k \frac{|S_k|}{\sum_{k=1}^{K} |S_k|}$$  \hspace{1cm} (3)$$

$$I_{eq}^k = \sum_{p=1}^{P} \sum_{k=1}^{K} S_{p,k} \frac{1}{\sum_{q=1}^{Q} I_{p,q}}$$  \hspace{1cm} (4)$$

### C. Device equivalencing

As shown in Figs. 2, 3 and 4, component-based models of DER and load devices consist of several components. By scaling the parameter values of the components, the models can be scaled to different power ratings with equal dynamics. Since two parallel devices with identical parameters can be represented as a single device with two times the power rating, this concept is fundamental for the device equivalencing methodology. For example, a constant impedance load can be scaled to different power ratings by changing the impedance. Likewise, two parallel constant impedance loads can be represented by a single constant impedance load with the equivalent impedance.

This paper considers electrical, mechanical, controller and protection system parameters for device equivalencing. The remainder of this section describes how these parameters are determined. Note that as the models in this paper are nonlinear, the accuracy of the equivalent model depends on how similar the parameters of the original devices are. When two devices have very different parameters e.g. mechanical loads with similar power ratings and a large inertia mismatch, these devices should be aggregated into multiple models as discussed in section II-A.

1) **Electrical parameters:** The electrical parameters are determined according to the parallel impedance. Resistance $R_{eq}$, impedance $L_{eq}$ and capacitance $C_{eq}$ values are determined with equation 5 where $D$ is the number of parallel devices.

$$R_{eq} = \frac{1}{\sum_{i=1}^{D} R_i}, \quad L_{eq} = \frac{1}{\sum_{i=1}^{D} L_i}, \quad C_{eq} = \sum_{i=1}^{D} C_i$$  \hspace{1cm} (5)$$

2) **Mechanical load parameters:** The equivalent inertia $J_{eq}$ is equal to the sum of the inertia, friction $F_{eq}$ and torque $T_{eq}$ are equal to the sum of the inertia, friction and torque of the devices as described by equation 6.

$$J_{eq} = \sum_{i=1}^{D} J_i, \quad F_{eq} = \sum_{i=1}^{D} F_i, \quad T_{eq} = \sum_{i=1}^{D} T_i$$  \hspace{1cm} (6)$$

3) **Controller parameters:** DER and load devices are generally controlled by PI controllers. The equivalent proportional ($k_P$) and integral ($k_I$) controller parameters are determined by the PWA values as described by equation 7.

$$k_{P,eq} = \sum_{i=1}^{D} \frac{k_{P,i}}{\sum_{d=1}^{D} |S_d|}, \quad k_{I,eq} = \sum_{i=1}^{D} \frac{k_{I,i}}{\sum_{d=1}^{D} |S_d|}$$  \hspace{1cm} (7)$$

4) **Protection parameters:** Mainly three types of internal protection systems are implemented in DER and load devices: overcurrent, under-/overvoltage and under-/overfrequency. Overcurrent protection usually limits the peak value of the current and therefore only requires a maximum setting. The setting of the equivalent model is equal to the sum of the overcurrent protection settings of the devices.

The AC under-/overvoltage and under-/overfrequency protection of DERs are dictated by the anti-islanding and fault-ride-through requirements in the local grid codes. Although this is often equal for all DERs, different requirements may be specified for different DERs (e.g. power rating). This can be solved by classifying DERs in different types.

The DC undervoltage protection of load devices causes temporary or permanent load drop-off. The setting of DC undervoltage protection commonly consists of a minimal threshold value, the setting of the equivalent model is therefore equal to the PWA value of the DC undervoltage protection settings of the devices.
III. NUMERICAL VALIDATION

The methodology described in the last section is validated by comparing the original and equivalent models during large voltage and frequency transients, and by comparing the voltage waveforms during FII in a case study.

A. Devices

Three types of devices are considered: compact fluorescent lighting (CFL) load, variable frequency drive (VFD) load and three phase photovoltaic (TPPV, 8kHz switching frequency) DER as shown in Figs. 2, 3 and 4. The electrical parameter values of the models are shown in Table I. The CFL has a constant current equivalent resistance \( R_{eq} \) with a drop-off voltage of 35V. The VFD has a direct torque controller with controller gains \( k_{pd} = 0.005 \), \( k_{id} = 100 \) and a speed controller with controller gains \( k_{ps} = 10 \), \( k_{is} = 4 \) to drive an 11kW symmetrical induction machine modeled in the dq reference frame as described by [15]. The VFD drop-off voltage is 420V with a hysterisis of 30V. The TPPV has a synchronous reference frame phase-locked loop (PLL) with controller gains \( k_{ppd} = 30.87 \), \( k_{ipd} = 1029 \) and a current controller in the dq reference frame with gains \( k_{ppi} = 1.63 \), \( k_{ippi} = 204 \). The passive anti-islanding protection trips when the voltage \( V \leq 0.7 \text{pu} \) for 10ms, \( V \geq 1.05 \text{pu} \) or \( V \leq 0.8 \text{pu} \) for 1.95s, or if the frequency \( f \geq 75 \text{Hz} \) or \( f \leq 25 \text{Hz} \) for 1ms or \( f \geq 51 \text{Hz} \) or \( f \leq 48 \text{Hz} \) for 2s. The original are scaled to higher power ratings. The scaled CFL models have a power rating of 2.7kW, 5.4kW, 10.8kW and 21.6kW divided over three phases. The scaled VFD models have a power rating of 11kW, 22kW, 33kW and 44kW. The scaled TPPV models have a power rating of 4kW, 8kW, 16kW and 32kW. Thus, the equivalent CFL, VFD and TPPV models have a power rating of 40.5kW, 110kW and 60kW respectively.

\[
e(t) = \frac{1}{T} \int_{t=0}^{T} |I_{eq}(t) - I_{or}(t)| \, dt \quad (8)
\]

The NMAE is determined over a period of 500ms during which a voltage or frequency step change takes place. Voltage transients are performed by step changing the voltage from 1.0pu to 0.1pu and back to 1.0pu after 200ms. Frequency transients are performed by a step change from 50Hz to 20Hz and back to 50Hz over the same period. Pre- and post-disturbance performance are thus analyzed over 150ms before and after the transient. This mimics the temporary voltage and frequency deviations which are expected during the FII transient. The transients are initiated at a phase angle of 90°, where the voltage waveform of phase a is at its peak value.

As devices in a network may have different parameters and the nodes to which the devices are connected may have voltage differences, a sensitivity analysis is performed by varying the parameters and voltage difference between the devices. The difference between the electrical, mechanical and controller parameters of the devices are varied between 0.3pu and 1.7pu according to table II (additional to scaling due to the different power ratings). The voltage differences are varied between 0.1pu and 0.4pu according to table III. In this scenario, the equivalent model is connected to the PWA of the voltages of devices 1, 2, 3 and 4.

C. Case study

A modified version of the CIGRE European LV residential distribution benchmark network is used to validate the load aggregation methodology as shown in Fig. 6. The network contains two three-phase battery energy storage systems (BEss) which are modeled as TPPV normally operating in grid-feeding operation and switch to grid-supporting operation when the anti-islanding detection trips. The grid-feeding and grid-supporting controllers are modeled as described by [16].
A three phase fault is simulated in the external grid at \( t = 0.1s \) by reducing the voltage of the external grid to 0.1pu. At \( t = 0.15s \) the network is islanded by opening the point of common coupling. After islanding, the islanding detection systems of the BESS will trip and switch the BESS from grid-feeding to grid-supporting operation.

The network nodes are classified in the following way: nodes 1, 2 and 5 are boundary nodes, nodes 3, 4 and 6 are device nodes, nodes 7 and 9 are interior nodes, and nodes 8 and 10 are empty nodes. The grid-feeding DER and load devices at each node are shown in table IV. The constant impedance loads are modeled as parallel RL branches with a power factor of 0.95. The network consist of 240mm\(^2\) AL cables modeled as series RL branches with a resistance and reactance of 0.15Ω/km and 0.071Ω/km respectively. The parasitic cable capacitance is neglected due to the short length. The network cables lengths are shown in table V.

Three scenarios are analyzed to evaluate the performance of the aggregation methodology in different conditions. Scenario 1 is the baseline where the parameters of the grid-feeding DER and load models of the same type are scaled from the models described in III-A based on power rating. In scenario 2, the electrical (impedance), mechanical (inertia and friction) and controller (controller gains) parameters of the grid-feeding DER and load models are multiplied by the corresponding multipliers shown in table IV. This scenario emulates a case in which DERs and loads have different parameters. In scenario 3, the network impedance is five times larger which emulates a network with larger voltage differences between DERs and loads.

### Results

A. Device equivalencing

As discussed before, large differences between the parameters of devices and the voltages of different network nodes may impact the accuracy of the aggregated model. The parameter and node voltage variations are simulated as described in section II-C. The NMAE of the equivalent CFL, VFD and TPPV models with different parameter variations in case of a voltage and frequency transients is shown in Figs. 7 and 8 respectively. The NMAE is below 0.1pu in almost all cases which indicates that the equivalent models accurately represent the dynamics of the original models and that a larger variation in parameters generally leads to an increasing error. The impact of parameter variations is most significant in case of VFDs, however even with a variation of 1.4 p.u. the maximum NMAE is smaller than 0.05. The NMAE does not increase linearly with the parameter variation due to nonlinear effects. For example, when the parameter variation is 0.6pu or larger the smallest VFD drops off during a voltage transient, while the largest VFDs and the equivalent model remain operational.

The NMAE of the current absorbed injected by the equivalent CFL, VFD and TPPV models with different parameter variations in case of a voltage transient is shown in Figs. 9 and 10 respectively. The NMAE increases when the node voltage variation increases in three cases, while the NMAE remains approximately constant in three other cases. Node voltage variation mostly impacts the accuracy of the equivalent TPPV due to PLL tracking issues during the frequency transients. The equivalent model of the TPPV is therefore considered accurate during frequency transients when the voltage variation is smaller than 0.3pu.

B. Case Study

The voltage waveforms at node 2 in the original and aggregated networks are shown in Figs. 11, 12 and 13 for scenario 1, 2 and 3 respectively. The dynamics during the FII transient are similar in all scenarios. At \( t = 0.1s \) the
fault causes the voltage to decrease significantly. When the network is islanded at \( t = 0.15\text{s} \) the voltage increases while the frequency slightly decreases. Islanding is detected by the BESS around \( t = 0.21\text{s} \) in all scenarios, recovering the voltage and frequency to near nominal values. The voltage waveforms remain partly distorted after control-mode switching due to the nonsinusoidal current waveforms of power electronic loads.

As shown in Figs. 11, 12 and 13, the original and aggregated voltage waveforms are very similar during and after the FII transient. Slight differences are visible during islanding before the BESS switch to grid-feeding control. The results from scenario 2 show that parameter differences between models impact the accuracy of the aggregated model. The increased network impedance has no significant negative effect on the accuracy of the aggregated model.

The simulations are performed in Matlab/Simulink using the ode23tb solver with a maximum step size of \( 1\mu\text{s} \) on a PC with an Intel i7-4700MQ processor and 8GB of RAM. The computation time of the three scenarios is averaged. The average computation time to simulate a single scenario is 36837 seconds using the original model and 4909 seconds on a PC with an Intel i7-4700MQ processor and 8GB of RAM. The reduction in computational burden enables periodical (bi-hourly) analysis of the voltage and frequency stability analysis during and right after FII, even on a laptop computer.

Detailed component-based DER and load models are required to develop the aggregated model. This information may not be available to the microgrid operator, which requires the load to be identified. A suitable methodology to identify load devices based on aggregated measurements is proposed in [17]. Alternatively, smart meters can be utilized to identify load devices as described by [18].

The accuracy of the aggregated model is mainly impacted by parameter variations between different DER and load models of the same type. This is evident as models with very different dynamics are difficult to represent with an equivalent model. This can be solved by increasing the number of grid-feeding DER and load classifications, however this also increases the computation time as this results in additional equivalent models. Optimization of the number of DER and load type classifications should therefore be treated by future research.

V. DISCUSSION

The results indicate that the proposed methodology accurately aggregates component-based grid-feeding DER and load models which greatly reduces the computational burden of FII simulations. The error of the dynamic equivalent models developed in section II-C increases when the devices of the same classification have a larger variation in parameters and when the devices are connected to nodes with different voltages. It should be noted that the device equivalencing negates possible interactions between devices of the same type. However the impact on the simulation results is limited since these interactions mainly occur between droop controlled grid-supporting DERs [6], which are preserved in the proposed methodology. Additionally, a small-signal stability analysis approach is often preferred to analyze the high frequency (voltage and current controller) interactions in microgrids.

The reduction in computational burden enables periodical (bi-hourly) analysis of the voltage and frequency stability analysis during and right after FII, even on a laptop computer.
Large voltage differences between network nodes also impact the accuracy of the aggregated model, however this error is less significant since the voltage differences in microgrids are small, even with high network impedance.

There are many different types of DERs and loads connected to distribution networks [19]. Additionally, these devices can have different dynamics e.g. due to the type of control [20], [21], phase locked loop [22], anti-islanding detection [23], power factor correction [19] and mechanical load [24]. Several component-based DER and load models have been proposed in the literature [7], [8], however more DER and load models should be developed and validated in future research to enable analysis of microgrids with different types of DERs and loads.

VI. CONCLUSION

This paper describes a methodology to aggregate grid-feeding DER and load models in microgrids to reduce computational burden while enabling accurate voltage and frequency stability analysis during and right after FII. The comparison of original and equivalent DER and load models, and the case study show that the proposed methodology constructs accurate aggregated models. In the case study the computation time of the aggregated model is reduced by 87% while a small error is introduced, enabling accurate periodical stability analysis.

Future research should treat the optimal classification of load types before equivalencing, and the development and validation of more component-based models.

REFERENCES


