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Setting Planned Leadtimes in Customer-Order-Driven Assembly Systems

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We study an assembly system with a number of parallel multistage processes feeding a multistage final assembly process. Each stage has a stochastic throughput time. We assume that the system is controlled by planned leadtimes at each stage. From these planned leadtimes the start and due times of all stages can be derived. If a job finishes at a particular stage and has to wait before the start of the next job(s), a holding cost proportional to the waiting time is incurred. A penalty cost proportional to the lateness is incurred when the last stage of the final assembly process finishes after its due time. The objective is to determine planned leadtimes for each individual stage, such that the expected cost of a customer order is minimized.

We derive the recursive equations for the tardiness and earliness at all stages and an exact expression for the expected cost. We discuss the similarity between these expressions and those for serial inventory systems. Based on this observation and a conjecture related to the generalized Newsvendor equations, we develop an iterative heuristic procedure. Comparison with a numerical optimization method confirms the accuracy of the heuristic. Finally, we discuss an application of the model to a real-life case, showing the added value of a system-wide optimization of planned leadtimes compared to current practice.

Keywords: planned leadtimes; customer-order-driven; assembly systems

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1. Introduction
The aim of any supply chain is to provide timely delivery of products in the most cost-effective way. Attaining this goal is a particularly difficult challenge for companies in the capital goods industry because of their low and highly unpredictable demand, low production volumes, long procurement leadtimes, huge capacity requirements, and low inventory turnovers (Hicks 2004, Hicks and Pongcharoen 2006).

The semiconductor industry shares some of the challenges and characteristics of the capital goods industry. As a technology enabler, the semiconductor industry has grown rapidly from its formation in 1960 to a $305.6 billion market in 2013 (Rosso 2014). A well-known company contributing to this growth is ASML (originally ASM Lithography). Founded in 1984 and headquartered in Veldhoven, the Netherlands, ASML is the world’s leading provider of lithography systems with $5.86 billion in sales in 2014 (ASML 2014). These are complex and expensive systems that are used in the production of integrated circuits and microchips.

As a supplier for the semiconductor industry, ASML’s demand pattern has a strong relationship with the demand for semiconductors. Historically, the semiconductor industry has been volatile, with periods of rapid growth followed by downturns. This implies a fluctuating demand for ASML’s systems. ASML’s customers include all of the world’s leading chip manufacturers, who use ASML’s systems to manufacture a wide range of different chips. ASML constantly improves the capabilities of its lithography systems, allowing the customers to make smaller, faster, and more energy-efficient chips. ASML adapts its technologies to its customers’ requirements by innovating its systems. As a result, the fluctuating demand, together with the risk of technology obsolescence, makes it infeasible to hold inventories of fully manufactured systems.

ASML faces uncertainties in the supply chain and its manufacturing processes. The company procures components and submodules from approximately 700 different suppliers. Careful component and submodule inventory management and intense communication between ASML and its suppliers (how to reschedule component and submodule orders) ensure that at the moment of order release all components and submodules are available for a timely start of their assembly into the main modules of the lithography systems. Both the assembly of submodules into
modules and the assembly of modules into systems involve complicated processes and extensive functional testing, which together require a large number of different operations.

High product complexity implies variability in ASML’s operations throughput times. As order release is planned such that assembly of submodules can start on their planned start date, ASML decouples the uncertainty in the manufacturing process from uncertainties in demand and supplies. Thus, after order release the only uncertainty to be taken into account is the throughput time uncertainty of the various phases in the assembly process.

Holding safety stocks and planning for safety times are two techniques that companies use to absorb uncertainties in demand, procurement, and manufacturing. At ASML, safety times are preferred since each type of system has its own specific modules. In fact, each module is associated with an end product. Because of the uniqueness of the modules, keeping safety stock is not an option. Another reason is the dynamic nature of the semiconductor industry. If a module is kept in stock and a redesign is executed, the module must either undergo rework or become obsolete. Both outcomes are costly and undesirable. Another problem is the need to produce and store the modules in cleanrooms with a controlled level of contamination. These rooms are expensive to build and maintain. Therefore, keeping safety module stocks implies high holding costs.

Multiple papers investigate the use of safety times versus safety stocks and provide guidelines on which technique works better under different circumstances. The simulation study by Whybark and Williams (1976) suggests using safety times instead of safety stocks when uncertainties in demand and supply are mostly due to timing rather than quantity. In another simulation study, Molinder (1997) concludes that using safety times instead of safety stocks results in lower costs when the variabilities in demand and leadtime are high at the same time. Yano (1987b) argues that when all units in a batched order are produced at the same time, the safety stock needs to be as large as the batch size. Therefore, using safety times should be a preferred strategy, especially when the batch size is large.

ASML and other companies using safety times as a buffering technique face the challenging problem of determining planned leadtimes for their processes. The planned leadtime is the sum of the average leadtime and the safety time. The difficulty of the problem arises from the interactions among multiple processes. The tardiness of one process might imply delays in the subsequent processes and, eventually, late delivery of products. In this paper, we study the problem of optimizing the planned leadtimes. Motivated by ASML’s manufacturing environment, we consider an assembly system that consists of multiple processing stages each delivering subassemblies (or modules) to its succeeding processing stage, eventually yielding the final product, which in this case is the lithography system. The throughput times at all stages are stochastic. The system incurs holding costs from the start of the process until delivery of the system to the customer and a penalty cost for late delivery of the final product. Our objective is to find the planned leadtimes of all the stages so that the sum of holding and penalty costs is minimized.

As acknowledged in Yano (1987b) and Axsäter (2005), it is difficult to obtain exact solutions for this problem, especially for large assembly systems. We contribute to the literature by proposing an iterative heuristic procedure that relies on a conjecture related to the generalized Newsvendor equations. We compare our heuristic with a procedure based on the well-known nonlinear optimization method of Davidon-Fletcher-Powell (Press et al. 2007). Our results indicate that the heuristic performs extremely well, with an average percentage cost difference from the Davidon-Fletcher-Powell (DFP) method of only 1.33%.

At ASML, the leadtimes are planned using a decomposition approach. For each planned stage, throughput time data are collected over a period of time and their mean and standard deviation are computed. The planned leadtime of each stage is set to a fixed percentile of the normal distribution with the computed mean and standard deviation.

Our contribution to ASML practice and thereby to the practice of order-driven manufacturing is as follows: First, we show empirical validity of our model by comparing the actual on-time delivery of two lithography systems and the most complicated main module with the on-time delivery percentage according to the model. Second, we show that the decomposition approach described above can be improved by considering the overall process from release of submodules to the delivery of the systems to the customer. Our optimization method yields overall cycle time reductions of 10%–11% compared to ASML’s decomposition method. When all cleanrooms are occupied, the throughput reduction of 10%–11% yields an increase in output of 11%–12%.

In the remainder of this section, we provide a review of studies related to the problem of setting planned leadtimes. We then describe ASML’s manufacturing environment and the procedure used at ASML for setting planned leadtimes. In §2, we explain the derivation of the average cost expression. We detail the development of the iterative heuristic procedure in §3. We analyze the performance of our heuristic in §4. The application of this at ASML is

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**Figure**

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discussed in §5. We provide concluding remarks and future research directions in §6.

1.1. Literature Review

The problem of determining the optimal planned leadtimes for general multistage production systems is recognized to be very difficult in terms of obtaining exact solutions. Earlier work focuses mostly on single-stage systems and systems with specific structures.

One of the earliest studies analyzing the single-stage problem is by Weeks (1981), who establishes the equivalence between the News-vendor problem and the problem of planned leadtime optimization. Subsequently, Matsuura and Tsubone (1993), Matsuura et al. (1996), and Buzacott and Shanthikumar (1994) develop single-stage models. The model by Matsuura and Tsubone (1993) is suitable for material requirements planning (MRP) systems and considers the trade-off between work-in-process inventories and variations in capacity requirements while determining the planned leadtime. The model by Matsuura et al. (1996) is for multioperation jobs and, similar to Matsuura and Tsubone (1993), it takes the trade-off between work-in-process inventories and capacity requirement variations on a bottleneck job into consideration. Buzacott and Shanthikumar (1994) compare the effectiveness of safety times versus safety stocks in single-stage MRP-controlled manufacturing systems. They conclude that safety times are only preferable to safety stocks when future required shipments over the leadtime can be accurately forecast. Otherwise, holding safety stocks is a better strategy to cope with changes in customer demands.

Earlier work on setting planned leadtimes in multiechelon supply chains includes Yano (1987a), Gong et al. (1994), and Yano (1987c). Yano (1987a) studies two-stage serial production systems and, assuming quasi-convexity of the cost function, develops an algorithm to solve it. Yano (1987a) generalizes this algorithm to serial systems with more than two locations. Studying the same system as Yano (1987a), Gong et al. (1994) show that the problem of determining the optimal planned leadtimes is equivalent to that of determining the optimal base-stock levels in serial inventory systems. Therefore, the well-known algorithm by Clark and Scarf (1960) can be used to find the optimal planned leadtimes. Yano (1987c) studies the same problem as Yano (1987a) and Gong et al. (1994) for one-warehouse, two-retailer distribution systems. The author suggests two heuristic policies that rely on the optimal solutions for the decoupled serial systems.

The first research on planned leadtimes for assembly systems is by Yano (1987b), who considers a two-component assembly process with stochastic component production/procurement and stochastic assembly processing times. The problem is to determine when to produce/procure the components and when to start the assembly process. On the one hand, inventory holding costs are charged if components wait until the assembly process starts and if the finished product waits to be shipped to the customer. On the other hand, if the final product is available after the promised delivery date, penalty costs are charged per time unit late. The problem is formulated as a nonlinear program. Yano shows that the cost function is not convex for all leadtimes but has some properties that can be used when solving the problem numerically. Yano concludes that the objective function is not well-behaved even for a simple assembly system with two components and argues that the general case with more than two components will be even less well-behaved. Yano acknowledges that we must resort to heuristics for assembly systems with an arbitrary number of components.

After the pioneering work by Yano (1987b), multiple researchers tackled the problem of planned leadtime optimization for assembly systems with stochastic leadtimes. Hopp and Spearman (1993) consider an assembly system in which multiple components are purchased and then assembled. They develop an approximate procedure to solve the problem of leadtime determination. Song et al. (2001) develop a recursive heuristic method for due date planning of all the components and the final assembly process. The method does not minimize the cost but allows the system to meet specified service targets. Axsäter (2005) studies a multiechelon assembly system with the objective of choosing the starting times of different processes to minimize the total expected holding and penalty costs. The author suggests an approximate decomposition technique that does not perform well for systems with more than two echelons. Chauhan et al. (2009) consider a single-period model for a multicomponent assembly process where the component procurement times are random variables with known distributions. The author develops an approximate procedure to determine the release dates of all the components. Another stream of research views the planned leadtimes as a tactical decision to capture the trade-off between resource requirements and Work in Process in the light of demand uncertainty (Chhaochhria and Graves 2013; Teo et al. 2011, 2012).

Our work contributes to the literature by providing a fast and accurate heuristic procedure that outperforms existing approaches and works for assembly systems with two and more echelons. The heuristic is based on an important conjecture that the optimal planned leadtimes solution satisfies so-called generalized News-vendor equations (Diks and de Kok 1998). As outlined in §6, our heuristic has the potential to be extended to distribution systems and even multiechelon systems with general structures.
1.2. ASML Case Study

ASML produces lithography systems, which are used in the production of integrated circuits and microchips. Lithography systems are assembled from modules. ASML’s manufacturing process necessitates timely deliveries of system components by external and internal suppliers to start the final assembly of the systems. The external suppliers provide the components and submodules and the internal suppliers are in charge of producing the modules.

In this study we focus on one module and two systems. The module is the most complex module in a lithography system. We refer to the analyzed systems as Systems A and B. System A was introduced at the end of 2010 and its successor, System B, was introduced at the beginning of 2012. Next, we explain ASML’s module production and final assembly processes and summarize its current practice for setting planned leadtimes.

1.2.1. Manufacturing Processes. Modules are produced by assembling the components and submodules in work centers. Each work center is responsible for the production of one module or submodule type. The module production itself is a complex process requiring multiple operations in series and for some work centers in parallel, too. An operation is defined to be the most basic step, requiring one to four hours for completion. The production of one module from each type requires the completion of 100–150 operations. Although the execution of these operations is based on standard procedures, many adjustments may be required during execution based on the specifications demanded by customers and the results of intermediate tests.

To manage the complex assembly process of the modules, operations are clustered into so-called milestones. The total processing time of a milestone is 2–20 days. The notion of milestones is mainly used for planning purposes. The leadtimes are planned not for the individual operations but only for the milestones.

The operations constituting a milestone are performed by the same workforce inside the same work center. Each workforce consists of 5–25 employees with expertise in performing the required operations. As the plan for the assembly of a system is made, the workforce is informed about the planned start time of its milestone. Each milestone starts either at its planned start time or later if the previous milestones are not completed on time. Early completion of a milestone does not permit for an early start of the consecutive milestone. This will require additional planning for getting the whole team of employees ready to perform their operations earlier than planned. ASML planners require the milestone workforces to stick to their designated starting times to avoid any complications that might result from rescheduling.

In this study we analyze two ASML systems: Systems A and B. Both systems are assembled from seven modules, six of which are manufactured in their own work centers and each of which has its own planned leadtime. The most complex module requires the longest production time. At ASML, the operations of this complex module are grouped into two distinct milestones and a planned leadtime is calculated for each milestone. The first milestone is used for assembly and test of two similar submodules in parallel. The second milestone, performed by a different workforce in a different work center, concerns the assembly and test of the most complex module (Figure 1). After all seven modules are produced, the final assembly of Systems A and B can start. The final assembly process takes place in rooms called cabins, which are specifically designed for the final assembly process. Together with the work centers, the cabins are located in the cleanrooms. The final assembly and the subsequent tests constitute three milestones. As explained above, different workforces are responsible for the completion of each milestone. Assembled and tested systems are packed and shipped to customers. The average number of operations required for the completion of the final assembly and test processes is approximately 800. We refer to Figure 2 for a schematic overview of Systems A and B’s production processes. Among the module production, final assembly, test, and packing processes, only the first three account for the uncertainty in the production leadtimes. The packing times do not depend on the system type; they are fairly standard and stable. Therefore, we exclude the packing process from our analysis.

1.2.2. Calculation of Planned Leadtimes. We now explain ASML’s current method for setting the milestone planned leadtimes.

Each milestone consists of multiple operations. The average time required for the completion of an operation is called the operation leadtime. In addition to its operation leadtime, each operation requires an additional time because of the uncertainties related to production. This additional time is called the buffer time.

At ASML, each milestone has an internal and an external cycle time. Consider a milestone with \( N \) operations. Let \( t_0 \) and \( b_0 \) be the operation leadtime and
buffer time for operation \( n \in \{1, 2, \ldots, N\} \), respectively. The milestone’s internal cycle time is calculated as \( \sum_{n=1}^{N} (t_n + b_n) \). Hence, the internal cycle time includes the operations’ leadtimes and the buffer times within the operations. According to ASML’s strategy, every milestone’s internal cycle time should ensure 80% delivery reliability. The difference between the internal and external cycle times is the additional time needed to cope with possible disturbances. This additional time is referred to as safety time. Let \( S \) represent the safety time of a milestone with \( N \) operations. Then the external cycle time is calculated as \( S + \sum_{n=1}^{N} (t_n + b_n) \). (Refer to Figure 3.) ASML’s strategy suggests that after adding the safety time, each milestone should reach 99% delivery reliability. Note that the buffer and safety time are not scientific but ASML notions. In the literature safety time equals \( S + \sum_{n=1}^{N} b_n \).

The current method of calculating the buffer and safety times of a module at ASML relies on the data of the last \( M \) production instances. After removing outliers, the production times are plotted on a histogram and a Normal distribution is fitted. The mean of the distribution is assumed to represent the total operation leadtime of all the operations, i.e., \( \sum_{n=1}^{N} t_n \). The time between the 50th percentile and the 80th percentile is set as the total buffer time for all the operations, i.e., \( \sum_{n=1}^{N} b_n \), and the time between the 80th percentile and the 99th percentile is set as the safety time, i.e., \( S \) (Figure 4).

The planned leadtime of a milestone is set to the milestone’s external cycle time. The planned leadtimes are communicated to the responsible work centers. More detailed plans regarding the allocation of the total buffer time to operations are developed manually by experienced planners at the work centers, and allocation does not rely on a structured methodology.

Clearly, the current method for determining the planned leadtimes poses some problems. Most importantly, the current method does not take the interrelated nature of module production processes into account. As suppliers for the final assembly, the module production processes need synchronized on-time deliveries. Setting the right module leadtimes prevents delayed start of the final assembly, while setting the right final assembly and test leadtimes is crucial for meeting promised due dates. Delayed deliveries result in high penalty costs and loss of future demands. This is why ASML is interested in a more integrated approach for determining the milestone leadtimes.

2. Formulation of the Model

We consider an assembly system consisting of \( M \) multistage processes delivering subassemblies to a multistage final assembly process whose index is 0. Each process \( m, m \in \{0, 1, \ldots, M\} \), consists of \( N_m \) stages. We refer to process \( m \)'s stage \( j \) as stage \((m, j)\). We number the stages in all the processes in decreasing order. This means that stage \((m, j + 1)\) is the predecessor of stage \((m, j)\). An example is given in Figure 5.

We define \( \tau_{mj} \) as the throughput time of stage \((m, j)\).2 Throughput times at each stage are ran-

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1 In terms of ASML terminology, stage and subassembly correspond to milestone and module, respectively.

2 If not stated otherwise, all the definitions are valid for all \( j \in [1, 2, \ldots, N_m] \) and \( m \in [0, 1, \ldots, M] \).
dom variables with known continuous distributions. Throughput times at different stages are independent.

The system incurs a marginal holding cost $h_{mji}$ from the moment the production at stage $(m, j)$ starts until the final product is delivered to the customer. Typically, the holding cost, in the setting discussed here, consists of labor and material costs. We define $H_{mji}$ as the holding cost per unit time at stage $(m, j)$. For all $m \in \{1, 2, \ldots, M\}$ and $j \in \{1, 2, \ldots, N_{m}\}$, we have $H_{mji} = \sum_{m' = 1}^{N_m} h_{m'm} + \sum_{j' = 1}^{N_{m'}} h_{0j}$. We assume that $H_{mji} < H_{m'm}$, for $m = \{0, \ldots, M\}$ and $j \in \{1, 2, \ldots, N_{m}\}$, and for $m' \in \{0, \ldots, M\}$, is $-\sum_{m' = 1}^{M} T_{mi}$. The earliness and tardiness of stage $(m, j)$ are calculated as $E_{mji} = \left(-\sum_{m' = 1}^{M} T_{mi} - F_{mji}\right)^+ \text{ and } L_{mji} = \left(F_{mji} + \sum_{m' = 1}^{M} T_{mi}\right)^+$. The planned finish time at stage $(0, j)$, $\forall j \in \{1, 2, \ldots, N_{m}\}$, is $\sum_{m' = 1}^{M} T_{mi}$. The earliness and tardiness of this stage are calculated as $E_{0j} = \left(\sum_{m' = 1}^{M} T_{0} - F_{0j}\right)^+ \text{ and } L_{0j} = \left(F_{0j} - \sum_{m' = 1}^{M} T_{0j}\right)^+$.

For the final assembly process to start, the production of all subassemblies must be completed. If all the subassemblies are on time or early, the final assembly process starts at time 0. Otherwise, the start time is delayed by an amount equal to the maximum tardiness of the subassembly processes. Therefore, we have $W_0 = \max_{1 \leq m \leq M} L_{mN_m}$. The waiting time of subassembly $m$ because of the tardiness of other subassemblies is $W_{m} = \left(\max_{1 \leq m' \leq M, m' \neq m} L_{m'} - L_{m}\right)^+$. We now introduce alternative expressions for the earliness and tardiness at each stage to provide recursive expressions that relate the values of these performance measures for consecutive stages.

The instantaneous delivery of raw materials to subassembly processes implies that these processes can always start at the planned time, which is $-\sum_{m' = 1}^{M} T_{m'i}$ for subassembly $m$. Therefore, the earliness and tardiness of the first stage, i.e., $(m, N_m)$, depend only on the planned leadtime and the throughput time of that stage. We have $E_{mN_m} = \left(T_{mN} - \tau_{mN_m}\right)^+ \text{ and } L_{mN_m} = \left(\tau_{mN_m} - T_{mN_m}\right)^+.

On the other hand, the earliness and tardiness of the subsequent production stage, i.e., $(m, N_m - 1)$,
also depend on the tardiness of stage \((m, N_m)\). Since production is held back if the previous stage finishes earlier than planned, the earliness of stage \((m, N_m)\) does not affect the earliness or the tardiness of \((m, N_m - 1)\). Therefore, \(L_{mN_m} - E_mN_m - 1\) and \(L_{mN_m} - 1\) are related as \(L_{mN_m} - 1 = (E_mN_m - L_{mN_m} + \tau_{mN_m} - \tau_{mN_m - 1})^+\) and \(L_{mN_m} - 1 = (E_mN_m + \tau_{mN_m} - \tau_{mN_m - 1})^+\).

Based on the same intuition, we can write recursive expressions to relate the earliness and tardiness of the stages in the subassembly processes. Given that \(N_{mN_m} + 1 = 0, \forall j \in \{1, 2, \ldots, N_m\}\) and \(\forall m \in \{1, 2, \ldots, M\}\), we have \(E_m = (E_m - \tau_m - L_{m+1})^+\) and \(L_m = (L_m + \tau_m - T_m)^+\).

As discussed above, the start of the final assembly process is delayed for \(W_0\) time units. Therefore, for the first stage of the final assembly process, i.e., stage \((0, N_0)\), we have \(E_{0N_0} = (T_{0N_0} - \tau_{0N_0} - W_0)^+\) and \(L_{0N_0} = (W_0 + \tau_{0N_0} - T_{0N_0})^+\). For \((0, j)\), \(\forall j \in \{1, 2, \ldots, N_0 - 1\}\), the recursive expressions for the earliness and tardiness are \(E_j = (T_j - \tau_j - L_{j+1})^+\) and \(L_j = (L_j + \tau_j - T_j)^+\).

Given that the throughput times at all stages are random variables, \(E_m\) and \(L_m\), \(\forall j \in \{1, 2, \ldots, N_0\}\) and \(\forall m \in \{0, 1, \ldots, M\}\), are random variables as well. Let \(\mathbb{E} [\cdot]\) denote the expectation of a random variable.

### 2.2. Cost Expression

Next, we write the total expected cost of the system. Let \(T\) be the vector of all planned leadtimes, i.e., \(T = \{T_{mn}\}_{m=1, N_m}^M\), and \(C(T)\) the total expected cost of the system as a function of \(T\). From the moment production at stage \((m, j)\) starts until the final product is delivered to the customer, the system incurs a marginal holding cost \(h_{mj}\). The product is delivered to the customer at time \(\sum_{i=1}^{N_m} T_{0i} + L_{01}\) and production starts at stage \((m, j)\) at time \(F_{mj} + E_{mj}\). In addition, a penalty cost \(p\) is charged per unit tardy to the customer. The sum of the expected holding and penalty costs is

\[
C(T) = \mathbb{E} \left[ \sum_{m=0}^{M} \sum_{n=1}^{N_m} h_{mj} \left( \sum_{i=1}^{N_m} T_{0i} + L_{01} - (F_{mj} + E_{mj}) \right) + pL_{01} \right].
\]

We can write the production start time at stage \((m, j)\) as the sum of the start time of process \(m\) and the time elapsed until the start of production at stage \((m, j)\) starts. For subassembly process \(m\), the start time is \(-\sum_{i=1}^{N_m} T_{mi}\) and the final assembly starts at time \(W_0\). The elapsed time from the start of these processes until the production at stage \((m, j)\) starts is \(\sum_{i=j+1}^{N_m} (\tau_{mi} + E_{mi})\). Therefore, we can express the production start times as follows:

\[
F_{mj} + E_{mj} = -\sum_{i=1}^{N_m} T_{mi} + \sum_{i=j+1}^{N_m} (\tau_{mi} + E_{mi}),
\]

\(m \in \{1, 2, \ldots, M\}\), \(j \in \{1, 2, \ldots, N_m\}\)

\(F_{0j+1} + E_{0j+1} = W_0 + \sum_{i=1}^{N_0} (\tau_{0i} + E_{0i}),\)

\(j \in \{1, 2, \ldots, N_0\}\).

Using these expressions and the equality \(H_{01} = \sum_{m=1}^{M} \sum_{i=1}^{N_m} h_{mj}\), we can rewrite \(C(T)\) as follows:

\[
C(T) = \mathbb{E} \left[ \sum_{m=1}^{M} \sum_{n=1}^{N_m} h_{mj} \left( \sum_{i=1}^{N_m} T_{0i} + \sum_{i=1}^{N_m} T_{mi} - \sum_{i=j+1}^{N_m} (\tau_{mi} + E_{mi}) \right) + \sum_{i=1}^{N_m} h_{0j} \left( \sum_{i=1}^{N_m} T_{0i} - W_0 - \sum_{i=j+1}^{N_m} (\tau_{0i} + E_{0i}) \right) + (H_{01} + p)L_{01} \right].
\]

Our objective is to find the vector \(T\) that minimizes the total expected cost \(C(T)\). As acknowledged by other authors who have studied similar problems, this problem cannot be solved to optimality since the cost function is not well-behaved. In the next section, we propose a solution approach that generates quite accurate results.

### 3. Solution Approach

The main idea behind our approach is to decompose the assembly system into \(M\) serial systems, one for each subassembly. For that purpose, we write the cost expression from serial system \(m\)’s perspective. We use the following equality, which states the time elapsed from the start of subassembly process \(m\) until the start of the final assembly process in two alternative ways:

\[
\sum_{i=1}^{N_m} T_{mi} + W_0 = \sum_{i=1}^{N_m} (\tau_{mi} + E_{mi}) + W_{m-1}
\]

This equality implies that the maximum tardiness, i.e., \(W_0\), equals \(\sum_{i=1}^{N_m} (\tau_{mi} + E_{mi}) + W_{m-1} - \sum_{i=1}^{N_m} T_{mi}\). Substituting \(W_0\) in (2), we rewrite \(C(T)\) from serial system \(m\)’s perspective as follows:

\[
C(T) = \mathbb{E} \left[ \sum_{m=1}^{M} \sum_{n=1}^{N_m} h_{mj} \left( \sum_{i=1}^{N_m} T_{0i} + \sum_{i=1}^{N_m} T_{mi} - \sum_{i=j+1}^{N_m} (\tau_{mi} + E_{mi}) \right) + \sum_{i=1}^{N_m} h_{0j} \left( \sum_{i=1}^{N_m} T_{0i} - W_0 - \sum_{i=j+1}^{N_m} (\tau_{0i} + E_{0i}) \right) - \sum_{i=j+1}^{N_m} (\tau_{0i} + E_{0i}) + (H_{01} + p)L_{01} \right].
\]

### Serial system

The variable \(m\) is \(m \in \{1, 2, \ldots, M\}\), and \(N_m\) stages. We renumber the stages so that stage \((m, j)\)
becomes \((m, j + N_0), \forall j \in \{1, 2, \ldots, N_m\}\) and stage \((0, j)\) becomes \((m, j), \forall j \in \{1, 2, \ldots, N_0\}\). Therefore, serial system \(m\) starts with stage \((m, N_m + N_0)\) and ends with stage \((m, 1)\). The top and bottom pictures in Figure 6 represent the old and new numbering, respectively.

We retain the recursive expressions for the earliness and tardiness with a slight modification at stage \((m, N_0)\), which was originally the first stage of the final assembly process, i.e., stage \((0, N_0)\). The production start time at this stage is related to the tardiness of the other subassemblies. We link serial system \(m\) with the other serial systems by considering the tardiness caused by the other subassemblies. This link is reflected by the random variable \(W_{-m}\). Given that \(L_{m(N_m+N_0+1)}=0\), the earliness and tardiness expressions for the stages in serial system \(m\) are summarized in Table 1.

Using the new numbering for the stages in serial system \(m\), we can combine the second and third lines of the cost function (3) into one. The resulting cost expression is as follows:

\[
C(T) = \mathbb{E}\left[ \sum_{m=1}^{M} \sum_{i=1}^{N_m} h_{m_i} \left( \sum_{i=1}^{N_0} T_{0i} + \sum_{i=1}^{N_m} T_{mi} - \sum_{i=j+1}^{N_0} (\tau_{mi} + E_{mi}) \right) \right] + \sum_{j=1}^{N_m+N_0} h_{m_j} \left( \sum_{i=1}^{N_0} T_{m_j} - \sum_{i=j+1}^{N_0} (\tau_{m_j} + E_{m_j}) \right) - W_{-m} \left( \mathbb{1}_{j \leq N_0} \right) + (H_{m_1} + \hat{p}) L_{m_1}. \tag{4}
\]

Here, \(H_{m_1} = \sum_{j=1}^{N_m+N_0} h_{m_j}\) and \(\hat{p} = p + \sum_{i=1}^{M} \sum_{i=m}^{N_m} \sum_{j=1}^{N_0} h_{ij}\).

In addition, \(\mathbb{1}_{j \leq N_0}\) is the indicator function, which equals 1 if \(j \leq N_0\) and 0 otherwise.

Note that the recursion in Table 1 resembles the well-known Clark-Scarf backorder recursion. In addition, the cost expression for serial system \(m\) has a similar form as the cost for serial inventory systems (refer to Zipkin 2000). Here, backorders correspond to the tardiness and the demand random variable is replaced by the throughput time. The only difference is that stage \((m, j), \forall j \in \{1, 2, \ldots, N_0\}\), has an extra random variable \(W_{-m}\). In fact, the coupling between the serial systems is due to the random variable \(W_{-m}\) and our decomposition idea is based on the observation that from serial system \(m\)'s point of view, \(W_{-m}\) is exogenous. The exact correspondence between the serial planned leadtime problem and the serial multistage inventory optimization problem is shown by Gong et al. (1994).

For divergent multiechelon inventory systems, Diks and de Kok (1998) show that we can find cost-optimal base-stock policies by recursively solving so-called generalized Newsvendor equations. Obviously, this implies the same for the serial systems in Clark and Scarf (1960) and the assembly systems in Rosling (1989). It is not obvious whether such a result holds for the planned leadtime problem discussed in this paper, which is more complicated because of the mutual impact of the multistage assembly processes on each other through \(W_{-m}\). Unlike the situation discussed by Rosling (1989) with constant leadtimes, we cannot coordinate the system such that an equivalent serial system emerges. Fundamentally, the exogenous stochastic throughput times do not allow for that. Despite this fact, our exploratory research suggests that we can formulate generalized Newsvendor equations from which the planned leadtimes can be determined. For a two-echelon assembly system consisting of multiple parallel single-stage processes feeding a single-stage process, we formulate this statement as a theorem and prove it in the appendix. In addition, we prove that the leadtimes determined through Newsvendor equations are unique. Additional numerical experimentation for a large set of multistage systems, part of which we present in the subsequent sections, supports our conjecture.

For the sake of simplicity we identify each stage \((m, j)\) with a unique number \(s\). We associate 0 with stage \((0, 1)\). Thus, we consider a convergent assembly system with, say, \(S\) stages. With each stage \(s\) we

<table>
<thead>
<tr>
<th>Stage(s)</th>
<th>Earliness</th>
<th>Tardiness</th>
</tr>
</thead>
<tbody>
<tr>
<td>(j \in {1, 2, \ldots, N_0 - 1, N_0 + 1, \ldots, N_m + N_0}) ((m, N_k))</td>
<td>((T_{0j} - \tau_{0j} - L_{m+1})^+) ((L_{m+1} + \tau_{m+1} - T_{0j})^+)</td>
<td>((T_{mk} - \tau_{mk} - (L_{m+1} + W_{-m}))^+) ((L_{m+1} + W_{-m} + \tau_{m+1} - T_{mk})^+)</td>
</tr>
</tbody>
</table>
can associate a random variable $W_{-s}$ which is the delay after stage $s$ caused by other predecessors of stage $s$’s immediate successor. Note that, in our system, we have $W_{-s} = 0$, except for the last stages of the subassembly processes, i.e., stage $(m, 1)$, $\forall m \in \{1, 2, \ldots, M\}$. In general convergent assembly systems, each stage $s$ whose immediate successor has more than one predecessor has a nonzero $W_{-s}$. To formulate our conjecture, we introduce a set of events $A_s$. An element $w$ of the probability space is an element of $A_s$ if and only if under $w$, (i) stage $s$ starts in time, (ii) if the immediate successor of stage $s$ starts in time then the final stage finishes in time, and (iii) the tardiness of stage $s$ exceeds $W_{-s}$, and the final stage, 0, is late. Informally, $A_s$ represents the set of all events in which stage $s$ is to be blamed for the tardiness of the system. It is easy to see that the sets $A_s$, $\forall s \in \{1, 2, \ldots, S\}$, are mutually exclusive and the union of these events consists of all $w$ in the probability space for which the final stage is late. The generalized Newsvendor equation conjecture is as follows:

**Conjecture 1.** The optimal planned leadtimes policy satisfies

$$ P(A_s) = \frac{h_s}{p + H_0}, \quad \forall s \in \{1, 2, \ldots, S\}. \quad (5) $$

This conjecture is the most important driver for the accuracy and speed of the heuristic. An immediate consequence of Conjecture 1 is that the probability of on time delivery equals the classical Newsvendor fractile $p/(p + H_0)$ ($p/(p + H_0)$ with the original notation). The definition of $A_s$ involves mutually dependent events, implying, at first sight, great computational complexity. Yet the introduction of the random variables $W_{-s}$ enables the derivation of tractable expressions. We compute these expressions using so-called two-moment approximations. The key observation made earlier is that assuming the $W_{-s}$ are known, the system to be considered for computation of $P(A_s)$ is a serial system, consisting of the stages on the path starting from stage $s$ until stage 0. From this we can also see that given $W_{-s}$ the optimal planned leadtimes can be computed recursively starting with the computation of $T_0$ (originally $T_{00}$) from the equation $P(A_0) = h_0/(p + H_0)$. To compute $P(A_s)$ we associate with each stage $s$ a subsystem $B_s$ of the original serial system, i.e., the system starting with stage $s$. Denote the immediate successor of stage $s$ as $s-1$. Remember that $L_s$ is the tardiness of stage $s$. Then we have the following equations:

$$ P(A_s) = P(\text{final stage of system } B_{s-1} \text{ is in time and } L_s \geq W_{-s}, \text{ and final stage of system } B_s \text{ is late}), $$

$$ = P(\text{final stage of system } B_{s-1} \text{ is in time and } L_s \geq W_{-s}) - P(\text{final stage of system } B_{s-1} \text{ is in time and } L_s \geq W_{-s} \text{ and final stage of system } B_s \text{ is in time}), $$

$$ = P(\text{final stage of system } B_{s-1} \text{ is in time}) \cdot [P(L_s \geq W_{-s}) - P(L_s \geq W_{-s} \text{ and final stage of system } B_s \text{ is in time})], $$

$$ = P(\text{final stage of system } B_{s-1} \text{ is in time}) \cdot [P(L_s \geq W_{-s}) - P(\text{final stage of system } B_s \text{ with conditional tardiness } L_{s+1} \text{ given } L_s \geq W_{-s} \text{ is in time})]. $$

In the above derivation we used the independence of (i) the events in system $B_{s-1}$ and (ii) $L_s$ and $W_{-s}$. Thus, the probabilities $P(A_s)$ can be computed from the expressions for serial subsystems with exogenous delays $W_{-s}$. However, we do not know the random variables $W_{-s}$. To deal with this we propose an iterative approach, which starts with $W_{-s} \equiv 0$, $\forall s \in \{1, 2, \ldots, S\}$. As the exogenous delays are 0, the optimal planned leadtimes are minimal. With these minimal planned leadtimes we compute the associated $W_{-s}$, which should be maximal. With the maximal $W_{-s}$ we compute maximal optimal planned leadtimes. By repeated application of this scheme we get an ordered series of $W_{-s}$ that approaches some limit from above and below. Although our reasoning is not much more than intuitive, it is supported by numerical evidence.

The above reasoning enables us to develop an iterative heuristic procedure. In this heuristic, we rely on two-moment fits. Therefore, we do not even need the exact distribution of $W_{-s}$ but only the first two moments. The details are explained in the next section.

### 3.1. Iterative Heuristic Procedure

In our iterative solution approach, we initially set the values of $W_{mr}$, $\forall m \in \{1, 2, \ldots, M\}$ to 0. We then rely on Conjecture 1 to determine the values of the decision variables, i.e., $T_{mr}$, for all the stages in all the serial systems. We apply the proven technology of two-moment mixed Erlang fits to approximate the distributions of $\tau_j + L_{j+1}$, $\forall j \in \{1, 2, \ldots, N_m + N_0\}$, $-N_0$, and $\tau_{N_0} - L_{N_0+1} - W_{-mr}$, i.e., the sum of tardiness and throughput times for all the stages (Diks and de Kok 1999). Next, we consider the original system, and by setting the planned leadtimes to the values obtained from the decomposed system, we determine the first two moments of $W_{mr}$, $\forall m \in \{1, 2, \ldots, M\}$. For this purpose we use two-moment approximations based on the recursive determination of $\max_{1 \leq m \leq M, n \neq m} L_{n1}$ from the maximum of two random variables (Whitt 1982). Using these moments and relying on the two-moment mixed Erlang fits to approximate the distributions of the sum of $W_{mr}$ tardiness and throughput.
times, we go back to the serial systems and recompute the corresponding values of the decision variables. Therefore, at each iteration, given the first two moments of $W_{m}$, we solve the serial systems for the planned leadtimes. Using this solution, we recompute the first two moments of $W_{m}$. Let $E[W_{m}^k]$ be the expected values of $W_{m}$ after iteration $k$. The algorithm stops when \( \sum_{m=1}^{M} |E[W_{m}^k] - E[W_{m}^{k-1}]| \leq 0.01 \). Refer to Figure 7 for a flow chart of the iterative heuristic procedure. Our numerical experiments show that by setting the initial value of $W_{m}$ to 0, the stopping criterion is satisfied, on average, in 5.32 iterations. As mentioned above, if $E[W_{m}^0] = 0$, $\forall m \in \{1, 2, \ldots, M\}$, the next iteration results in the maximum possible values of $E[W_{m}]$. In every iteration, the expectations get closer and, eventually, the stopping criterion is satisfied.

4. Numerical Experiments

To develop some intuition about the solution structure and performance of the iterative heuristic and to compare it with ASML’s fractile method, we solve a set of test problems. We resort to a numerical optimization technique as a benchmark. We use the DFP method as described in Press et al. (2007). This method is one of the earliest and most effective quasi-Newton methods (Hashamdar and Ibrahim 2010). It simultaneously generates conjugate directions and constructs approximations of the inverse Hessian matrix, providing monotonically improving approximations to the exact solution. To test the validity of our results, we also built a simulation model that uses the planned leadtimes, the distributions of throughput times, and the cost parameters as inputs. The outputs are the service levels and the average costs.

Using ASML’s SAP (systems applications and products) system, we gathered actual leadtime data for the milestones of Systems A and B. Our cycle time data for Systems A and B consist of 128 and 11 samples, respectively. In addition, we were provided with the data on the value added by each milestone (echelon holding costs). In our numerical analysis we initially fix a fractile and use ASML’s method to calculate the planned leadtimes. We then simulate the system with these planned leadtimes to determine the corresponding on-time delivery performance. Next, we exploit the relationship between the Newsvendor fractile and the calculated on-time delivery probability to estimate the penalty cost associated with late deliveries. Finally, we use our iterative heuristic procedure and the DFP method to calculate the planned leadtimes. We simulate both results to test their validity.

One might argue that the amount of data for System B is not sufficient for validation. However, our model validation does not concern standard statistical modeling, where the model is estimated from the data. We use the actual leadtime data to determine the sample average and standard deviation. Then our model computes the service level and compares this to the observed service level for the same set of systems. Given the mathematical complexity of the model, it would be very unlikely to find accurate model estimates for both systems unless the model is a good description of reality. In fact, our service level estimates for both systems are within 2% of the actual values.

For each fractile value we report the percentage cycle time reduction, $CT_{red}$, and the percentage cost reduction, $TC_{red}$. ASML would achieve if our heuristic were used instead of the fractile method. Table 2 summarizes our results. For both systems the 85-percentile corresponds to the actual values for which we obtained good validation results. The heuristic results in significant reductions in the cycle times and total costs, especially if ASML targets high on-time probability.

\(^3\) Because of confidentiality reasons, we cannot report the data.
The average percentage cycle time and cost differences between the DFP method and the heuristic are 1.45% and 1.33%, respectively. The heuristic terminates, on average, in 5.1 iterations. We validate our analytical cost and on-time delivery probability figures by simulating the systems with the planned leadtimes suggested by all the methods (ASML’s method, heuristic, DFP). The average and standard deviation of the percentage difference between the costs (service levels) calculated analytically and by simulation are 0.31% and 0.15% (0.83% and 0.65%), respectively.

5. Detailed ASML Analysis
The iterative heuristic was developed with the intention of solving ASML’s planned leadtime problem. Given the complexity of the analysis of the assemble-to-order planned leadtime model, it is impractical to explain the mathematical principles to the people responsible for setting the planned leadtimes. This is why, to gain credibility at ASML, we empirically validated the model for the most complex module and Systems A and B and, to our knowledge, we report the first results on the empirical validity of the planned leadtime model. In this section, we explain our findings on ASML’s current practice of planning the leadtimes and provide the details of our validation. In addition, we explain the benefits of implementing the suggested solutions.

5.1. Analysis of the Most Complex Module
Using ASML’s SAP system, we gathered 112 actual leadtime data for two milestones of the most complex module. In addition, we gathered data for the planned leadtimes of these milestones. The planned leadtimes in the SAP system differ considerably from those suggested by the fractile procedure. This implies that employees on the shop floor do not follow the recommendations based on the fractile method. Therefore, the fractile method does not provide a relevant benchmark for this module.

To compare ASML’s current performance with the solution suggested by the iterative heuristic procedure, we construct an efficient frontier based on the relationship between the cycle time, i.e., the total planned leadtime, and the service level (Figure 8).

To our surprise, we found that the planned leadtime data in the SAP system, as decided by people in the most complex module’s work center, yields a solution on the efficient frontier generated using our iterative heuristic, indicating that our solutions are in line with the understanding of the people in the work center. In Table 3, we summarize the planned cycle times (CT), on-time delivery percentages, holding costs, and total costs for the solutions from ASML’s SAP system, the DFP method, and the Newsvendor (NV) method, i.e., the iterative heuristic. We present results both from discrete event simulation (Sim) and our analytical procedures (Anly).

The planned leadtimes solution from SAP is considered as the benchmark solution. We normalize the holding costs of the benchmark solution at 100%. The DFP and NV solutions are remarkably close to the solution found by the ASML operators. A detailed analysis on the planned leadtimes of the milestones suggest that for the first milestone our method’s planned leadtime (3.4 days) is 10.5% shorter than the current ASML plan (3.8 days). On the other hand, for the second milestone our method’s planned leadtime (10.5 days) is 1.9% longer than the current ASML plan (10.3 days).

5.2. Optimization of the System Planned Leadtimes
The SAP data for Systems A and B indicate that the fractile chosen for determining safety leadtimes is 85%, resulting in actual service levels of only 0.82
and 0.81, with associated overall planned leadtimes of 86 days and 165 days, respectively. For both systems, we use our heuristic results to draw the efficient frontiers in Figure 9.

Figure 9 suggests that unlike in the most complex module, the current cycle times for Systems A or B are not on the efficient frontiers. Therefore, using our Newsvendor method to calculate the planned leadtimes provides an attractive alternative to the fractile method.

Table 4 presents the results of the alternative solution procedure using the same notation as in Table 3. As the ASML solution yields a simulated on-time delivery percentage of 82.3%, we set the penalty costs accordingly using the Newsvendor fractile. In this case, we normalize the DFP solution’s holding costs for earliness at 100%, as we find that the DFP solution yields the lowest overall cost. For all performance data presented we find that our analytical expressions perform quite well. The Newsvendor solution is quite close to the DFP solution in performance and in overall planned throughout time, yielding a cycle time reduction of 11.8% over the current planned leadtimes. Typically, cycle time reduction is targeted for earliness at 100%, as we find that the resulting solution differs very little in performance from the Newsvendor solution, which shows robustness of the optimal solution. This also follows from the fact that the DFP and Newsvendor solutions have a relatively large buffer at the last milestone, representing about 24% of the overall cycle time. The Critical Chain (CC) solution for the final assembly and testing phases has a safety time buffer at the last stage representing 52% of the overall cycle time.

Taking the Critical Chain concept to the extreme, we also consider the solution with planned leadtimes equal to zero for all milestones, but the last one. We find a serious deterioration of the performance, as we lose 5% of our 7% reduction in capital tied up in work in progress. This shows the importance of coordinating the module assembly processes and a proper trade-off between earliness and tardiness costs.

Based on these results, ASML planners are convinced that our Newsvendor method can reduce the overall System A cycle time. The next question they have is related to the detailed plan: Are there any

<table>
<thead>
<tr>
<th>Case</th>
<th>CT (days)</th>
<th>CT_ear (%</th>
<th>On-time delivery (%)</th>
<th>Holding cost (%)</th>
<th>Total cost (%)</th>
<th>Work in progress (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Sim</td>
<td>Anly</td>
<td>Sim</td>
<td>Anly</td>
</tr>
<tr>
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<td>82.6</td>
<td>130.9</td>
<td>130.4</td>
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<td>DFP</td>
<td>75.7</td>
<td>12</td>
<td>82.7</td>
<td>82.4</td>
<td>100.0</td>
<td>99.8</td>
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<tr>
<td>NV</td>
<td>76.5</td>
<td>11</td>
<td>82.7</td>
<td>82.3</td>
<td>100.7</td>
<td>100.5</td>
</tr>
<tr>
<td>Critical chain from final</td>
<td>76.2</td>
<td>11</td>
<td>82.8</td>
<td>81.8</td>
<td>100.7</td>
<td>100.0</td>
</tr>
<tr>
<td>Critical chain for system</td>
<td>68.8</td>
<td>20</td>
<td>82.7</td>
<td>82.1</td>
<td>120.6</td>
<td>120.1</td>
</tr>
</tbody>
</table>
significant differences in the detailed plans when the current method and the proposed method are compared? To answer this question, we show the structure of the planned leadtimes obtained for System A using ASML’s method and the Newsvendor method (Figure 10). ASML’s method results in positive safety times at all milestones. The total cycle time is dictated by the most complex module (5th module) and the final assembly and test milestones. On the other, the detailed plan generated by our method is quite different. Our method suggests that at the start of the final assembly process, the 1st module has a planned leadtime of zero, thereby becoming the drum of the assembly process. When we compare the throughput times and the added values of all the module milestones, we see that the value added per unit throughput time is the highest for the first module. Therefore, our method, which also takes the earliness cost into account, suggests a solution with a planned leadtime of zero for the first module. The other modules are planned such that they have a limited impact on the first module-final assembly interaction. Furthermore, as Figure 10 suggests, our method results in a substantial mean earliness at the last milestone of the final assembly process. This is consistent with both the claims of Goldratt (1997) and the finding for multiechelon systems that the majority of the slack should be at the most downstream stage (de Kok and Fransoo 2003).

In Table 5 we present our results for System B, which was introduced in 2012 and represents the next generation of ASML’s lithography systems. This system has substantially longer and more erratic assembly and testing times and thus a substantially longer overall cycle time. We find that the DFP solution does not meet the target on-time delivery of 81.1%, set by the on-time delivery performance of the currently used 85%-fractile solution. We modify the planned leadtime of the last milestone to meet the required performance. This modified solution is used as the benchmark: the earliness costs are normalized at 100%. The cycle time reduction from the modified DFP solution is about 11%. This yields a 9.6% reduction in working capital. The Newsvendor solution yields a capital reduction of 8.4%, while meeting the required on-time delivery without need for modification. For this system Critical Chain performs better than for System A, but still much worse than the Newsvendor solution. Again we observe robustness of the Critical Chain solution for the final assembly and testing milestones. The structure of the planned leadtimes for System B is similar to the one for System A in Figure 10.

Our results show that we can reduce the overall cycle times by 11%. Apart from the impact of cycle time reduction on working capital, in the ASML situation there is another important impact: capacity increase. As explained in §1.2 each system is assembled and tested in its own cleanroom and occupies this cleanroom until delivery to the customer. The number of available cleanrooms limits the WIP measured in number of systems. Little’s formula tells us that the capacity of ASML in terms of output per week is less than the number of cleanrooms divided by the cycle time in weeks. A cycle time reduction of 11% yields an increase in this capacity limit by 12%. As ASML’s demand for lithography systems is volatile, it has gone through periods of high demand, where the number of cleanrooms was the binding constraint. This implies that in times of high demand, the 11% cycle time reduction resulting from our optimization yields an effective output increase of 12%. To give an indication of the financial impact of such an increase, consider that the company sold €5.86 billion worth of systems in 2014. Thus the cycle time reduction from our planned leadtime optimization would yield an additional revenue of €0.70 billion, assuming fully occupied cleanrooms throughout the year. Clearly, this is the best case scenario, but the order of magnitude underlines the contribution of our research in this case.

5.3. Discussion of Implementation

The study outlined in this paper is motivated by the problem of setting the planned leadtimes at ASML. As
discussed in §1.2.2, ASML currently relies on a fractile method for setting the milestone leadtimes. This method has been used for many years. There have been multiple initiatives to question its validity but none have resulted in an alternative method. According to ASML’s business engineers the current method is problematic because it does not take the interrelated nature of module production processes into account. As suppliers for the final assembly, the module production processes need synchronized on-time deliveries. Our collaboration with ASML on this problem started in 2012. The research outlined in this paper was conducted together with ASML’s business engineers, planners, and a group of operators. After gathering information, we developed the model in §2, which reflects ASML’s current manufacturing environment. The results, as explained in §§4 and 5, were presented to the business engineers and senior leadership.

First of all, we discussed our finding regarding the most complex module. Our analysis revealed that, for this module, ASML’s fractile plan is not followed. The planned leadtimes are determined by experienced shop floor workers. ASML’s managers do not interfere with the current way this work center is operating since they find the realized leadtimes satisfactory. It is impossible for the workers in the complex module’s work center to come up with a model which explains their intuition developed over many years. Our analysis shows that their intuition results in a solution, which is on the efficient frontier generated using our model. This was a major contribution to the acceptance of our model and software tool developed. Now ASML has a model, which is in line with the understanding of very experienced people in the most complex module’s work center.

Next, we communicated the main results of our analysis for Systems A and B in the form of the efficient frontiers versus the solutions from the ASML fractile approach. We shared our finding that the overall cycle times of Systems A and B can be reduced. The detailed plans as depicted in Figure 10 are explained to the planners. The detailed milestone modeling showed economic considerations behind the clustering of milestones: some milestones should not have safety buffers, implying that these milestones should be clustered with their successor. The message that overall cycle time could be reduced substantially (11%) while maintaining the same on-time delivery performance or even higher, and support for decisions on milestone clustering led to the decision to use our planned leadtime optimization tool. Another driver of this decision is ASML’s interest on the reduction of WIP capital.

ASML has had an active cycle time reduction program for years. Satisfied with our promising results, a separate workstream has been added to this program with an overall cycle time reduction target of more than 11%. Toward this objective, our model and the software tool has been used for detailed analysis of the leadtimes in the factories since the second half of 2014. Over the last year, in a pilot run for a limited number of systems, the planned leadtimes have been set as suggested by our model. The described dynamics of our model, together with other initiatives and interpretations, have already resulted in cycle time reductions achieving the target, although it is difficult to specify the reduction percentage that is solely due to our model.

Looking forward, we made a mutual agreement with ASML to continue our collaboration on this topic. Initially, the lessons learned from this study will be translated into a better setting for other systems. This setting is planned to accommodate our suggestions on milestone clustering. As mentioned above, ASML has other initiatives to reduce the cycle time. Before a company-wide implementation, we agreed to adjust/extend our model and software tool to accommodate these initiatives.

### Table 5: Performance of Alternative Solution Procedures for System B

<table>
<thead>
<tr>
<th>Case</th>
<th>CT (days)</th>
<th>CT_red (%)</th>
<th>On-time del. (%)</th>
<th>Holding cost (%)</th>
<th>Total cost (%)</th>
<th>Work in progress (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Sim Anly</td>
<td>Sim Anly</td>
<td>Sim Anly</td>
<td>Sim Anly</td>
</tr>
<tr>
<td>ASML</td>
<td>165.0</td>
<td>0</td>
<td>81.1 80.8</td>
<td>157.5 157.5</td>
<td>192.3</td>
<td>191.9 0 0</td>
</tr>
<tr>
<td>DFP</td>
<td>144.3</td>
<td>10</td>
<td>78.6 79.0</td>
<td>93.5 93.5</td>
<td>146.2</td>
<td>145.7 10.7</td>
</tr>
<tr>
<td>DFP(modified)</td>
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<td>11</td>
<td>81.0 81.4</td>
<td>100.0 100.0</td>
<td>145.6</td>
<td>145.3 9.6</td>
</tr>
<tr>
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<td>80.9 81.1</td>
<td>107.5 107.3</td>
<td>153.2</td>
<td>152.5 8.4</td>
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<tr>
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<td>81.1 81.1</td>
<td>108.4 108.3</td>
<td>153.5</td>
<td>153.1 8.2</td>
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<tr>
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<td>119.4 119.3</td>
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### 6. Conclusions and Future Research Directions

In this paper, we studied an assembly system that consists of a number of parallel multistage processes feeding a multistage final assembly process. Each stage has a stochastic throughput time and is controlled by planned leadtimes. All stages incur a cost for early completion and a lateness cost when the customer order is delivered late. The objective is to determine the planned leadtimes to minimize...
the sum of expected earliness and lateness costs. We developed an iterative heuristic that relies on a conjecture related to the generalized Newsvendor functions. Comparing our results with those from a numerical optimization method (Davidon-Fletcher-Powell), we concluded that our heuristic performs well with a percentage cost error of 1.33%. This heuristic was developed with the intention of solving the planned leadtime problem of ASML, the world’s leading provider of lithography systems for the semiconductor industry. The data revealed that the most complex module’s production was already performed until the start of process 0 because of the tardiness of other processes.

As a future research direction, our heuristic can be extended to solve for the leadtimes of divergent systems since there are no imbalance issues as there is no allocation problem. It might be possible to use the same approach to obtain optimality equations for general networks under planned leadtime control. Material availability may also be incorporated based on the delay distribution under some material control policy and demand process. The effectiveness of the heuristic also suggests an investigation of whether the Clark and Scarf (1960) decomposition also holds for assembly systems and beyond.

Appendix. Proof of Conjecture 1 for Two-Echelon Assembly Systems

We consider an assembly system consisting of M single-stage processes feeding a single-stage final process whose index is 0. Let \( i \in \{0, 1, 2, \ldots, M\} \) be the index of the \( i \)th process. Let \( \tau_i \) denote the planned leadtime of process \( i \in \{0, 1, 2, \ldots, M\} \). We define \( h_i \) and \( H_i \) as the marginal and local holding costs of process \( i \in \{0, 1, 2, \ldots, M\} \) and \( p \) as the penalty cost per unit time late for delivery to the customer. Note that \( h_i = H_i \), \( \forall i \in \{1, 2, \ldots, M\} \) and \( \sum_{j=0}^{M} h_j = H_0 \). As in \$2.1$, we define \( E_i \) and \( L_i \) as the earliness and lateness of process \( i \in \{0, 1, 2, \ldots, M\} \). \( W_{ij} = \max_{j \in \{1, 2, \ldots, M\}} (L_i - T_j)^+ \) is the waiting time of process \( i \) until the start of process 0 because of the tardiness of other processes. \( W_0 \) is the waiting time of process 0 because of the tardiness of processes 1 to \( M \). We define \( \hat{C}(T) \) as the cost of an arbitrary system assembled from process \( i \)'s perspective. Hence, \( \hat{C}(T) = E[\hat{C}(T)] \). Using Equation (3), \( \hat{C}(T) \) can be written as

\[
\hat{C}(T) = \sum_{j=1}^{M} h_{j}(T_j + T_0) + h_{0}(T_0 + T_0) + h_{0}(T_0 - \tau_0 - E_0 - W_0) + (p + H_0) L_0.
\]

Using the above notation, we rewrite Conjecture 1 for two-echelon assembly systems as a theorem.

**Theorem 2.** The optimal planned leadtimes \( \{T_i\}_{i=0}^{M} \) satisfy the following Newsvendor equations:

1. \( P(0 < T_0) = h_0/(p + H_0) \)
2. \( P(\max_{j \in \{1, 2, \ldots, M\}, j \neq i}(T_j - T_i)^+ < (T_i - \tau_i)^+, \tau_i < T_0, (T_i - \tau_i)^+ + (0 - T_0_0 > 0) = h_i/(p + H_0), \forall i \in \{0, 1, 2, \ldots, M\} \).

**Proof.** We initially establish the equality \( P(L_0^i > 0) = H_0/(p + H_0) \). We perturb the optimal solution \( \{T_i\}_{i=0}^{M} \).

For \( \epsilon > 0 \), define \( \hat{T}_0 = T_0 + \epsilon \) and \( \hat{T}_i = T_i, \forall j \in \{1, 2, \ldots, M\} \). With these definitions of \( \{\hat{T}_i\}_{i=0}^{M} \), we can associate the random variables \( \hat{E}_i, \hat{L}_i \) and \( \hat{W}_{ij}, \forall j \in \{0, 1, \ldots, M\} \). We have the following identities \( \forall j \in \{1, 2, \ldots, M\}; \hat{E}_j = E_j, \hat{L}_j = L_j, \) and \( \hat{W}_{ij} = W_{ij} \). Using these, we find that

\[
\hat{C}(\hat{T}_i) = \hat{C}(T_i) - \hat{C}(T_0) = \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) (\hat{L}_0 - \hat{L}_0).
\]

We consider 3 cases regarding the relative magnitudes of \( L_0^i \) and \( \epsilon \) (a) \( L_0^i = 0 \), (b) \( 0 < L_0^i < \epsilon \), and (b) \( L_0^i > \epsilon \).

(a) \( L_0^i = 0 \): In this case we have \( \hat{L}_0 = L_0 \) implying that

\[
\hat{C}(\hat{T}_i) = \hat{C}(T_i) - \hat{C}(T_0) = \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) (\hat{L}_0 - \hat{L}_0).
\]

(b) \( 0 < L_0^i < \epsilon \): In this case \( 0 \leq \hat{L}_0 - \hat{L}_0 < \epsilon \) implying that

\[
\hat{C}(\hat{T}_i) = \hat{C}(T_i) - \hat{C}(T_0) = \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) (\hat{L}_0 - \hat{L}_0).
\]

(c) \( L_0^i > \epsilon \): Using the fact that for some \( i, L_0^i = (L_i + W_{ij} + \tau_0 - \hat{T}_0)^+ \) and \( \hat{L}_0 = (\hat{L}_0 + W_{ij} + \tau_0 - \hat{T}_0)^+ = (\hat{L}_0 + W_{ij} + \tau_0 - \hat{T}_0)^+ \), we find that \( \hat{L}_0 = L_0^i - \epsilon \) implying that

\[
\hat{C}(\hat{T}_i) = \hat{C}(T_i) - \hat{C}(T_0) = \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) \epsilon.
\]

Combining these three cases, we get

\[
\hat{C}(\hat{T}_i) = \hat{C}(T_i) - \hat{C}(T_0) = \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) (L_0^i - \hat{L}_0).
\]

Taking the expectations with respect to all possible events and dividing the expected cost difference by \( \epsilon \) we find

\[
\frac{C(\hat{T}_i) - C(T_i)}{\epsilon} = \mathbb{E} \left[ \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) (L_0^i - \hat{L}_0) \right]
\]

\[
= \mathbb{E} \left[ \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0) (L_0^i - \hat{L}_0) \right] - \mathbb{E} \left[ \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0)(L_0^i - \hat{L}_0) \right]
\]

\[
= \mathbb{E} \left[ \left( \sum_{j=0}^{M} h_j \right) \epsilon - (p + H_0)(L_0^i - \hat{L}_0) \right] = 0.
\]
Given that for $0 < L_0^* \leq \varepsilon$ we have $0 \leq L_0^* - \hat{L}_0 \leq \varepsilon$, the following inequality is satisfied
\[
\mathbb{E}[1_{0 < L_0^* - \hat{L}_0} \left( \sum_{j=0}^{M} h_j + (p + H_0) \right)] \geq \frac{|C((T_j^M)_{j=0}^M) - C((T_j^M)_{j=0}^M)|}{\varepsilon} - \mathbb{E}[1_{L_0^* - \hat{L}_0} \left( \sum_{j=0}^{M} h_j \right)] - \mathbb{E}[1_{L_0^* - \hat{L}_0} \left( \sum_{j=0}^{M} h_j - (p + H_0) \right)].
\]

As $(T_j^*)_{j=0}^M$ is the optimal solution and the left-hand side tends to zero as $\varepsilon$ goes to zero, we have
\[
P(L_0^* > 0) \left( \sum_{j=0}^{M} h_j - (p + H_0) \right) + P(L_0^* = 0) \sum_{j=0}^{M} h_j = 0,
\]
\[
P(L_0^* > 0) \left( \sum_{j=0}^{M} h_j - (p + H_0) \right) + (1 - P(L_0^* > 0)) \sum_{j=0}^{M} h_j = 0,
\]
\[
\sum_{j=0}^{M} h_j - (p + H_0)P(L_0^* > 0) = 0,
\]
\[
P(L_0^* > 0) = \frac{H_0}{p + H_0}.
\]

Hence, the required equality is established.

Next, relying on the same idea, we prove the News-vendor equations. We consider process $i$. For $\varepsilon > 0$, we define $\hat{T}_i = T_i^* + \varepsilon$, $\hat{T}_i = T_i^* - \varepsilon$, $\hat{T}_i = T_i^*$, $\forall j \in \{1, 2, \ldots, M\}$, $\neq i$. We derive an expression for $\bar{C}((T_j^M)_{j=0}^M) - \bar{C}((T_j^M)_{j=0}^M)$ by considering 3 cases and multiple subcases. The cases are based on the relative magnitudes of $E_i^*$ and $\varepsilon$: (a) $E_i^* > 0$, (b) $0 < E_i^* \leq \varepsilon$, and (c) $E_i^* < \varepsilon$.

(a) $E_i^* > 0$: It follows that $\hat{E}_i = 0$ and $\hat{L}_i = L_i^* + \varepsilon$. Now, we consider 3 subcases based on the relative magnitudes of $W_i^*$ and $\varepsilon$: (a.1) $W_i^* > 0$, (a.2) $0 < W_i^* \leq \varepsilon$, (a.3) $W_i^* > \varepsilon$.

(a.1) $W_i^* = 0$: This implies that $\hat{W}_{i-1} = 0$. We write $\hat{L}_0$ as follows
\[
\hat{L}_0 = (\hat{L}_i + \hat{W}_{i-1} + \tau_0 - \hat{T}_0^*) = (L_i^* + \varepsilon + 0 + \tau_0 - T_0^* - \varepsilon)^* = (L_i^* + \tau_0 - T_0^* - \varepsilon)^*.
\]
Thus, we have
\[
\bar{C}((T_j^M)_{j=0}^M) - \bar{C}((T_j^M)_{j=0}^M) = \left( \sum_{j=1, j \neq i}^{M} h_j \right) \varepsilon.
\]

(a.2) $0 < W_i^* \leq \varepsilon$: This implies that $\hat{W}_{i-1} = 0$. We write $\hat{L}_0$ and $L_0^* - \hat{L}_0$ as follows
\[
\hat{L}_0 = (\hat{L}_i + \hat{W}_{i-1} + \tau_0 - \hat{T}_0^*) = (L_i^* + \varepsilon + 0 + \tau_0 - T_0^* - \varepsilon)^* = (L_i^* + \tau_0 - T_0^* - \varepsilon)^*,
\]
\[
L_0^* - \hat{L}_0 = (L_i^* + W_i^* + \tau_0 - T_0^*)^* - (L_i^* + \tau_0 - T_0^*)^*.
\]
Then, it follows that $0 \leq L_0^* - \hat{L}_0 \leq \varepsilon$ and the cost difference is
\[
\bar{C}((T_j^M)_{j=0}^M) - \bar{C}((T_j^M)_{j=0}^M) = \left( \sum_{j=1, j \neq i}^{M} h_j \right) \varepsilon + h_0(W_i^* - \hat{W}_{i-1}) + (p + H_0)(L_0^* - \hat{L}_0).
\]
Now we have covered all possible cases. This implies that
\[ \tilde{C}(|\hat{T}_j|_{\text{job}}) - \tilde{C}((T^*_j)_{\text{job}}) = \]
\[ = \mathbf{1}_{[\hat{w}_j = 0], W^*_j = 0} \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \epsilon + \mathbf{1}_{[\hat{w}_j = 0], W^*_j > 0} \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \epsilon \]
\[ + h_0(W^*_j - \hat{W}_j - \epsilon) - (p + H_0)(L^*_j - \hat{L}_0) \]
\[ + \mathbf{1}_{[\hat{w}_j = 0], W^*_j > 0, L^*_j > 0} \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \epsilon \]
\[ + h_0(W^*_j - \hat{W}_j - \epsilon) - (p + H_0)(L^*_j - \hat{L}_0) \]
\[ + \mathbf{1}_{[\hat{w}_j > 0], L^*_j > 0} \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \epsilon \]
\[ + h_0(W^*_j - \hat{W}_j - \epsilon) - (p + H_0)(L^*_j - \hat{L}_0) \].

As all the terms on the right-hand side are bounded and the intervals approach zero probability mass, we find that the right-hand side of the above equality goes to zero as \( \epsilon \) approaches 0. We also note that because of the optimality of \( (T^*_j)_{\text{job}} \), we have \( \lim_{\epsilon \to 0} \left( \tilde{C}((T^*_j)_{\text{job}}) - \tilde{C}((T_j)_{\text{job}}) / \epsilon \right) = 0 \). Then it follows that
\[ P(E^*_j = 0, W^*_j = 0) \left( \sum_{j=1, j \neq i}^M h_j \right) \]
\[ + P(E^*_j = 0, W^*_j > 0, L^*_j = 0) \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \]
\[ + P(E^*_j = 0, W^*_j > 0, L^*_j > 0) \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) - (p + H_0) \]
\[ + P(E^*_j > 0, L^*_j > 0) \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) - (p + H_0) \] \( = 0 \).

After rearrangement of the terms we get
\[ (p + H_0)(P(E^*_j = 0, W^*_j > 0, L^*_j > 0) + P(E^*_j > 0, L^*_j > 0)) \]
\[ = \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \left( P(E^*_j = 0, W^*_j > 0, L^*_j = 0) \right) \]
\[ + P(E^*_j = 0, W^*_j > 0, L^*_j > 0) \]
\[ + \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) \left( P(E^*_j > 0, L^*_j = 0) + P(E^*_j > 0, L^*_j > 0) \right) \]
\[ + \left( \sum_{j=1, j \neq i}^M h_j \right) \left( P(E^*_j = 0, W^*_j = 0) \right). \]

The sum of the probabilities with \( (\sum_{j=1, j \neq i}^M h_j + h_0) \) as a multiplier can be simplified to
\[ P(E^*_j = 0, W^*_j > 0, L^*_j = 0) + P(E^*_j = 0, W^*_j > 0, L^*_j > 0) \]
\[ + P(E^*_j > 0, L^*_j = 0) + P(E^*_j > 0, L^*_j > 0) \]
\[ = P(E^*_j > 0) + P(E^*_j = 0, W^*_j > 0) = 1 - P(E^*_j = 0, W^*_j = 0). \]

Using this simplification we write
\[ (p + H_0)(P(E^*_j = 0, W^*_j > 0, L^*_j > 0) + P(E^*_j > 0, L^*_j > 0)) \]
\[ = \left( \sum_{j=1, j \neq i}^M h_j + h_0 \right) - h_0 P(E^*_j = 0, W^*_j = 0). \]

Next we note that the following identity holds
\[ P(L^*_j > 0) = P(E^*_j > 0, L^*_j > 0) + P(E^*_j = 0, L^*_j > 0) \]
\[ = P(E^*_j > 0, L^*_j > 0) + P(E^*_j = 0, W^*_j > 0, L^*_j > 0) \]
\[ + P(E^*_j = 0, W^*_j = 0, L^*_j > 0). \]
Using this identity and the equality $\sum_{j=1, j\neq i}^{M} h_j + h_i = H_0 - h_i$ we write the optimality equation as
\[
(p + H_0)(P(E_i^* = 0, W_{i-0}^* = 0, L_{i-0}^* > 0)) = (p + H_0)(P(E_i^* = 0, W_{i-0}^* = 0, L_{i-0}^* = 0)).
\]

Furthermore, we have
\[
P(E_i^* = 0, W_{i-0}^* = 0, L_{i-0}^* > 0) = P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 > T_0^*) + P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 \leq T_0^*, L_{i-0}^* > 0)
\]
\[
= P(E_i^* = 0, W_{i-0}^* = 0) + (p + H_0)P(\tau_0 > T_0^*) = (p + H_0)P(\tau_0 > T_0^%).
\]

The last equality is due to independence of $\tau$ of $E_i^* = 0$ and $W_{i-0}^*$. Substituting the latest equality into our optimality equation, we find
\[
(p + H_0)(P(E_i^* > 0) - P(E_i^* = 0, W_{i-0}^* = 0)) = (p + H_0)P(\tau_0 > T_0^%)
\]
\[
= (p + H_0)P(\tau_0 > T_0^%) - h_0 P(E_i^* = 0, W_{i-0}^* = 0)
\]
\[
= (p + H_0)P(\tau_0 > T_0^%) - h_0 P(E_i^* = 0, W_{i-0}^* = 0)
\]
\[
= -h_0 + (p + H_0)P(\tau_0 > T_0^%) - h_0 = (p + H_0)P(\tau_0 > T_0^%).
\]

Using the equality $- (p + H_0)P(L_0^* > 0) + H_0 = 0$, we obtain the following set of equations $\forall i \in \{1, 2, \ldots, M\}$
\[
(p + H_0)P(\tau_0 > T_0^%) = (p + H_0)P(\tau_0 > T_0^%)
\]
\[
= -h_0 + (p + H_0)P(\tau_0 > T_0^%) = 0
\]
\[
\sum_{j=1, j\neq i}^{M} h_j + h_i = H_0 - h_i.
\]

The above set of equations are solved if
\[
P(\tau_0 > T_0^%) = \frac{h_0}{p + H_0} \quad \text{and} \quad P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 > T_0^*, L_{i-0}^* > 0) = \frac{h_i}{p + H_0}.
\]

Note that $P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 > T_0^*, L_{i-0}^* > 0)$ equals the probability that process $i$ finishes late, and it is the process that causes process $0$ to start late, and process $0$ has a throughput time shorter than its planned leadtime and the system is delivered late. This event is donated as the event that process $i$ causes lateness. The event $\tau_0 > T_0^*$ is defined as the event that process $0$ causes lateness. Thus we find that $P(\text{process } i \text{ causes system lateness}) = h_i/(p + H_0), \forall i \in \{0, 1, \ldots, M\}$. Furthermore, the events are mutually exclusive and together make up all possible events for which the system is delivered late. Thus, we have $P(\text{system is delivered late}) = \sum_{i=0}^{M} h_i/(p + H_0) = H_0/(p + H_0).

For sake of completeness, we note that
\[
P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 \leq T_0^*, L_{i-0}^* > 0)
\]
\[
= P\left( \max_{j|\{1, 2, \ldots, M\}, j|\neq i} (\tau_j - T_0^%) < (\tau_i - T_0^%) \right) > 0, \tau_0 < T_0^*, (\tau_i - T_0^%) + \tau_0 - T_0^* > 0
\]

Finally, with the following lemma we prove the uniqueness of the solution.

**Lemma 3.** The solution to the optimality equations in Theorem 2 is unique.

**Proof.** If we sum the optimality Equations (7) $\forall i \in \{1, 2, \ldots, M\}$ we obtain
\[
((p + H_0)P(\tau_0 > T_0^%) - h_0) \sum_{i=1}^{M} P(E_i^* = 0, W_{i-0}^* = 0) = -(\sum_{i=1}^{M} h_i + (p + H_0) \sum_{i=1}^{M} P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 \leq T_0^*, L_{i-0}^* > 0))
\]
\[
- h_0 - (p + H_0)P(\tau_0 > T_0^%)
\]
\[
= P(L_0^* > 0) - P(\tau_0 > T_0^%) = H_0/(p + H_0) - P(\tau_0 > T_0^%).
\]

Therefore, the equality (8) becomes
\[
((p + H_0)P(\tau_0 > T_0^%) - h_0) \sum_{i=1}^{M} P(E_i^* = 0, W_{i-0}^* = 0) = \frac{H_0}{p + H_0} - P(\tau_0 > T_0^%)
\]
\[
= h_0 - (p + H_0)P(\tau_0 > T_0^%).
\]

As $\sum_{i=1}^{M} P(E_i^* = 0, W_{i-0}^* = 0) < 1$, for any solution $\{T_j\}_{j=1}^{M}$, we must have $P(\tau_0 > T_0^%) = h_0/(p + H_0)$. This implies the equality $P(E_i^* = 0, W_{i-0}^* = 0, \tau_0 \leq T_0^*, L_{i-0}^* > 0) = h_i/(p + H_0)$. Hence, the solution to the optimality equations is unique and gives a global minimum. □

**References**


