Stuttering congruence for $\chi$

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STUTTERING CONGRUENCE FOR $\chi$

Bas Luttik$^{1,2}$ and Nikola Trčka$^1$

$^1$ Department of Mathematics and Computer Science, Eindhoven University of Technology, P.O. Box 513, NL-5600 MB Eindhoven, The Netherlands  
$^2$ CWI, P.O. Box 94079, NL-1090 GB Amsterdam, The Netherlands

Abstract. The language $\chi$ is a modeling and simulation language which is currently mainly used to analyse and optimize the performance of industrial systems. To be able to also verify functional properties of a system using a $\chi$ model, part of the language has been given a formal semantics. Rather than implementing a new model checker for $\chi$, the philosophy is to provide automatic translations from $\chi$ into the specification languages of existing state-of-the-art model checkers such as, e.g., Spin and UPPAAL.

In this paper, we propose for $\chi$ a notion of stuttering congruence, which is an adaptation of the notion of stuttering equivalence. We prove that our notion preserves the validity of CTL$^*_X$ formulas, that it preserves deadlock, and that it is indeed a congruence with respect to the constructs of $\chi$. We also indicate how our notion is to be used to establish confidence in the correctness of a translation from $\chi$ into Promela.

1 Introduction

The language $\chi$ [15] is a modeling language developed for detecting design flaws and for optimizing performance of industrial systems (machines, manufacturing lines, warehouses, factories, etc.) by simulation. Quite a few case studies have shown the usefulness of $\chi$ in an industrial context [11, 6, 9, 17]; simulation turns out to be a powerful technique for doing performance analysis such as approximating throughput and cycle time. However, for the verification of functional properties such as, e.g., deadlock freedom, simulation is less suitable. To be able to also do verification with $\chi$, either verification tools have to be developed especially for $\chi$, or existing state-of-the-art verification tools and techniques have to be made available for use with $\chi$. Currently, the latter approach is pursued.

The idea is to extend $\chi$ with facilities for doing formal verification by establishing a connection with other state-of-the-art verification tools and techniques on the level of the specification language. That is, formal verification of a $\chi$ model is done by first translating it into the input language of some model checker and then performing the actual verification. The suitability of this approach was shown in [2, 3], where a $\chi$ model of a turntable machine was translated to Promela [12], $\mu$CRL [1] and UPPAAL timed automata [13], and then verified in Spin, CADP [8] and UPPAAL, respectively. Of course, to be of use, the whole process should be as automatic as possible; the goal is to implement translators from $\chi$ into the input languages of several model checkers.
In [16], the translation of \( \chi \) specifications into Promela is discussed in more generality. The translation proceeds in two phases. The first phase, which we will henceforth call the preprocessing phase, consists of a transformation of the \( \chi \) model in an attempt to eliminate all \( \chi \) constructs that do not directly map to Promela constructs. For instance, \( \chi \) has an explicit construct for parallel composition which facilitates nested parallelism, whereas Promela only allows the (implicit) parallel composition of sequential Promela processes; so in the preprocessing phase the nested parallelism in the \( \chi \) model is eliminated. If the result after the preprocessing phase is a \( \chi \) model that only has constructions with a direct translation into Promela, then it can be translated to a Promela model; this phase is called the translation phase.

The main difficulty for establishing the correctness of the whole translation is that usually the two languages do not have a formal semantics in common. An advantage of the two-phase approach sketched above then is that the preprocessing phase of the translation, which is usually the most involved part, takes place entirely within the realm of \( \chi \). Therefore, a correctness proof for this phase only involves the formal semantics of \( \chi \). An additional advantage of the two-phase approach is that the preprocessing phase (and its correctness proof) is potentially reusable, e.g., when defining a translation from \( \chi \) to some other language.

The appropriate correctness criterion for a translation depends of course on the application. If the purpose is to establish that a \( \chi \) model is deadlock-free, then the translation should preserve deadlock. If the purpose is to do LTL model checking, then the translation should preserve the validity of LTL formulas. If the purpose is to do CTL model checking, then the translation should preserve the validity of all CTL formulas. In all cases, establishing the desired preservation of properties directly is usually cumbersome. It is often more convenient to relate the \( \chi \) model and its transformation by establishing that they are related according to some behavioural equivalence pertaining to the operational semantics of \( \chi \). The purpose of this paper is to define a behavioural equivalence that can be used to establish the correctness of the preprocessing phase of translations of \( \chi \) models into the language of state-based model checkers such as, e.g., Spin or UPPAAL.

Of course, such a behavioural equivalence should then preserve the relevant properties, which in our case means that it should preserve deadlock and the validity of (state-based) CTL\( ^* \chi \) formulas. Its intended application for establishing the correctness of syntactic translations puts some further requirements on the notion. For instance, since the transformations are generally defined in a compositional manner, it is particularly convenient if the behavioural equivalence is a congruence with respect to the syntactic constructs of \( \chi \). Also, it is desirable that it is defined on the operational semantics of \( \chi \) as directly as possible, i.e., it should be bisimulation-like. We propose a notion of stuttering congruence that meets these requirements.

The paper is organised as follows.

In Section 2 we present the syntax and the operational semantics of the discrete-event and untimed part of the language \( \chi \). We use the operational semantics to
define when a χ-process has deadlock, and we give the semantics of CTL$^\ast_X$ formulas with respect to χ processes.

In Section 3, we propose an adaptation of divergence blind stuttering bisimilarity [14]. We add to it a termination condition, which takes care of the distinction between successful and unsuccessful termination present in χ, and a divergence condition, which is needed both for the preservation of deadlock and preservation of CTL$^\ast_X$. We prove that our version of stuttering bisimilarity is an equivalence relation, and that it indeed preserves deadlock and the validity of (state-based) CTL$^\ast_X$ formulas.

In Section 4 we argue that stuttering bisimilarity as defined in Section 3 is not a congruence. So we adapt it further by excluding send and receive transitions as stuttering steps and by adding a root condition. The resulting notion we call stuttering congruence and we prove that it is indeed a congruence with respect to the syntactic constructs of the discrete-event, untimed part of χ.

In Section 5 we briefly discuss how our notion of stuttering congruence can be used to establish part of the correctness of the translation proposed in [16]. The paper ends with a conclusion.

2 The language χ

In this section we present the syntax and operational semantics of χ. We also define the notion of deadlock and the semantics of CTL$^\ast_X$ for χ processes. We use the formalization of χ proposed in [4], but without the time support and with a few minor differences that we shall mention on the fly.

2.1 Syntax and semantics

There are several predefined data types in χ, but they are not relevant for the present paper. For our purposes, it is enough to presuppose a set of variables $V$, a set of data values $D$, a set of data expressions $E$ that includes $D$ and $V$, and a set of boolean expressions $B$ that includes the set of truth values $\{true, false\}$.

**Definition 1.** A partial mapping $\sigma : V \rightarrow D$ with a finite domain (denoted $\text{dom}(\sigma)$) is called a state. The set of all states is denoted $\Sigma$.

We assume that $\sigma$ also extends to data expressions ($\sigma : E \rightarrow D$) and to boolean expressions ($\sigma : B \rightarrow \{true, false\}$). In the latter case we require $\sigma$ to be total.

To correctly override global variables by local ones of the same name, we use the function $\gamma$ defined as:

$$\text{dom}(\gamma(\sigma_1, \sigma_2)) = \text{dom}(\sigma_1) \cup \text{dom}(\sigma_2)$$

$$\gamma(\sigma_1, \sigma_2)(x) = \begin{cases} 
\sigma_1(x), & \text{if } x \in \text{dom}(\sigma_1) \\
\sigma_2(x), & \text{if } x \in \text{dom}(\sigma_2) \setminus \text{dom}(\sigma_1).
\end{cases}$$
We now give the syntax of $\chi$. The set of atomic processes $A$, and the set of all $\chi$ process terms $P$, are generated by the following grammar:

$$
\begin{align*}
a & ::= \varepsilon | \delta | \text{skip} | x := e | m!e | m?x \\
p & ::= a | b \rightarrow p | p;p | p|p | p^* | p \parallel [s|p] | \partial(p).
\end{align*}
$$

Here $a \in A$, $p \in P$, $x \in V$, $e \in E$, $b \in B$, $s \in \Sigma$ and $m \in M$, where $M$ is a set of channel names.

Elements of the set $C = P \times \Sigma$ we call configurations. They represent processes together with their context. If $c = (p, \sigma)$, then $\sigma$ is the state of $c$. The semantics of $\chi$ is given in terms of configurations.

We make a distinction between successful and unsuccessful termination. The statement $c \downarrow$ denotes that $c \in C$ successfully terminates. The statement $c \xrightarrow{a} c'$ denotes that $c \in C$ can execute the action $a$ and transform into a configuration $c'$. The set of actions that can be performed (denoted $A$) consists of the internal action $\tau$, the assignment action $aa(x, d)$, the send action $sa(m, d)$, the receive action $ra(m, d)$ and the communication action $ca(m, d)$, where $x \in V$, $m \in M$ and $d \in D$.

**Atomic processes** We explain each atomic process informally; the operational rules are given in Table 1.

The constant $\delta$ stands for the deadlock process. It cannot execute an action nor terminate successfully. The empty process $\varepsilon$ cannot do an action either, but it is considered successfully terminated. The skip process performs the internal action $\tau$, the assignment process $x := e$ assigns to $x$ the value of the expression $e$ according to the current state. The send process $m!e$ outputs the value of $e$ (in the current state) along channel $m$. The receive process $m?x$ inputs a value along channel $m$ and assigns it to $x$.

<table>
<thead>
<tr>
<th>Configuration</th>
<th>Action</th>
<th>New Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\langle \varepsilon, \sigma \rangle$</td>
<td>$\varepsilon$</td>
<td>$\langle \varepsilon, \sigma \rangle$</td>
</tr>
<tr>
<td>$\langle \varepsilon, \sigma \rangle$</td>
<td>$\delta$</td>
<td>$\langle \varepsilon, \sigma \rangle$</td>
</tr>
<tr>
<td>$\langle \varepsilon, \sigma \rangle$</td>
<td>$\text{skip}$</td>
<td>$\langle \varepsilon, \sigma \rangle$</td>
</tr>
<tr>
<td>$\langle x := e, \sigma \rangle$</td>
<td>$aa(x, d)$</td>
<td>$\langle \varepsilon, \gamma({x \mapsto d}, \sigma) \rangle$</td>
</tr>
<tr>
<td>$\langle m!e, \sigma \rangle$</td>
<td>$sa(m, d)$</td>
<td>$\langle \varepsilon, \gamma({x \mapsto d}, \sigma) \rangle$</td>
</tr>
<tr>
<td>$\langle m?x, \sigma \rangle$</td>
<td>$ra(m, d)$</td>
<td>$\langle \varepsilon, \gamma({x \mapsto d}, \sigma) \rangle$</td>
</tr>
</tbody>
</table>

**Compound processes** Here we give an informal explanation for each of the seven operators; the operational rules are given in Table 2.

The guarded process $b \rightarrow p$ behaves as $p$ when the value of the guard $b \in B$ is true (in the current state). The sequential composition $p ; q$ behaves as $p$ followed by the process $q$. The alternative composition $p \parallel q$ stands for a non-deterministic choice between $p$ and $q$. The process $p^*$ behaves as $p$, executed
zero (successful termination), or more times. The parallel composition operator \( \parallel \) executes \( p \) and \( q \) concurrently in an interleaved fashion. In addition, if one of the processes can execute a send action and the other one can execute a receive action on the same channel, then they can also communicate, i.e. \( p \parallel q \) can also execute the communication action on this channel. The scope operator is used for declarations of local variables. The process \([s \mid p]\) behaves as \( p \) in a local state \( s \). In contrast to [4] and [15], channel declarations are not allowed, i.e. channels are global. Finally, the encapsulation operator \( \partial \) disables all send and receive actions of a process. This is slightly more restrictive than in [4] and [15] where \( \partial \) is parameterized by a set of actions that should be blocked, but corresponds to current practice.

**Table 2.** Operational semantics for composed processes

<table>
<thead>
<tr>
<th>Step</th>
<th>Action</th>
<th>Transition</th>
</tr>
</thead>
<tbody>
<tr>
<td>6</td>
<td>( \sigma(b) = \text{true}, (p, \sigma) \downarrow )</td>
<td>( b:\rightarrow (p, \sigma) \downarrow )</td>
</tr>
<tr>
<td>7</td>
<td>( \sigma(b) = \text{true}, (p, \sigma) \downarrow )</td>
<td>( (p, \sigma) \rightarrow (p, \sigma') )</td>
</tr>
<tr>
<td>8</td>
<td>( (p, \sigma) \downarrow, (q, \sigma) \downarrow )</td>
<td>( (p : q, \sigma) \rightarrow (p', \sigma') )</td>
</tr>
<tr>
<td>9</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (p : q, \sigma) \rightarrow (p', \sigma') )</td>
</tr>
<tr>
<td>10</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (p : q, \sigma) \rightarrow (p', \sigma') )</td>
</tr>
<tr>
<td>11</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (p : q, \sigma) \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>12</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (q \parallel p, \sigma) \rightarrow (p', \sigma') )</td>
</tr>
<tr>
<td>13</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (q \parallel p, \sigma) \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>14</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (q \parallel p, \sigma) \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>15</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (q \parallel p, \sigma) \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>16</td>
<td>( (p, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (q \parallel p, \sigma) \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>17</td>
<td>( (p \parallel q, \sigma) \rightarrow (p', \sigma') )</td>
<td>( (q \parallel p, \sigma) \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>18</td>
<td>( \langle s \parallel p, \sigma \rangle \downarrow )</td>
<td>( \langle s \parallel p, \sigma \rangle \rightarrow (p', \sigma') )</td>
</tr>
<tr>
<td>19</td>
<td>( \langle s \parallel p, \sigma \rangle \rightarrow (p', \sigma') )</td>
<td>( \langle s \parallel p, \sigma \rangle \rightarrow (q', \sigma') )</td>
</tr>
<tr>
<td>20</td>
<td>( \langle p, \sigma \rangle \rightarrow (p', \sigma') )</td>
<td>( \langle (p, \sigma) \rangle \rightarrow (p', \sigma') )</td>
</tr>
<tr>
<td>21</td>
<td>( \langle p, \sigma \rangle \rightarrow (p', \sigma') )</td>
<td>( \langle (p, \sigma) \rangle \rightarrow (p', \sigma') )</td>
</tr>
</tbody>
</table>

### 2.2 Deadlock and CTL\(^*_\chi\) in \( \chi \)

First we give some abbreviations: \( c \rightarrow c' \) denotes that there exists \( a \in A \) such that \( c \xrightarrow{a} c' \); \( c \not\rightarrow c' \) denotes that there does not exist \( c' \) such that \( c \rightarrow c' \).

Now we define when a configuration has a deadlock.
Definition 2. We say that $c \in C$ has a deadlock iff there exist $c_0, \ldots, c_n \in C$ such that
\[c_0 = c, \quad c_0 \rightarrow \cdots \rightarrow c_n, \quad c_n \not\rightarrow \quad \text{and} \quad c_n \not\downarrow .\]

Next, we recall the formulas of the logic $\text{CTL}^*_X$ [7] and give their semantics. Let $AP$ be a set that we call the set of atomic propositions.

Definition 3. The formulas of the logic $\text{CTL}^*_X$ are defined as follows:

1. every atomic proposition is a state formula;
2. if $\varphi$ is a state formula, then $\neg \varphi$ is a state formula;
3. if $\varphi_1$ and $\varphi_2$ are state formulas, then $\varphi_1 \land \varphi_2$ is a state formula;
4. if $\psi$ is a path formula, then $\exists \psi$ is a state formula;
5. if $\varphi$ is a state formula, then $\varphi$ is a path formula;
6. if $\psi$ is a path formula, then $\neg \psi$ is a path formula;
7. if $\psi_1$ and $\psi_2$ are path formulas, then $\psi_1 \land \psi_2$ is a path formula;
8. if $\psi_1$ and $\psi_2$ are path formulas, then $\psi_1 \mathbin{U} \psi_2$ is a path formula.

For the satisfaction relation we need the notion of a path.

Definition 4. A path is an infinite sequence of configurations $c_0, c_1, c_2, \ldots$ such that either:

a. $c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots$ or
b. $c_0 \rightarrow \cdots \rightarrow c_n, \quad c_n \not\rightarrow \quad \text{and} \quad c_{i+1} = c_i$ for all $i \geq n$.

If $\pi$ is a path $c_0, c_1, c_2, \ldots$ then $\pi^i$ denotes the path $c_i, c_{i+1}, c_{i+2}, \ldots$. Sometimes we call $\pi$ a path from $c_0$.

We require that two configurations with the same state satisfy the same atomic propositions, by assuming that for each state $\sigma \in \Sigma$ there is a mapping $\text{val}_\sigma : AP \rightarrow \{\text{true}, \text{false}\}$. It will be used in the definition of the satisfaction relation between configurations and $\text{CTL}^*_X$ formulas.

Definition 5. We simultaneously define the satisfaction of a state formula $\varphi$ by a configuration $c$ (notation: $c \models \varphi$) and the satisfaction of a path formula $\psi$ by a path $\pi$ (notation: $\pi \models \psi$) as follows:

1. $c \models \alpha \in AP$ iff $\text{val}_\sigma(\alpha) = \text{true}$
2. $c \models \neg \varphi$ iff $c \not\models \varphi$,
3. $c \models \varphi_1 \land \varphi_2$ iff $c \models \varphi_1$ and $c \models \varphi_2$,
4. $\pi \models \varphi$ iff $c \models \varphi$ where $c$ is the first configuration in path $\pi$,
5. $c \models \exists \psi$ iff there is a path $\pi$ from $c$ such that $\pi \models \psi$,
6. $\pi \models \neg \psi$ iff $\pi \not\models \psi$,
7. $\pi \models \psi_1 \land \psi_2$ iff $\pi \models \psi_1$ and $\pi \models \psi_2$,
8. $\pi \models \psi_1 \mathbin{U} \psi_2$ iff there exists $j \geq 0$ such that $\pi^j \models \psi_2$ and $\pi^i \models \psi_1$ for all $i < j$.
3 Stuttering bisimilarity

Stuttering equivalence was originally proposed and proved to preserve the validity of CTL\textsuperscript{*} formulas by Browne, Clarke and Grumberg [5]. They define the notion on maximal paths associated with total Kripke structures, i.e., Kripke structures without deadlocked states. De Nicola and Vaandrager [14] drop the requirement that Kripke structures are total, and provide a definition of stuttering equivalence that proceeds via a notion of divergence blind stuttering bisimilarity defined on the Kripke structures themselves. We take divergence blind stuttering bisimilarity as a starting point, and add to it two conditions:

1. a \textit{termination condition} that ensures a proper handling of the distinction between successful and unsuccessful termination as it is present in $\chi$; and
2. a \textit{divergence condition} similar to one that appears in [10] to ensure the preservation of deadlock and the preservation of CTL\textsuperscript{*}.$X$

\textbf{Remark 1.} To obtain a notion that coincides with the notion of [5], instead of adding a divergence condition, de Nicola and Vaandrager define a divergence sensitive version of stuttering bisimilarity by extending Kripke structures with a fresh state that serves as a sink-state for deadlocked or divergent states. This approach is not suitable in our case, because it identifies deadlock and livelock, and because it is in conflict with our requirement, mentioned in the introduction, that the equivalence is defined directly on the operational semantics of $\chi$.

\textbf{Definition 6.} A symmetric relation $R \subseteq C \times C$ is a \textbf{stuttering bisimulation} iff, for all $(c, d) \in R$, $c$ and $d$ have the same state and:

1. if $c \downarrow$, then there exist $d_0, \ldots, d_n \in C$ such that $d_0 = d$, $d_0 \rightarrow \cdots \rightarrow d_n$, $d_n \downarrow$ and $cRd_i$ for all $i \leq n$, 
2. if $c \rightarrow c'$ for some $c' \in C$, then there exist $d_0, \ldots, d_n \in C$ such that $d_0 = d$, $d_0 \rightarrow \cdots \rightarrow d_n$, $cRd_i$ for all $i \leq n - 1$, and $c' Rd_n$, 
3. if there exists an infinite sequence $c_0, c_1, c_2, \ldots \in C$ such that $c_0 = c$, $c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots$ and $c_i Rd$ for all $i \geq 0$,

then there exist $d' \in C$ and $j > 0$ such that $d \rightarrow d'$ and $c_j Rd'$.

We refer to condition 1 as the \textit{termination condition}, to condition 2 as the \textit{transfer condition}, and to condition 3 as the \textit{divergence condition}.

A non-empty and finite sequence of configurations we call a \textit{block}. If $B = c_0, \ldots, c_n$ and $C = d_0, \ldots, d_m$ are blocks and $R$ is a stuttering bisimulation, we write $BRC$ when $c_0Rd_0$, $c_nRd_m$ and when, for all $i < n, j < m$, $c_i Rd_j$ implies $c_{i+1} Rd_j$ or $c_iRd_{j+1}$. Note that, $BRC$ implies $CRB$.  

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Definition 7. Let R be a stuttering bisimulation. Two sequences of configurations $\Omega_1$ and $\Omega_2$ are R-corresponding if:

- they are both finite and can be partitioned as $\Omega_1 = B^0, \ldots, B^K$ and $\Omega_2 = C^0, \ldots, C^K$ where $B^k RC^k$ for all $0 \leq k \leq K$; or
- they are both infinite and can be partitioned as $\Omega_1 = B^0, B^1, B^2, \ldots$ and $\Omega_2 = C^0, C^1, C^2, \ldots$ where $B^k RC^k$ for all $k \geq 0$.

It is clear that R-correspondence is a symmetric relation. Also note that, if $c_0, \ldots, c_n$ and $d_0, \ldots, d_m$ (similarly for the infinite case) are R-corresponding then for all $i \leq n$ there exists $j \leq m$ and for all $j \leq m$ there exists $i \leq n$ such that $c_i Rd_j$.

We now prove some properties of a stuttering bisimulation $R$.

Lemma 1. Suppose $c Rd$, $c_0, \ldots, c_n \in C$, $c_0 = c$ and $c_0 \rightarrow \cdots \rightarrow c_n$. Then there exist $d_0, \ldots, d_m \in C$, such that $d_0 = d$, $d_0 \rightarrow \cdots \rightarrow d_m$ and that $c_0, \ldots, c_n$ and $d_0, \ldots, d_m$ are R-corresponding.

Proof. The proof goes by induction on $n$. If $n = 0$, we take $m = 0$. Assume that the statement is valid for all $n \geq 0$ and suppose $c_0 \rightarrow \cdots \rightarrow c_n \rightarrow c_{n+1}$. From the inductive hypothesis, there exist $d_0, \ldots, d_m \in C$ such that $d_0 = d$ and $d_0 \rightarrow \cdots \rightarrow d_m$, and $c_0, \ldots, c_n$ and $d_0, \ldots, d_m$ can be partitioned as $B^0, \ldots, B^K$ and $C^0, \ldots, C^K$ respectively, such that $B^k RC^k$ for all $k \leq K$. This implies $c_n Rd_m$. Since $c_n \rightarrow c_{n+1}$, there exist $d_0^m, \ldots, d_m^k$ such that

$$d_0 = d_m, d_0^m \rightarrow \cdots \rightarrow d_m^k, c_n Rd_m^j$$

for all $j \leq k - 1$, and $c_{n+1} Rd_m^k$.

Lemma 2. If $c Rd$, then

a. for every sequence of configurations $c_0, \ldots, c_n$ such that $c_0 = c$, $c_0 \rightarrow \cdots \rightarrow c_n$ and $c_n \downarrow$, there exists an R-corresponding sequence $d_0, \ldots, d_m$ such that

$$d_0 = d, d_0 \rightarrow \cdots \rightarrow d_m \text{ and } d_m \downarrow; \text{ and}$$

b. for every sequence of configurations $c_0, \ldots, c_n$ such that $c_0 = c$, $c_0 \rightarrow \cdots \rightarrow c_n$ and $c_n \not\rightarrow$, there exists an R-corresponding sequence $d_0, \ldots, d_m$ such that

$$d_0 = d, d_0 \rightarrow \cdots \rightarrow d_m, \text{ and } d_m \not\rightarrow$$

Proof. Consider a sequence of configurations $c_0, \ldots, c_n$ such that $c_0 = c$ and $c_0 \rightarrow \cdots \rightarrow c_n$. Then, by Lemma 1, there exists an R-corresponding sequence $d_0, \ldots, d_m$ such that $d_0 = d$ and $d_0 \rightarrow \cdots \rightarrow d_m$; it follows that $c_n Rd_m$.

a. Suppose $c_n \downarrow$. Then, there exist $d_0^m, \ldots, d_m^k \in C$ such that

$$d_0 = d_m, d_0^m \rightarrow \cdots \rightarrow d_m^k, d_m^k \downarrow \text{ and } c_n Rd_m^j \text{ for all } 0 \leq j \leq k.$$
b. Suppose \( c_n \neq \emptyset \). There cannot exist an infinite sequence of configurations \( d^n_0, d^n_1, d^n_2, \ldots \) such that

\[
d^n_0 = d_m, \quad d^n_0 \rightarrow d^n_1 \rightarrow d^n_2 \rightarrow \cdots \quad \text{and} \quad c_n R d^n_m \quad \text{for all} \quad i \geq 0,
\]

for then \( c_n \neq \emptyset \) would contradict the divergence condition. Also, there cannot exist \( d^n_m, \ldots, d^n_k \in C \) (\( k \geq 0 \)) such that

\[
d^n_0 = d_m, \quad d^n_0 \rightarrow \cdots \rightarrow d^n_k, \quad c_n R d^n_m \quad \text{for all} \quad i \leq k - 1, \quad \text{and} \quad c_n R d^n_k,
\]

for then \( c_n \neq \emptyset \) would contradict the transfer condition. Hence, there exist \( d^n_m, \ldots, d^n_k \in C \) such that

\[
d^n_0 = d_m, \quad d^n_0 \rightarrow \cdots \rightarrow d^n_k, \quad d^n_k \neq \emptyset, \quad \text{and} \quad c_n R d^n_m \quad \text{for all} \quad i \leq k.
\]

Clearly, in both cases \( c_0, \ldots, c_n \) and \( d_0, \ldots, d_m, d^1_m, \ldots, d^k_m \) are R-corresponding.

**Lemma 3.** If \( c R d \), then for every infinite sequence of configurations \( c_0, c_1, c_2, \ldots \) such that \( c_0 = c \) and \( c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots \), there exists an R-corresponding sequence of configurations \( d_0, d_1, d_2, \ldots \) such that

\[
d_0 = d, \quad d_0 \rightarrow d_1 \rightarrow d_2 \rightarrow \cdots.
\]

**Proof.** Consider an infinite sequence of configurations \( c_0, c_1, c_2, \ldots \) such that \( c_0 = c \) and \( c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots \); we define infinite sequences of blocks \( C^0, C^1, C^2, \ldots \) and \( D^0, D^1, D^2, \ldots \) such that \( C^k R D^k \) for all \( k \geq 0 \), and such that \( C^0, C^1, C^2, \ldots \) is a partitioning of \( c_0, c_1, c_2, \ldots \) and \( D^0, D^1, D^2, \ldots \) is a partitioning of a sequence \( d_0, d_1, d_2, \ldots \) such that \( d_0 = d \) and \( d_0 \rightarrow d_1 \rightarrow d_2 \rightarrow \cdots \). The construction is by induction on \( k \), and each step delivers the blocks \( C^k \) and \( D^k \) and the first configurations of \( C^{k+1} \) and \( D^{k+1} \).

Let \( C^0, \ldots, C^K \) and \( D^0, \ldots, D^K \) be sequences of blocks such that \( C^k R D^k \) for all \( k \geq 0 \), and let \( c_n \) and \( d_m \) are the first configurations of \( C^{k+1} \) and \( D^{k+1} \) respectively.

If \( c_i Rd_m \) for all \( i \geq n \), then by the divergence condition there exist \( d^n_m \in C \) and \( j > n \) such that \( d^n_m \rightarrow d^n_m \) and \( c_j R d^n_m \). Then, take

\[
C^{K+1} = c_n, \ldots, c_{j-1} \quad \text{and} \quad D^{K+1} = d_m,
\]

and take \( c_j \) and \( d^n_m \) for the first configurations of \( C^{k+2} \) and \( D^{k+2} \) respectively.

On the other hand, if \( c_i Rd_m \) for all \( i \leq k \) but not \( c_{k+1} Rd_m \), then, since \( c_k R d_m \), there exist \( d^k_m, \ldots, d^n_m \in C \) such that

\[
d^k_m = d_m, \quad d^k_m \rightarrow \cdots \rightarrow d^l_m, \quad c_k R d^l_m \quad \text{for all} \quad i \leq l - 1, \quad \text{and} \quad c_{k+1} R d^l_m.
\]

Note that \( l > 0 \) because not \( c_{k+1} Rd_m \). Take

\[
C^{k+1} = c_n, \ldots, c_k \quad \text{and} \quad D^{k+1} = d^0_m, \ldots, d^{k-1}_m.
\]

Take \( c_{k+1} \) and \( d^l_m \) for the first configurations of \( C^{k+2} \) and \( D^{k+2} \) respectively.
Definition 8. Two configurations $c$ and $d$ are stuttering bisimilar, denoted $c \sim_{st} d$, if there exists a stuttering bisimulation $R$ such that $c R d$.

We now prove that it is stuttering bisimilarity is an equivalence relation. The usual way to prove that a bisimulation-like equivalence $\sim$ is transitive, is to suppose that $c \sim d$ and $d \sim e$ are witnessed by bisimulation relations $R_1$ and $R_2$ respectively, and then show that $R_1 \circ R_2$ is again a bisimulation relation. However, this method fails here, due to the nature of the divergence condition. We prove transitivity by showing that the transitive closure of a stuttering bisimulation is a stuttering bisimulation.

Lemma 4. Let $R$ be a stuttering bisimulation. Then $R^+ = \bigcup_{o \in \omega} R^o$ is also a stuttering bisimulation.

Proof. The transitive closure of any symmetric relation is symmetric so $R^+$ is symmetric.

Suppose $c R^+ e$. First we prove that, for all $o \geq 1$ and all $(c, e) \in R^o$, $c$ and $e$ have the same state, $(c, e)$ satisfies the termination and transfer condition and if $c_0 = c$, $c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots$ then there exist $e_0, e_1, e_2, \ldots \in C$ such that $e_0 = e$, $e_0 \rightarrow e_1 \rightarrow e_2 \rightarrow \cdots$ and for all $j \geq 0$ there is $i \geq 0$ such that $c_i R^o e_j$.

The proof is by induction on $o$. If $o = 1$ then the statement follows from the definition of $R$ and Lemma 3. Suppose the statement is valid for $o \geq 1$ and suppose $c R^{o+1} e$. This means that there is $d \in C$ such that $c R^o d$ and $d R e$. By the inductive hypothesis, $c$ and $d$ have the same state. By the definition of $R$, $d$ and $e$ have the same state. Therefore, $c$ and $e$ have the same state.

Suppose $c \vdash_o$. By the inductive hypothesis, there exist $d_0, \ldots, d_n \in C$ such that

$$d_0 = d, \quad d_0 \rightarrow \cdots \rightarrow d_n, \quad d_n \downarrow \quad \text{and} \quad c R^o d_i \quad \text{for all} \quad i \leq n.$$ 

By Lemma 2a, there exist $e_0, \ldots, e_m \in C$ such that

$$e_0 = e, \quad e_0 \rightarrow \cdots \rightarrow e_m, \quad e_m \downarrow \quad \text{and} \quad \text{for all} \quad j \leq m \quad \text{there is} \quad i \leq n \quad \text{such that} \quad d_i R e_j$$

(see the note after Definition 7). Therefore, $c R^{o+1} e_j$ for all $j \leq m$.

Suppose $c \rightarrow c'$. By the inductive hypothesis, there exist $d_0, \ldots, d_n \in C$ such that

$$d_0 = d, \quad d_0 \rightarrow \cdots \rightarrow d_n, \quad c R^o d_i \quad \text{for all} \quad i \leq n - 1, \quad \text{and} \quad c' R^o d_n.$$ 

By Lemma 1, there exist $e_0, \ldots, e_m \in C$ such that $e_0 = e$,

$$e_0 \rightarrow \cdots \rightarrow e_m, \quad d_{n-1} R e_m, \quad \text{and} \quad \text{for all} \quad j \leq m \quad \text{there is} \quad i \leq n - 1 \quad \text{such that} \quad d_i R e_j.$$ 

Now, there exist $e_0^0, \ldots, e_m^k \in C$ such that

$$e_0 = e_m, \quad e_0^0 \rightarrow \cdots \rightarrow e_m^k, \quad d_{n-1} R e_m^i \quad \text{for all} \quad i \leq k - 1, \quad \text{and} \quad d_n R e_m^k.$$ 

Since $c R^o d_i$ and $d_i R e_j$, $c R^{o+1} e_j$ for all $j \leq m$. Since $c R^o d_{n-1}$ and $d_{n-1} R e_m^i$, $c R^{o+1} e_m^i$ for all $i \leq k - 1$. Since $c' R^o d_n$ and $d_n R e_m^k$, $c' R^{o+1} e_m^k$. 

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Suppose now \( c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots \). By the inductive hypothesis, there exists an infinite sequence of configurations \( d_0, d_1, d_2, \ldots \in C \) such that
\[
d_0 = d, \ d_0 \rightarrow d_1 \rightarrow d_2 \rightarrow \cdots \quad \text{and for all } j \geq 0 \text{ there is } i \geq 0 \text{ such that } c_i R^* d_j.
\]
By Lemma 3 there exist \( e_0, e_1, e_2, \ldots \in C \) such that
\[
e_0 = e, \ e_0 \rightarrow e_1 \rightarrow e_2 \rightarrow \cdots \quad \text{and for all } k \geq 0 \text{ there is } j \geq 0 \text{ such that } d_j R e_k.
\]
Therefore, for all \( k \geq 0 \) there is \( j \geq 0 \) such that \( c_i R^{k+1} e_k \).

Since \( R^+ = \bigcup_{o \geq 0} R^o \), it now follows immediately that \( R^+ \) satisfies the termination and transfer condition and that, for all \( (c, d) \in R^+, c \) and \( d \) have the same state. It remains to prove that \( R^+ \) also satisfies the divergence condition.

Suppose \( c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots \) and \( c_i R^+ d \) for all \( i \geq 0 \). Since \( c R^+ d \) we conclude that \( c R^o d \) for some \( o \geq 1 \). As we showed before, there exist \( d_0, d_1, d_2, \ldots \in C \) such that \( d_0 = d, \ d_0 \rightarrow d_1 \rightarrow d_2 \rightarrow \cdots \) and for all \( j \geq 0 \) there exists \( i \geq 0 \) such that \( c_i R^o d_j \). In particular, there exists \( i \geq 0 \) such that \( c_i R^+ d_j \). If \( i > 0 \) then the divergence condition is proved, so suppose \( i = 0 \). Then \( c_1 R^+ d_0, c_0 R^+ d_0 \) and \( c_0 R^+ d_1 \), and hence, since \( R^+ \) is symmetric and transitive, it follows that \( c_1 R^+ d_1 \).

**Theorem 1.** Stuttering bisimilarity on configurations is an equivalence relation.

**Proof.** The set \( \{(c, c) \mid c \in C\} \) is clearly a stuttering bisimulation, so \( \sim_{st} \) is reflexive. Furthermore, that \( \sim_{st} \) is symmetric follows directly from the required symmetry of a stuttering bisimulation. It remains to prove transitivity.

Suppose \( c \sim_{st} d \) and \( d \sim_{st} e \). Then, there exist stuttering bisimulations \( R_1 \) and \( R_2 \) such that \( c R_1 d \) and \( d R_2 e \). Let \( R = R_1 \cup R_2 \). It is not hard to show that \( R \) is also a stuttering bisimulation. By Lemma 4, so is \( R^+ \). Since \( R \subseteq R^+ \), \( c R^+ d \) and \( d R^+ e \). By the transitivity of \( R^+ \), we conclude \( c R^+ e \), and hence \( c \sim_{st} e \).

**Corollary 1.** \( \sim_{st} \)-correspondence is an equivalence relation.

In the remainder of this section we establish that stuttering bisimilarity preserves deadlock and the validity of CTL\(^*_X\) formulas.

**Theorem 2.** If \( c \sim_{st} d \) then \( c \) has deadlock if \( d \) has deadlock.

**Proof.** Suppose \( c \) has deadlock (when \( d \) has deadlock the proof is similar). This means that there exist \( c_0, \ldots, c_n \) such that
\[
c_0 = c, \ c_0 \rightarrow \cdots \rightarrow c_n, \ c_n \not\rightarrow \text{ and } c_n \not\forall.
\]
By Lemma 2b, there exist \( d_0, \ldots, d_m \) such that
\[
d_0 = d, \ d_0 \rightarrow \cdots \rightarrow d_m, \ d_m \not\rightarrow \text{ and } c_n Rd_m.
\]
Suppose \( d_m \downarrow \). Then, there exist \( c^0_n, \ldots, c^k_n \) such that
\[
c^0_n \rightarrow \cdots \rightarrow c^k_n, \text{ and } c^k_n \downarrow.
\]
This is however not possible (even when \( k = 0 \)) because \( c_n \not\rightarrow \) and \( c_n \not\forall \).
The following lemma plays a crucial role in the proof that stuttering bisimilarity preserves the validity of $\text{CTL}^*_{\neg X}$ formulas.

**Lemma 5.** If $c \sim_{\text{st}} d$, then for every path from $c$ there is a $\sim_{\text{st}}$-corresponding path from $d$.

**Proof.** Let $c_0, c_1, c_2, \ldots$ be a path from $c$. There are two cases:

- if $c_0 \rightarrow \cdots \rightarrow c_n$, $c_n \neq c_i$ and $c_{i+1} = c_i$ for all $i \geq n$, then the statement follows directly from Lemma 2b;
- if $c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots$, then the statement follows directly from Lemma 3.

Now we present the main theorem.

**Theorem 3.** If $c \sim_{\text{st}} d$ then for all $\text{CTL}^*_{\neg X}$ formulas $\varphi$, $c \models \varphi$ iff $d \models \varphi$.

**Proof.** The proof is a slight adaptation of the proof of Theorem 3.2.3 from [14]. Let $c$ and $d$ be two configurations such that $c \sim_{\text{st}} d$. Let $\pi_p$ and $\pi_q$ be two stuttering equivalent paths from $c$ and $d$ respectively. Let $\varphi$ be a $\text{CTL}^*_{\neg X}$ formula. We prove:

1. if $\varphi$ is a state formula, then $c \models \varphi$ iff $d \models \varphi$, and
2. if $\varphi$ is a path formula, then $\pi_p \models \varphi$ iff $\pi_q \models \varphi$.

The proof goes by the structural induction on $\varphi$, first for state and then for path formulas.

1. Suppose $\varphi = \alpha$ for $\alpha \in \text{AP}$. We have $c \models \alpha$ iff $\text{val}_x(\alpha) = \text{true}$ iff $d \models \alpha$.
2. Suppose $\varphi = \neg \varphi'$. We have $c \models \varphi$ iff $c \not\models \varphi'$. From the inductive hypothesis this is equivalent to $d \not\models \varphi'$, which in turn is equivalent to $d \models \varphi$.
3. Suppose $\varphi = \varphi_1 \land \varphi_2$. We have $c \models \varphi$ iff $c \models \varphi_1$ and $c \models \varphi_2$. From the inductive hypothesis this is equivalent to $d \models \varphi_1$ and $d \models \varphi_2$, which is in turn equivalent to $d \models \varphi$.
4. Suppose $\varphi = \exists \psi$. Let $c \models \varphi$. This implies that there is a path $\pi'_p$ from $p$ such that $\pi'_p \models \psi$. Let $\pi'_q$ be a path from $q$ such that $\pi'_p \sim_{\text{st}} \pi'_q$ (from Lemma 5, $\pi'_q$ always exists). From the inductive hypothesis we obtain $\pi_q \models \psi$, which implies $d \models \varphi$.

Symmetrically when $d \models \varphi$.
5. Suppose $\varphi$ is a path formula. We have $\pi_p \models \varphi$ iff $c \models \varphi$. From the inductive hypothesis $c \models \varphi$ is equivalent to $d \models \varphi$, which is in turn equivalent to $\pi_q \models \varphi$.
6. Suppose $\varphi = \neg \psi'$. We have $\pi_p \models \varphi$ iff $\pi_p \not\models \psi'$. From the inductive hypothesis $\pi_p \not\models \psi'$ is equivalent to $\pi_q \not\models \psi'$, which is in turn equivalent to $\pi_q \models \varphi$.
7. Suppose $\varphi = \psi_1 \land \psi_2$. We have $\pi_p \models \varphi$ iff $\pi_p \models \psi_1$ and $\pi_p \models \psi_2$. From the inductive hypothesis this is equivalent to $\pi_q \models \psi_1$ and $\pi_q \models \psi_2$, which is in turn equivalent to $\pi_q \models \varphi$.

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8. Suppose $\phi = \psi_1 U \psi_2$. Let $\pi_p \models \phi$. This implies that there exists $j_p \geq 0$ such that

$$\pi^{j_p}_p \models \psi_2 \text{ and } \pi^{i_p}_p \models \psi_1 \text{ for all } 0 \leq i_p < j_p.$$  

Let $\pi_p$ and $\pi_q$ be partitioned as

$$\pi_p = B^0, B^1, B^2, \ldots \text{ and } \pi_q = C^0, C^1, C^2, \ldots,$$

such that for all $k \geq 0$, $B^K \sim_{st} C^K$. Let $B^K$ be the block that contains the first configuration of $\pi^{j_p}_p$. It is clear that

$$\pi^{j_p}_p \sim_{st} B^K, B^{K+1}, B^{K+2}, \ldots \sim_{st} C^K, C^{K+1}, C^{K+2}, \ldots$$

From $\pi^{j_p}_p \models \psi_2$ and the inductive hypothesis we conclude that

$$C^K, C^{K+1}, C^{K+2}, \ldots \models \psi_2.$$

Let $j_q \geq 0$ be such that $\pi^{j_q}_q = C^K, C^{K+1}, \ldots$ and let $i_q < j_q$ be an arbitrary index. Let $C^L$ be the block that contains the first configuration of $\pi^{j_q}_q$. We have

$$\pi^{i_q}_q \sim_{st} C^L, C^{L+1}, C^{L+2}, \ldots \sim_{st} B^L, B^{L+1}, B^{L+2}, \ldots$$

From $i_q < j_q$ we have $L < K$. This implies

$$B^L, B^{L+1}, B^{L+2}, \ldots \models \psi_1.$$

By the inductive hypothesis, $\pi^{j_q}_q \models \psi_1$.

The proof for the other direction is symmetric.

## 4 Stuttering congruence

First we extend the definition of stuttering bisimilarity to the level of $\chi$ processes.

**Definition 9.** Two processes $p$ and $q$ are stuttering bisimilar, denoted $p \sim_{st} q$, if for all $\sigma \in \Sigma$, $(p, \sigma) \sim_{st} (q, \sigma)$.

To see that stuttering bisimilarity is not a congruence on $\chi$ processes, consider the following example.

**Example 1.** Note that the execution of a send action does not affect the state (see Rule 4 in Table 1), so $a!0 \sim_{st} b0$. However, $a!0 \parallel a?x \not\sim_{st} b0 \parallel a?x$, for the process on the left can do a communication action and change the value of $x$, whereas the process on the right cannot. It follows that $\sim_{st}$ is not a congruence for parallel composition. Also note that $a!0 \sim_{st} skip$, whereas $\partial(a!0) \not\sim_{st} \partial(skip)$ ($\partial(a!0)$ is deadlocked; $\partial(skip)$ does a $\tau$-transition and terminates successfully). So $\sim_{st}$ is not a congruence for encapsulation either.
The example shows that for an equivalence on \( \chi \) processes to be a congruence, it should not be completely action insensitive. We adapt the definition of stuttering bisimilarity in such a way that it distinguishes the send and receive actions from the other actions.

Let \( \mathcal{A}^{\text{com}} \) be the set of all send and receive actions. Let \( e \leftrightarrow e' \) mean that there is an \( a \in \mathcal{A} \setminus \mathcal{A}^{\text{com}} \) such that \( a \overset{a}{\rightarrow} e \). Furthermore, let \( e \overset{a}{(\alpha)} \rightarrow e' \) denote \( e \overset{a}{\rightarrow} e' \) when \( a \in \mathcal{A}^{\text{com}} \), and \( e \leftrightarrow e' \) when \( a \in \mathcal{A} \setminus \mathcal{A}^{\text{com}} \).

**Definition 10.** A symmetric relation \( R \subseteq C \times C \) is an interaction sensitive stuttering bisimulation iff, for all \( (c, d) \in R \), \( c \) and \( d \) have the same state and:

1. if \( c \downarrow \), then there exist \( d_0, \ldots, d_n \in C \) such that
   \[
d_0 = d, \quad d_0 \leftrightarrow \cdots \leftrightarrow d_n, \quad d_n \downarrow \text{ and } cRd_i \text{ for all } i \leq n,
   \]
2. if \( c \overset{a}{\rightarrow} e' \), then there exist \( d_0, \ldots, d_n \in C \) such that
   \[
d_0 = d, \quad d_0 \leftrightarrow \cdots \leftrightarrow d_{n-1} \overset{(\alpha)}{\rightarrow} d_n, \quad cRd_i \text{ for all } i \leq n-1, \text{ and } e' Rd_n
   \]
   (we allow \( n \) to be 0 only if \( a \in \mathcal{A} \setminus \mathcal{A}^{\text{com}} \) and \( c \) and \( e' \) have the same state),
3. if there exists an infinite sequence \( c_0, c_1, c_2, \ldots \in C \) such that
   \[
c_0 = c, \quad c_0 \leftrightarrow c_1 \leftrightarrow c_2 \leftrightarrow \cdots \text{ and } c_i Rd \text{ for all } i \geq 0,
   \]
   then there exist \( d' \in C \) and \( j > 0 \) such that \( d \leftrightarrow d' \) and \( c_j Rd' \).

Two configurations \( c \) and \( d \) are interaction sensitive stuttering bisimilar, denoted \( c \sim_{\text{isst}} d \), if there exists an interaction sensitive stuttering bisimulation \( R \) such that \( cRd \).

**Theorem 4.** Interaction sensitive stuttering bisimilarity is an equivalence.

**Proof.** Reflexivity and symmetry are proved as before. For the transitivity proof, it can be easily seen that Lemmas 1, 2a, and 3 hold when \( \rightarrow \) is replaced by \( \leftrightarrow \). Then, the proof goes similarly as for Theorem 1.

To show that all the previous results hold, we prove the following theorem.

**Theorem 5.** Interaction sensitive stuttering bisimilarity is a stuttering bisimulation.

**Proof.** Suppose that \( c \sim_{\text{isst}} d \) for some \( c, d \in C \). To show that the termination and transfer condition hold is trivial since \( c \leftrightarrow c' \) implies \( c \overset{a}{\rightarrow} c' \) for all \( c, c' \in C \). We verify the divergence condition in detail.

Suppose
\[
c_0 \rightarrow c_1 \rightarrow c_2 \rightarrow \cdots \text{ and } c_i \sim_{\text{isst}} d \text{ for all } i \geq 0.
\]

If every arrow in this sequence is a ‘\( \leftrightarrow \)’ arrow, we have that there exist \( d' \) and \( i > 0 \) such that \( d \leftrightarrow d' \) and \( c_i \sim_{\text{isst}} d' \). In this case we are done because \( d \leftrightarrow d' \) implies \( d \rightarrow d' \).
Suppose now
\[c_0 \looparrowright \cdots \looparrowright c_n \xrightarrow{a} c_{n+1}\] and \(a \in A^{com}\).

Since \(c_n \sim_{\text{iss}} d\), there exist \(d_0, \ldots, d_m\) such that \(d_0 = d\),
\[d_0 \looparrowright \cdots \looparrowright d_{m-1} \xrightarrow{a} d_{m}, c_n \sim_{\text{iss}} d_i\] for all \(i \leq m - 1\) and \(c_{n+1} \sim_{\text{iss}} d_m\).

Because \(a \in A^{com}\), \(m > 0\). Then, \(c_n \sim_{\text{iss}} d_1\) or \(c_{n+1} \sim_{\text{iss}} d_1\). If \(c_n \sim_{\text{iss}} d_1\) and \(n = 0\) then, since \(c_0 \sim_{\text{iss}} d_0\), \(c_1 \sim_{\text{iss}} d_0\) and \(\sim_{\text{iss}}\) is symmetric and transitive, we conclude that \(c_1 \sim_{\text{iss}} d_1\).

Interaction sensitive stuttering bisimulation is not a congruence for alternative composition.

Example 2. Note that \(\text{skip} \sim_{\text{iss}} \text{skip}\) and \(\delta \sim_{\text{iss}} \text{skip} : \delta\). However, \(\text{skip} [] \delta \not\sim_{\text{iss}} \text{skip} [] \delta\); for the right-hand side process can execute \(\text{skip}\) and then deadlock, while the left-hand side process never deadlocks. So, interaction sensitive stuttering bisimulation is not a congruence for alternative composition.

Both the problem illustrated in the above example, and its solution are well known; we need to add a root condition.

**Definition 11.** Two configurations \(c\) and \(d\) are rooted interaction sensitive stuttering bisimilar (notation: \(c \sim_{\text{riss}} d\)) iff

1. \(c \Downarrow\) iff \(d \Downarrow\)
2. if \(c \xrightarrow{a} c'\), then there exists \(d' \in C\) such that \(d \xrightarrow{(a)} d'\) and \(c' \sim_{\text{iss}} d'\),
3. if \(d \xrightarrow{a} d'\), then there exists \(c' \in C\) such that \(c \xrightarrow{(a)} c'\) and \(c' \sim_{\text{iss}} d'\),

It is clear that \(c \sim_{\text{riss}} d\) implies \(c \sim_{\text{iss}} d\). Also, that rooted interaction sensitive stuttering bisimilarity is an equivalence relation is easily proved. The notion induces a congruence on \(\chi\) processes.

**Definition 12.** Two processes \(p\) and \(q\) are stuttering congruent, denoted \(p \equiv_{\text{st}} q\), if \((p, \sigma) \sim_{\text{riss}} (q, \sigma)\) for all \(\sigma \in \Sigma\).

Clearly, \(\equiv_{\text{st}}\) is an equivalence relation and \(p \equiv_{\text{st}} q\) implies \(p \sim_{\text{st}} q\), and it is a congruence for the constructs of \(\chi\).

**Theorem 6.** For all \(p, q, \overline{p}, \overline{q} \in P\), if \(p \equiv_{\text{st}} q\) and \(\overline{p} \equiv_{\text{st}} \overline{q}\), then:

1. \(b :: p \equiv_{\text{st}} b :: q\),
2. \(p ; \overline{p} \equiv_{\text{st}} q ; \overline{q}\),
3. \(p \parallel \overline{p} \equiv_{\text{st}} q \parallel \overline{q}\),
4. \(p^* \equiv_{\text{st}} q^*\),
5. \(p \parallel \overline{p} \equiv_{\text{st}} q \parallel \overline{q}\),
6. \([s | p] \equiv_{\text{st}} [s | q]\) for all states \(s\),
7. \(p \parallel \overline{p} \equiv_{\text{st}} q \parallel \overline{q}\),
8. \(\partial(p) \equiv_{\text{st}} \partial(q)\).
Proof. For each case we only check the conditions of rooted interaction sensitive stuttering bisimilarity in one direction since the other direction is symmetric.

1. To prove the first condition, suppose \( \langle b \mapsto p, \sigma \rangle \downarrow \). Note that Rule 6 in Table 2 is the final rule of any derivation with \( \langle b \mapsto p, \sigma \rangle \downarrow \) as conclusion, so it holds that \( \sigma(b) = \text{true} \) and \( \langle p, \sigma \rangle \downarrow \). Hence, since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \), it follows that \( \langle q, \sigma \rangle \downarrow \), so \( \langle b \mapsto q, \sigma \rangle \downarrow \) by Rule 6. Similarly, from \( \langle b \mapsto q, \sigma \rangle \downarrow \) one can prove that \( \langle b \mapsto p, \sigma \rangle \downarrow \).

To prove the second condition, suppose \( \langle b \mapsto p, \sigma \rangle \xrightarrow{a} \langle r, \sigma' \rangle \). Since Rule 7 must be the final rule of any derivation of this transition, it holds that \( \sigma(b) = \text{true} \), \( \langle p, \sigma \rangle \xrightarrow{a} \langle p', \sigma' \rangle \) and \( r = p' \). Since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \xrightarrow{(a)} \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \cong_{st} \langle q', \sigma' \rangle \), and hence, by Rule 7, \( \langle b \mapsto q, \sigma \rangle \xrightarrow{(a)} \langle q', \sigma' \rangle \).

2. To prove the first condition, suppose \( \langle p ; \overline{p}, \sigma \rangle \downarrow \). Since in any derivation with \( \langle p ; \overline{p}, \sigma \rangle \downarrow \) as conclusion Rule 8 is the final rule applied, it follows that \( \langle p, \sigma \rangle \downarrow \) and \( \langle \overline{p}, \sigma \rangle \downarrow \). Since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \) and \( \langle \overline{p}, \sigma \rangle \cong_{st} \langle \overline{q}, \sigma \rangle \), also \( \langle q, \sigma \rangle \downarrow \) and \( \langle \overline{q}, \sigma \rangle \downarrow \), and hence, by Rule 8, \( \langle q ; \overline{q}, \sigma \rangle \downarrow \).

To prove the second condition, suppose \( \langle p ; \overline{p}, \sigma \rangle \xrightarrow{a} \langle r, \sigma' \rangle \). Note that the final rule of a derivation with this transition as conclusion is either Rule 9 or Rule 10; we treat these cases separately.

If the final rule applied is Rule 9, then it holds that \( \langle p, \sigma \rangle \downarrow \), \( \langle \overline{p}, \sigma \rangle \xrightarrow{a} \langle \overline{p}', \sigma' \rangle \) and \( r = \overline{p}' \). Since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \), we have \( \langle q, \sigma \rangle \downarrow \). Moreover, since \( \langle \overline{p}, \sigma \rangle \cong_{st} \langle \overline{q}, \sigma \rangle \), there exists \( \overline{q} \) such that \( \langle \overline{q}, \sigma \rangle \xrightarrow{(a)} \langle \overline{q}', \sigma' \rangle \) and \( \langle \overline{p}', \sigma' \rangle \cong_{st} \langle \overline{q}', \sigma' \rangle \). So, by Rule 9, \( \langle q ; \overline{q}, \sigma \rangle \xrightarrow{(a)} \langle \overline{q}', \sigma' \rangle \).

If the final rule applied is Rule 10, then it holds that \( \langle p, \sigma \rangle \xrightarrow{a} \langle p', \sigma' \rangle \) and \( r = p' ; \overline{p} \). Since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \xrightarrow{(a)} \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \cong_{st} \langle q', \sigma' \rangle \), so, according to Rule 10, \( \langle q ; \overline{q}, \sigma \rangle \xrightarrow{(a)} \langle q' ; \overline{q}, \sigma' \rangle \).

It remains to establish that \( \langle p' ; \overline{p}, \sigma' \rangle \cong_{st} \langle q' ; \overline{q}, \sigma' \rangle \), and we do this by showing that the binary relation \( R \) on configurations defined by

\[
R = \cong_{st} \cup \{ \langle r ; \overline{p}, \sigma \rangle, \langle s ; \overline{q}, \sigma \rangle \mid \langle r, \sigma \rangle \cong_{st} \langle s, \sigma \rangle \}
\]

is an interaction sensitive stuttering bisimulation. Since \( \cong_{st} \) is an interaction sensitive stuttering bisimulation, \( R \) is symmetric and we only need to verify the conditions for pairs \( \langle r ; \overline{p}, \sigma \rangle, \langle s ; \overline{q}, \sigma \rangle \) with \( \langle r, \sigma \rangle \cong_{st} \langle s, \sigma \rangle \).

For the termination condition, suppose \( \langle r ; \overline{p}, \sigma \rangle \downarrow \). Since Rule 8 is the final rule applied in any derivation of \( \langle r ; \overline{p}, \sigma \rangle \downarrow \), it holds that \( \langle r, \sigma \rangle \downarrow \) and \( \langle \overline{p}, \sigma \rangle \downarrow \). Since \( \langle r, \sigma \rangle \cong_{st} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \) and

\[
\langle s_0, \sigma \rangle \xrightarrow{\cdots} \langle s_i, \sigma \rangle, \langle s_i, \sigma \rangle \downarrow \text{ and } \langle r, \sigma \rangle \cong_{st} \langle s_i, \sigma \rangle \text{ for all } i \leq n.
\]

So by Rule 10, \( \langle s_0 ; \overline{q}, \sigma \rangle \xrightarrow{\cdots} \langle s_i ; \overline{q}, \sigma \rangle \). Furthermore, since \( \langle \overline{p}, \sigma \rangle \cong_{st} \langle \overline{q}, \sigma \rangle \) implies that also \( \langle \overline{q}, \sigma \rangle \downarrow \), its follows by Rule 8 that \( \langle s_i ; \overline{q}, \sigma \rangle \downarrow \), and finally, according to the definition of \( R \), \( \langle r ; \overline{p}, \sigma \rangle \xrightarrow{R} \langle s_i ; \overline{q}, \sigma \rangle \) for all \( i \leq n \).
For the transfer condition, suppose \( \langle r ; p, σ \rangle \xrightarrow{a} \langle t, σ' \rangle \). As before, the final rule of a derivation with this transition as conclusion is either Rule 9 or Rule 10 and we treat these cases separately.

If the final rule applied is Rule 9, then it holds that \( \langle r, σ \rangle \downarrow, \langle p, σ \rangle \xrightarrow{a} \langle p', σ' \rangle \) and \( t = p' \). Since \( \langle r, σ \rangle \sim \text{last} \langle s, σ \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, σ \rangle \xrightarrow{\ldots} \langle s_n, σ \rangle \downarrow \quad \text{and} \quad \langle r, σ \rangle \sim \text{last} \langle s_i, σ \rangle
\]

for all \( 0 ≤ i ≤ n \). So, by Rule 10,

\[
\langle s_0 ; \overline{q}, σ \rangle \xrightarrow{\ldots} \langle s_n ; \overline{q}, σ \rangle.
\]

Furthermore, since \( \langle p, σ \rangle \xrightarrow{a} \langle \overline{q}, σ \rangle \), there exists \( \overline{q}' \) such that \( \langle q, σ \rangle \xrightarrow{a} \langle \overline{q}', σ' \rangle \) and \( \langle p', σ' \rangle \sim \text{last} \langle \overline{q}', σ' \rangle \). Now, by Rule 9, \( \langle s_n ; \overline{q}, σ \rangle \xrightarrow{a} \langle \overline{q}', σ' \rangle \). Finally, according to the definition of \( R \), \( \langle r ; p, σ \rangle R \langle s_i ; \overline{q}, σ \rangle \) for all \( i ≤ n \), and \( \langle t, σ \rangle R \langle \overline{q}', σ \rangle \).

If the final rule applied is Rule 10, then it holds that \( \langle r, σ \rangle \xrightarrow{a} \langle r', σ' \rangle \) and \( t = r' ; p \). Since \( \langle r, σ \rangle \sim \text{last} \langle s, σ \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, σ \rangle \xrightarrow{\ldots} \langle s_n ; \overline{q}, σ \rangle \xrightarrow{a} \langle s_n, σ' \rangle, \quad \langle r, σ \rangle \sim \text{last} \langle s_i, σ \rangle
\]

for all \( 0 ≤ n - 1 \), and \( \langle r', σ' \rangle \sim \text{last} \langle s_n, σ' \rangle \). So, by Rule 10,

\[
\langle s_0 ; \overline{q}, σ \rangle \xrightarrow{\ldots} \langle s_n ; \overline{q}, σ \rangle \xrightarrow{a} \langle s_n, σ' \rangle, \quad \langle r, σ \rangle \sim \text{last} \langle s_i, σ \rangle
\]

and, according to the definition of \( R \), \( \langle r ; p, σ \rangle R \langle s_i ; \overline{q}, σ \rangle \) for all \( i ≤ n - 1 \), and \( \langle t, σ \rangle R \langle s_n ; \overline{q}, σ \rangle \).

To prove the divergence condition, suppose \( t_0 = r ; p \),

\[
\langle t_0, σ \rangle \xrightarrow{\ldots} \langle t_1, σ \rangle \xrightarrow{\ldots} \langle t_2, σ \rangle \xrightarrow{\ldots}
\]

and \( \langle t_i, σ \rangle \sim \text{last} \langle s, σ \rangle \) for all \( i ≥ 0 \). From the definition of \( R \) it follows that there exist \( r_0, r_1, r_2, \ldots \) such that \( r_0 = r, t_i = r_i ; p \) and \( \langle r_i, σ \rangle \sim \text{last} \langle s, σ \rangle \) for all \( i ≥ 0 \). Rules 9 and 10 are the only final rules of a derivation with \( \langle t_1, σ \rangle \xrightarrow{\ldots} \langle t_i, σ \rangle \) as conclusion so we conclude that either \( \langle r_i, σ \rangle \xrightarrow{\ldots} \langle r_{i+1}, σ \rangle \) or \( \langle r_i, σ \rangle \xrightarrow{\ldots} \langle r_{i+1}, σ \rangle \) and \( \langle p, σ \rangle \xrightarrow{\ldots} \langle p_{i+1}, σ \rangle \).

Suppose first that \( \langle r_i, σ \rangle \xrightarrow{\ldots} \langle r_{i+1}, σ \rangle \) for all \( i ≥ 0 \) or i.e. that \( \langle r_0, σ \rangle \xrightarrow{\ldots} \langle r_1, σ \rangle \xrightarrow{\ldots} \langle r_2, σ \rangle \xrightarrow{\ldots} \). Since \( \langle r, σ \rangle \sim \text{last} \langle s, σ \rangle \), there exist \( s' \) and \( j > 0 \) such that \( \langle s, σ \rangle \xrightarrow{\ldots} \langle s', σ \rangle \) and \( \langle r_j, σ \rangle \sim \text{last} \langle s', σ \rangle \). So by Rule 10, \( \langle s ; \overline{q}, σ \rangle \xrightarrow{\ldots} \langle s' ; \overline{q}, σ \rangle \). According to the definition of \( R \), \( \langle r_j ; p, σ \rangle R \langle s' ; \overline{q}, σ \rangle \).

Suppose now that \( \langle r_0, σ \rangle \xrightarrow{\ldots} \langle r_n, σ \rangle \) \xrightarrow{\ldots} \langle r_n, σ \rangle \downarrow \) and \( \langle p, σ \rangle \xrightarrow{\ldots} \langle p_{n+1}, σ \rangle \). Since \( \langle r_n, σ \rangle \sim \text{last} \langle s, σ \rangle \), there exist \( s_0, \ldots, s_m \) such that \( s_0 = s \),

\[
\langle s_0, σ \rangle \xrightarrow{\ldots} \langle s_m, σ \rangle \xrightarrow{\ldots} \langle s_m, σ \rangle \downarrow
\]

and \( \langle r_n, σ \rangle \sim \text{last} \langle s_i, σ \rangle \) for all \( i ≤ m \). So by Rule 10,

\[
\langle s_0 ; \overline{q}, σ \rangle \xrightarrow{\ldots} \langle s_m ; \overline{q}, σ \rangle.
\]
Furthermore, since \( \langle p, \sigma \rangle \sim_s \langle \overline{p}, \sigma \rangle \), there exists \( \overline{q} \) such that \( \langle q, \sigma \rangle \sim_q \langle \overline{q}, \sigma \rangle \) and \( \langle r_n+1 : p, \sigma \rangle \sim_{\text{st}} \langle \overline{q}, \sigma \rangle \), and hence, by Rule 9, \( \langle s_n : p, \sigma \rangle \sim_q \langle \overline{q}, \sigma \rangle \). We now distinguish three cases and show for each that the divergence condition is satisfied. If \( m > 0 \) and \( n > 0 \), then, \( \langle r_n : p, \sigma \rangle \sim_{\text{st}} \langle s_1 : \overline{p}, \sigma \rangle \). If \( m > 0 \) but \( n = 0 \), using that \( \langle r_0, \sigma \rangle \sim_{\text{st}} \langle s_0, \sigma \rangle \), \( \langle r_0, \sigma \rangle \sim_{\text{st}} \langle s_1, \sigma \rangle \), \( \langle r_1, \sigma \rangle \sim_{\text{st}} \langle s_0, \sigma \rangle \), and that \( \sim_{\text{st}} \) is transitive, we conclude that \( \langle r_1, \sigma \rangle \sim_{\text{st}} \langle s_1, \sigma \rangle \), and hence, that \( \langle r_1 : p, \sigma \rangle \sim_{\text{st}} \langle s_1 : \overline{p}, \sigma \rangle \). The last case is when \( m = 0 \). In this case, \( \langle r_{n+1} : p, \sigma \rangle \sim_{\text{st}} \langle \overline{q}, \sigma \rangle \).

Since \( R \) satisfies all the conditions from Definition 1, we conclude that \( R \) is an interaction sensitive stuttering bisimulation.

3. To prove the first condition, suppose \( \langle p \parallel p, \sigma \rangle \). As Rule 11 is the final rule of any derivation with this as conclusion, it holds that \( \langle p, \sigma \rangle \) or \( \langle p, \sigma \rangle \). We only consider the first case: when \( \langle p, \sigma \rangle \) the proof is similar. Since \( \langle p, \sigma \rangle \equiv_s \langle q, \sigma \rangle \), it follows that \( \langle q, \sigma \rangle \), and hence, by Rule 11, that \( \langle q, \sigma \rangle \).

To prove the second condition, suppose \( \langle p \parallel p, \sigma \rangle \sim_q \langle r, \sigma' \rangle \). Since Rule 12 must be the final Rule of any derivation of this transition, it holds that \( \langle p, \sigma \rangle \sim_q \langle p', \sigma' \rangle \) and \( r = p' \) or that \( \langle p, \sigma \rangle \sim_q \langle \overline{q}, \sigma' \rangle \) and \( r = \overline{p} \). Suppose \( \langle p, \sigma \rangle \sim_q \langle p', \sigma' \rangle \) (the other case is symmetric). Since \( \langle p, \sigma \rangle \equiv_s \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \rightarrow (\alpha) \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \equiv_s \langle q', \sigma' \rangle \). Now, by Rule 12, \( \langle q \parallel \overline{q}, \sigma \rangle \rightarrow (\alpha) \langle q', \sigma' \rangle \).

4. By Rule 13, the first condition of rooted interaction sensitive stuttering bisimilarity holds trivially. For the second condition, suppose \( \langle p^*, \sigma \rangle \sim_q \langle r, \sigma' \rangle \).

Since in any derivation with this transition Rule 14 is the final rule applied, it follows that \( \langle p, \sigma \rangle \rightarrow \langle p', \sigma' \rangle \) and \( r = p' : p^* \). Since \( \langle p, \sigma \rangle \equiv_s \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \rightarrow (\alpha) \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \sim_{\text{st}} \langle q', \sigma' \rangle \). By Rule 14, \( \langle q', \sigma \rangle \rightarrow (\alpha) \langle q', \sigma' \rangle \). It remains to establish that \( \langle r, \sigma' \rangle \sim_{\text{st}} \langle q', \sigma' \rangle \).

We do this by showing that a symmetric relation

\[ R = \{ (\langle r : p^*, \sigma \rangle, \langle s : q^*, \sigma \rangle) | \langle r, \sigma \rangle \sim_{\text{st}} \langle s, \sigma \rangle \} \]

is a interaction sensitive stuttering bisimulation.

To prove the termination condition, suppose \( \langle r : p^*, \sigma \rangle \). Since Rule 8 is the final rule applied in any derivation of \( \langle r : p^*, \sigma \rangle \), and since \( \langle p^*, \sigma \rangle \) by Rule 13, it holds that \( \langle r, \sigma \rangle \). Since \( \langle r, \sigma \rangle \sim_{\text{st}} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[ \langle s_0, \sigma \rangle \sim \cdots \sim \langle s_n, \sigma \rangle, \langle s_n, \sigma \rangle \]

and \( \langle r, \sigma \rangle \sim_{\text{st}} \langle s_i, \sigma \rangle \) for all \( i \leq n \). By applying Rule 10 now, we obtain

\[ \langle s_0 : q^*, \sigma \rangle \sim \cdots \sim \langle s_n : q^*, \sigma \rangle, \]

and hence, by Rules 8 and 13 again, \( \langle s_n : q^*, \sigma \rangle \). According to the definition of \( R \), \( \langle r : p^*, \sigma \rangle \) for all \( i \leq n \).

To prove the transfer condition, suppose \( \langle r : p^*, \sigma \rangle \rightarrow (t, \sigma') \). Note that the final rule of a derivation with this transition as conclusion is either Rule 9 or Rule 10; we treat these cases separately.
If the final rule applied is Rule 9, then it holds that \( \langle r, \sigma \rangle \Downarrow \) and \( \langle p^*, \sigma \rangle \overset{a}{\rightarrow} \langle t, \sigma' \rangle \). Since \( \langle r, \sigma \rangle \sim_{\text{isst}} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n, \sigma \rangle \Downarrow \langle s, \sigma \rangle,
\]

and \( \langle r, \sigma \rangle \sim_{\text{isst}} \langle s_i, \sigma \rangle \) for all i ≤ n. Applying Rule 10,

\[
\langle s_0 : q^*, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n : q^*, \sigma \rangle.
\]

Furthermore, since Rule 14 is the final rule of a derivation with \( \langle p^*, \sigma \rangle \overset{a}{\rightarrow} \langle t, \sigma' \rangle \) as conclusion, we have \( \langle p, \sigma \rangle \overset{a}{\rightarrow} \langle p', \sigma' \rangle \) and \( t = p' : p^* \). Since \( \langle p, \sigma \rangle \equiv_{\text{st}} \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \overset{(a)}{\rightarrow} \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \sim_{\text{isst}} \langle q', \sigma' \rangle \). Applying Rule 14, we obtain \( \langle q^*, \sigma \rangle \overset{(a)}{\rightarrow} \langle q', \sigma' \rangle \). Since \( \langle s_n, \sigma \rangle \Downarrow \), by Rule 9, we have \( \langle s_n : q^*, \sigma \rangle \overset{(a)}{\rightarrow} \langle q', \sigma' \rangle \). Finally, according to the definition of \( R \), \( \langle r ; p^*, \sigma \rangle R \langle s_i ; q^*, \sigma \rangle \) for all \( i \leq n \), and \( \langle t, \sigma' \rangle R \langle q', \sigma' \rangle \).

If the final rule applied is Rule 10, then it holds that \( \langle r, \sigma \rangle \overset{a}{\rightarrow} \langle r', \sigma' \rangle \) and \( t = r' : p^* \). Since \( \langle r, \sigma \rangle \sim_{\text{isst}} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_{n-1}, \sigma \rangle \overset{(a)}{\rightarrow} \langle s_n, \sigma' \rangle,
\]

\( \langle r, \sigma \rangle \sim_{\text{isst}} \langle s_i, \sigma \rangle \) for all \( i \leq n - 1 \), and \( \langle r', \sigma' \rangle \sim_{\text{isst}} \langle s_n, \sigma' \rangle \). Now, applying Rule 6, we obtain

\[
\langle s_0 ; q^*, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_{n-1} ; q^*, \sigma \rangle \overset{(a)}{\rightarrow} \langle s_n ; q^*, \sigma' \rangle,
\]

and according to the definition of \( R \), \( \langle r ; p^*, \sigma \rangle R \langle s_i ; q^*, \sigma \rangle \) for all \( i \leq n - 1 \), and \( \langle t, \sigma' \rangle R \langle s_n ; q^*, \sigma' \rangle \).

Since the divergence condition can be proved similarly as in the case of the sequential operator, we conclude that \( R \) is an interaction sensitive stuttering bisimulation.

5. To prove the first condition, suppose \( \langle [s \mid p], \sigma \rangle \Downarrow \). Since in any derivation with this as conclusion Rule 18 is the final rule applied, we have \( \langle p, \gamma(s, \sigma) \rangle \Downarrow \). Since \( \langle p, \gamma(s, \sigma) \rangle \Downarrow \equiv_{\text{st}} \langle q, \gamma(s, \sigma) \rangle \Downarrow \), it follows that \( \langle q, \gamma(s, \sigma) \rangle \Downarrow \), and hence, by Rule 18, \( \langle [s \mid q], \sigma \rangle \Downarrow \).

To prove the second condition, suppose \( \langle [s \mid p], \sigma \rangle \overset{a}{\rightarrow} \langle r, \sigma'' \rangle \). Since Rule 19 is the final rule of any derivation with this transition as conclusion, it holds that \( \langle p, \gamma(s, \sigma) \rangle \overset{a}{\rightarrow} \langle p', \sigma' \rangle \), \( \sigma'' = \gamma(\sigma' \uparrow \text{dom}(\sigma) \setminus \text{dom}(s), \sigma) \), \( r = [s' \mid p'] \) and \( s' = \sigma' \uparrow \text{dom}(s) \). Since \( \langle p, \gamma(s, \sigma) \rangle \Downarrow \equiv_{\text{st}} \langle q, \gamma(s, \sigma) \rangle \), there exists \( q' \) such that \( \langle q, \gamma(s, \sigma) \rangle \overset{(a)}{\rightarrow} \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \sim_{\text{isst}} \langle q', \sigma' \rangle \). Hence, by Rule 19, we obtain \( \langle [s' \mid q'], \sigma'' \rangle \). It is not hard to show that

\[
\gamma(\sigma' \uparrow \text{dom}(s), \gamma(\sigma' \uparrow \text{dom}(\sigma) \setminus \text{dom}(s), \sigma)) = \sigma'
\]

so, to prove that \( \langle [s' \mid p'], \sigma'' \rangle \sim_{\text{isst}} \langle [s' \mid q'], \sigma'' \rangle \), it is enough to show that a symmetric binary relation

\[
R = \{ (\langle [st \mid r], \sigma \rangle, \langle [st \mid s], \sigma \rangle) \mid st \in \Sigma, \langle r, \gamma(st, \sigma) \rangle \sim_{\text{st}} \langle s, \gamma(st, \sigma) \rangle \}\}
\]

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is an interaction sensitive stuttering bisimulation.

To prove the termination condition, suppose \( \langle s, \gamma(st, \sigma) \rangle \). Since Rule 18 is the final rule of any derivation with this as conclusion, it holds that \( \langle r, \gamma(st, \sigma) \rangle \).

Since \( \langle r, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle s, \gamma(st, \sigma) \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \gamma(st, \sigma) \rangle \hookrightarrow \cdots \hookrightarrow \langle s_n, \gamma(st, \sigma) \rangle, \quad \langle s_n, \gamma(st, \sigma) \rangle \downarrow,
\]

and \( \langle r, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle s_i, \gamma(st, \sigma) \rangle \) for all \( i \leq n \). Hence, by Rule 18, \( \langle st \mid s_n \rangle, \sigma \rangle \).

Using that \( \gamma(st, \sigma) \mid \text{dom}(st) = st \) and \( \gamma(st, \sigma) \downarrow \text{dom}(\sigma) \text{dom}(s), \sigma = \sigma \), by Rule 19, we obtain

\[
\langle \langle st \mid s_0 \rangle, \sigma \rangle \hookrightarrow \cdots \hookrightarrow \langle \langle st \mid s_n \rangle, \sigma \rangle.
\]

Finally, according to the definition of \( R \), \( \langle \langle st \mid r \rangle, \sigma \rangle \sim_R \langle \langle st \mid s_i \rangle, \sigma \rangle \) for all \( i \leq n \).

To prove the transfer condition, suppose \( \langle \langle st \mid r \rangle, \sigma \rangle \sim_R \langle t, \sigma' \rangle \). Since Rule 19 is the final rule with this transition as conclusion, we have \( \langle r, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle r', \sigma' \rangle \), \( \gamma'(\sigma, \sigma') \downarrow \text{dom}(\sigma) \text{dom}(st) \), \( t = \langle st' \mid r' \rangle \), and \( st' = \sigma' \downarrow \text{dom}(st) \).

Since \( \langle r, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle s, \gamma(st, \sigma) \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \gamma(st, \sigma) \rangle \hookrightarrow \cdots \hookrightarrow \langle s_n-1, \gamma(st, \sigma) \rangle \quad \xrightarrow{(a)} \quad \langle s_n, \sigma' \rangle,
\]

\( \langle r, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle s_i, \gamma(st, \sigma) \rangle \) for all \( i \leq n \), and \( \langle r', \sigma' \rangle \sim_{\text{isst}} \langle s_n, \sigma' \rangle \). Now, applying Rule 19,

\[
\langle \langle st \mid s_0 \rangle, \sigma \rangle \hookrightarrow \cdots \hookrightarrow \langle \langle st \mid s_n-1 \rangle, \sigma \rangle \quad \xrightarrow{(a)} \quad \langle \langle st' \mid s_n \rangle, \sigma' \rangle.
\]

According to the definition of \( R \) it follows that \( \langle \langle st \mid r \rangle, \sigma \rangle \sim_R \langle \langle st \mid s_i \rangle, \sigma \rangle \) for all \( i \leq n \), and \( \langle \langle st' \mid r' \rangle, \sigma \rangle \sim_R \langle \langle st' \mid s_n \rangle, \sigma \rangle \).

To prove the divergence condition, suppose \( t_0 = \langle st \mid r \rangle, \langle t_0, \sigma \rangle \hookrightarrow \langle t_1, \sigma \rangle \hookrightarrow \cdots \hookrightarrow \langle \langle st \mid s \rangle, \sigma \rangle \sim_R \langle t_i, \sigma \rangle \) for all \( i \geq 0 \). Since Rule 19 is the final rule with \( \langle t_i, \sigma \rangle \hookrightarrow \langle t_{i+1}, \sigma \rangle \) as conclusion, we have \( r_0 = r \),

\[
\langle r_0, \gamma(st, \sigma) \rangle \hookrightarrow \langle r_1, \gamma(st, \sigma) \rangle \hookrightarrow \langle r_2, \gamma(st, \sigma) \rangle \hookrightarrow \cdots .
\]

According to the definition of \( R \), we also have \( \langle r_i, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle s, \gamma(st, \sigma) \rangle \) for all \( i \geq 0 \). From the divergence condition of \( \sim_{\text{isst}} \) it follows that there exist \( s' \) and \( j > 0 \) such that \( \langle s, \gamma(st, \sigma) \rangle \hookrightarrow \langle s', \gamma(st, \sigma) \rangle \) and \( \langle r_j, \gamma(st, \sigma) \rangle \sim_{\text{isst}} \langle s', \gamma(st, \sigma) \rangle \), and hence, by Rule 19, \( \langle st \mid s \rangle \hookrightarrow \langle st \mid s' \rangle \). According to the definition of \( R \), \( \langle \langle st \mid r_j \rangle, \sigma \rangle \sim_R \langle \langle st \mid s' \rangle, \sigma \rangle \).

We conclude that \( R \) is an interaction sensitive stuttering bisimulation.

6. To prove the first condition, suppose \( \langle p \parallel \overline{p}, \sigma \rangle \downarrow \). Since Rule 15 is the final rule with this as conclusion, we have \( \langle p, \sigma \rangle \downarrow \) and \( \langle \overline{p}, \sigma \rangle \downarrow \). Since \( \langle p, \sigma \rangle \equiv_{st} \langle q, \sigma \rangle \) and \( \langle \overline{p}, \sigma \rangle \equiv_{st} \langle \overline{q}, \sigma \rangle \), we obtain \( \langle q, \sigma \rangle \downarrow \) and \( \langle \overline{q}, \sigma \rangle \downarrow \), and hence, by Rule 15 again, \( \langle q \parallel \overline{q}, \sigma \rangle \downarrow \).

To prove the second condition, suppose \( \langle p \parallel \overline{p}, \sigma \rangle \sim_R \langle r, \sigma' \rangle \). The final rule of a derivation with this transition as conclusion is either Rule 16 or Rule 17; we treat these cases separately.
If the final rule applied is Rule 16, then \( \langle p, \sigma \rangle \xrightarrow{a} \langle p', \sigma' \rangle \) and \( t = p' \parallel \overline{p} \) (or symmetrically \( \langle \overline{p}, \sigma \rangle \xrightarrow{a} \langle \overline{p'}, \sigma' \rangle \) and \( t = p \parallel \overline{p'} \)). Since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \xrightarrow{(a)} \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \sim_{rast} \langle q', \sigma' \rangle \). Hence, by Rule 16, \( \langle q \parallel \overline{q}, \sigma \rangle \xrightarrow{(a)} \langle q' \parallel \overline{q'}, \sigma' \rangle \).

If the final rule applied is Rule 17, we have that

\[
\langle p, \sigma \rangle \xrightarrow{sa(m,c)} \langle p', \sigma \rangle \text{ and } \langle \overline{p}, \sigma \rangle \xrightarrow{ra(m,c)} \langle \overline{p'}, \sigma' \rangle,
\]

with \( a = ca(m, c) \) and \( t = p' \parallel \overline{p'} \). Since \( \langle p, \sigma \rangle \cong_{st} \langle q, \sigma \rangle \) and \( \langle \overline{p}, \sigma \rangle \cong_{st} \langle \overline{q}, \sigma \rangle \), there exist \( q' \) and \( \overline{q}' \) such that

\[
\langle q, \sigma \rangle \xrightarrow{sa(m,c)} \langle q', \sigma \rangle \text{ and } \langle \overline{q}, \sigma \rangle \xrightarrow{ra(m,c)} \langle \overline{q}', \sigma' \rangle,
\]

and \( \langle p', \sigma \rangle \sim_{rast} \langle q', \sigma \rangle \) and \( \langle \overline{p'}, \sigma' \rangle \sim_{rast} \langle \overline{q}', \sigma' \rangle \). Now, by Rule 17,

\[
\langle q \parallel \overline{q}, \sigma \rangle \xrightarrow{ca(m,c)} \langle q' \parallel \overline{q}', \sigma' \rangle.
\]

It remains to show that \( \langle p' \parallel \overline{p'}, \sigma' \rangle \sim_{rast} \langle q' \parallel \overline{q}', \sigma' \rangle \), and for this it is enough to show that the symmetric relation

\[
R = \{ \langle (r \parallel \overline{r}, \sigma), (s \parallel \overline{s}, \sigma) \rangle \mid \langle r, \sigma \rangle \sim_{rast} \langle s, \sigma \rangle, \langle \overline{r}, \sigma \rangle \sim_{rast} \langle \overline{s}, \sigma \rangle \}
\]

is an interaction sensitive stuttering bisimulation.

To prove the termination condition, suppose \( \langle r \parallel \overline{r}, \sigma \rangle \downarrow \). Since Rule 15 is the final rule with this as conclusion, we have \( \langle r, \sigma \rangle \downarrow \) and \( \langle \overline{r}, \sigma \rangle \downarrow \). Since \( \langle r, \sigma \rangle \sim_{rast} \langle s, \sigma \rangle \) there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n, \sigma \rangle, \langle s_n, \sigma \rangle \downarrow,
\]

and \( \langle r, \sigma \rangle \sim_{rast} \langle s_i, \sigma \rangle \) for all \( i \leq n \). Since \( \langle \overline{r}, \sigma \rangle \sim_{rast} \langle \overline{s}, \sigma \rangle \), there exist \( \overline{s}_0, \ldots, \overline{s}_m \) such that \( \overline{s}_0 = \overline{s} \),

\[
\langle \overline{s}_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle \overline{s}_m, \sigma \rangle, \langle \overline{s}_m, \sigma \rangle \downarrow,
\]

and \( \langle \overline{r}, \sigma \rangle \sim_{rast} \langle \overline{s}_i, \sigma \rangle \) for all \( i \leq m \). By Rule 16,

\[
\langle s_0 \parallel \overline{s}_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n \parallel \overline{s}_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n \parallel \overline{s}_m, \sigma \rangle,
\]

and by Rule 15 \( \langle s_n \parallel \overline{s}_m, \sigma \rangle \downarrow \). According to the definition of \( R \), it holds that

\[
\langle r \parallel \overline{r}, \sigma \rangle \xrightarrow{a} \langle t, \sigma \rangle \quad \text{for all } i \leq n, j \leq m.
\]

To prove the transfer condition, suppose \( \langle r \parallel \overline{r}, \sigma \rangle \xrightarrow{a} \langle t', \sigma' \rangle \). The final rule of a derivation with this transition as conclusion is either Rule 16 or Rule 17; we treat these cases separately.

If the final rule applied is Rule 16, then \( \langle r, \sigma \rangle \xrightarrow{a} \langle r', \sigma \rangle \) and \( t = r' \parallel \overline{r} \) (or, symmetrically \( \langle \overline{r}, \sigma \rangle \xrightarrow{a} \langle \overline{r'}, \sigma' \rangle \) and \( t = r \parallel \overline{r'} \)). Since \( \langle r, \sigma \rangle \sim_{rast} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_{n-1}, \sigma \rangle \xrightarrow{(a)} \langle s_n, \sigma' \rangle,
\]

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\(<r, \sigma> \sim_{\text{test}} (s_i, \sigma)\) for all \(i \leq n - 1\), and \(<r', \sigma'> \sim_{\text{test}} (s_n, \sigma')\). Applying Rule 16,
\[
\langle s_0 \parallel \overline{s}, \sigma \rangle \leftrightarrow \cdots \leftrightarrow \langle s_{n-1} \parallel \overline{s}, \sigma \rangle \xrightarrow{(a)} \langle s_n \parallel \overline{s}, \sigma' \rangle,
\]
and according to the definition of \(R\), \(<r \parallel \overline{\tau}, \sigma> R \langle s_i \parallel \overline{s}, \sigma \rangle\) for all \(i \leq n - 1\), and \(<t, \sigma> R \langle s_n \parallel \overline{s}, \sigma \rangle\).

If the final rule applied is Rule 17, \(<r, \sigma> \xrightarrow{sa(m,c)} \langle r', \sigma \rangle, \langle \overline{\tau}, \sigma \rangle \xrightarrow{ra(m,c)} \langle \overline{\tau}', \sigma' \rangle\), \(t = r' \parallel \overline{\tau}'\) and \(a = ca(m, c)\) (or, symmetrically, \(\overline{\tau}\) does send and \(r\) does receive).

Since \(<r, \sigma> \sim_{\text{test}} (s, \sigma)\), there exist \(s_0, \ldots, s_n, s'\) such that \(s_0 = s, \langle s_0, \sigma \rangle \leftrightarrow \cdots \leftrightarrow \langle s_n, \sigma \rangle \xrightarrow{sa(m,c)} \langle s', \sigma' \rangle\),
\(<r, \sigma> \sim_{\text{test}} (s_i, \sigma)\) for all \(i \leq n\), and \(<r', \sigma'> \sim_{\text{test}} (s', \sigma')\). Since \(<\overline{\tau}, \sigma> \sim_{\text{test}} (\overline{s}, \sigma)\), there exist \(\overline{s}_0, \ldots, \overline{s}_m, \overline{s}'\) such that \(\overline{s}_0 = \overline{s}, \langle \overline{s}_0, \sigma \rangle \leftrightarrow \cdots \leftrightarrow \langle \overline{s}_n, \overline{s}_0, \sigma \rangle \leftrightarrow \cdots \leftrightarrow \langle \overline{s}_n \parallel \overline{s}_m, \sigma \rangle\).

By Rule 17,
\[
\langle s_n \parallel \overline{s}_m, \sigma \rangle \xrightarrow{ra(m,c)} \langle s', \parallel \overline{s}', \sigma' \rangle.
\]

According to the definition of \(R\), \(<r \parallel \overline{\tau}, \sigma> R \langle s_i \parallel \overline{s}, \sigma \rangle\) for all \(i \leq n, j \leq m\), and \(<t, \sigma> R \langle s'_i \parallel \overline{s}', \sigma \rangle\).

To prove the divergence condition, suppose \(t_0 = r \parallel \overline{\tau}\),
\[
\langle t_0, \sigma \rangle \leftrightarrow \langle t_1, \sigma \rangle \leftrightarrow \langle t_2, \sigma \rangle \leftrightarrow \cdots
\]
and \(<s \parallel \overline{s}, \sigma> R \langle t_i, \sigma \rangle\) for all \(i \geq 0\). According to the definition of \(R\) there exist \(r_0, r_1, r_2, \ldots\) and \(t_0, t_1, t_2, \ldots\) such that \(t_i = r_i \parallel \overline{\tau}_i, \langle r_i, \sigma \rangle \sim_{\text{test}} (s, \sigma)\) and \(<\overline{\tau}_i, \sigma> \sim_{\text{test}} (\overline{s}, \sigma)\). Since the final rule of a derivation with \(<r_i \parallel \overline{\tau}_i, \sigma> \leftrightarrow \langle t_{i+1}, \sigma \rangle\) as conclusion is either Rule 16 or Rule 17, we conclude that for all \(i \geq 0\) either \(<r_i, \sigma> \leftrightarrow \langle r_{i+1}, \sigma \rangle\) and \(t_{i+1} = \overline{\tau}_i\), or \(<t_i, \sigma> \xrightarrow{sa(m,c)} r_{i+1}\) and \(\overline{\tau}_i \xrightarrow{ra(m,c)} \overline{\tau}_{i+1}\) (or the two symmetric cases).

Suppose that there exist \(n\) and \(m\) such that \(r_i \neq r_{i+1}\) and \(\overline{\tau}_j \neq \overline{\tau}_{j+1}\) for all \(i \geq n, j \geq m\) (note that this implies that for all \(i \geq n\) and for all \(j \geq m, r_{i+1} = r_i\) and \(\overline{\tau}_{i+1} = \overline{\tau}_i\)). Let \(k = \text{max}(n, m)\). Then, \(<t_k, \sigma> \xrightarrow{sa(m,c)} \langle t_{k+1}, \sigma \rangle\) which is a contradiction. We conclude that there exist \(i_0, i_1, i_2, \ldots\) such that \(i_0 = i, \langle r_{i_0}, \sigma \rangle \leftrightarrow \langle r_{i_1}, \sigma \rangle \leftrightarrow \langle r_{i_2}, \sigma \rangle \leftrightarrow \cdots\)
or that there exist \(j_0, j_1, j_2, \ldots\) such that \(j_0 = j, \langle \overline{\tau}_{j_0}, \sigma \rangle \leftrightarrow \langle \overline{\tau}_{j_1}, \sigma \rangle \leftrightarrow \langle \overline{\tau}_{j_2}, \sigma \rangle \leftrightarrow \cdots\).

We only consider the first case, the second one is similar.
Suppose first \( \langle r_{i_0}, \sigma \rangle \leftarrow \langle r_{i_1}, \sigma \rangle \leftarrow \langle r_{i_2}, \sigma \rangle \leftarrow \cdots \). Since \( \langle r_{i_j}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s, \sigma \rangle \) for all \( j \geq 0 \), there are \( s' \) and \( k > 0 \) such that \( \langle s, \sigma \rangle \leftarrow \langle s', \sigma \rangle \) and \( \langle r_{i_k}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s', \sigma \rangle \). By Rule 16, \( \langle s \parallel \bar{s}, \sigma \rangle \leftarrow \langle s', \parallel \bar{s}, \sigma \rangle \) and according to the definition of \( R \), \( \langle r_{i_k}, \sigma \rangle R \langle s', \parallel \bar{s}, \sigma \rangle \).

Suppose now \( \langle r_{i_0}, \sigma \rangle \leftarrow \cdots \leftarrow \langle r_{i_k}, \sigma \rangle \overset{a}{\leftarrow} \langle r_{i_{k+1}}, \sigma \rangle \) for some \( k \geq 0 \) and \( a \in \mathcal{A}^\text{com} \). Since \( \langle r_{i_k}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n, s' \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n, \sigma \rangle \overset{a}{\leftarrow} \langle s', \sigma \rangle,
\]

\( \langle r_{i_k}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s_j, \sigma \rangle \) for all \( j \leq n \), and \( \langle r_{i_{k+1}}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s', \sigma \rangle \). Suppose \( a = s a (m, c) \) (similarly if \( a \) is a receive action). Then, \( \langle r_{i_k}, \sigma \rangle \overset{\text{ra}(m, c)}{\leftarrow} \langle r_{i_{k+1}}, \sigma \rangle \). Since \( \langle r_{i_k}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s, \sigma \rangle \), there exist \( \bar{s}_0, \ldots, \bar{s}_m, \bar{s}' \) such that \( \bar{s}_0 = \bar{s} \),

\[
\langle \bar{s}_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle \bar{s}_m, \sigma \rangle \overset{\text{ra}(m, c)}{\leftarrow} \langle \bar{s}', \sigma \rangle,
\]

\( \langle r_{i_k}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s_j, \sigma \rangle \) for all \( j \leq m \), and \( \langle r_{i_{k+1}}, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s', \sigma \rangle \). By Rules 16, 17,

\[
\langle s \parallel \bar{s}, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n \parallel \bar{s}_m, \sigma \rangle \overset{\text{ca}(m, c)}{\leftarrow} \langle s' \parallel \bar{s}', \sigma \rangle
\]

and from the definition of \( R \), \( \langle r_{i_{k+1}}, \parallel \bar{s}_{k+1}, \sigma \rangle R \langle s' \parallel \bar{s}', \sigma \rangle \).

The proof that \( R \) is an interaction sensitive stuttering bisimulation relation is now complete.

7. To prove the first condition, suppose \( \langle \partial(p), \sigma \rangle \Downarrow \). Since Rule 20 is the final rule with this as conclusion, it follows that \( \langle p, \sigma \rangle \Downarrow \). Since \( \langle p, \sigma \rangle \mathrel{\cong_{st}} \langle q, \sigma \rangle \), we have \( \langle q, \sigma \rangle \Downarrow \), and hence, by Rule 20, that \( \langle \partial(q), \sigma \rangle \Downarrow \).

To prove the second condition, suppose \( \langle \partial(p), \sigma \rangle \overset{a}{\leftarrow} \langle r, \sigma' \rangle \). By Rule 21, as the final rule with this as transition as conclusion, we obtain \( \langle p, \sigma \rangle \overset{a}{\leftarrow} \langle p', \sigma' \rangle \), \( r = \partial(p') \) and \( a \in \mathcal{A} \setminus \mathcal{A}^\text{com} \). Since \( \langle p, \sigma \rangle \mathrel{\cong_{st}} \langle q, \sigma \rangle \), there exists \( q' \) such that \( \langle q, \sigma \rangle \overset{a}{\rightarrow} \langle q', \sigma' \rangle \) and \( \langle p', \sigma' \rangle \mathrel{\sim_{\text{test}}} \langle q', \sigma' \rangle \). By Rule 21 again, \( \langle \partial(q), \sigma \rangle \overset{a}{\rightarrow} \langle \partial(q'), \sigma' \rangle \). It remains to show that \( \langle \partial(p'), \sigma \rangle \mathrel{\sim_{\text{test}}} \langle \partial(q'), \sigma' \rangle \). We do this by showing that a symmetric relation

\[
R = \{ \langle \langle \partial(r), \sigma \rangle, \langle \partial(s), \sigma \rangle \rangle \mid \langle r, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s, \sigma \rangle \}
\]

is an interaction sensitive stuttering bisimulation relation.

To prove the termination condition, suppose \( \langle \partial(r), \sigma \rangle \Downarrow \). Rule 20 is the final rule with this as conclusion so we have \( \langle r, \sigma \rangle \Downarrow \). Since \( \langle r, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s, \sigma \rangle \), there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftarrow \cdots \leftarrow \langle s_n, \sigma \rangle \Downarrow,
\]

and \( \langle r, \sigma \rangle \mathrel{\sim_{\text{test}}} \langle s_i, \sigma \rangle \) for all \( i \leq n \). Now, by Rules 20 and 21,

\[
\langle \partial(s_0), \sigma \rangle \leftarrow \cdots \leftarrow \langle \partial(s_n), \sigma \rangle \Downarrow
\]

and \( \langle \partial(s_n), \sigma \rangle \Downarrow \). According to the definition of \( R \), \( \langle \partial(r), \sigma \rangle R \langle \partial(s_i), \sigma \rangle \) for all \( i \leq n \).
To prove the transfer condition, suppose \( \langle \partial(r), \sigma \rangle \overset{a}{\rightarrow} \langle t, \sigma' \rangle \). Since Rule 21 is the final rule with this transition as conclusion, we have \( \langle r, \sigma \rangle \overset{a}{\rightarrow} \langle r', \sigma' \rangle \), \( t = \partial(r') \) and \( a \in \mathcal{A} \setminus \mathcal{A}_{\text{comm}} \). Since \( \langle r, \sigma \rangle \overset{\text{st}}{\sim} \langle s, \sigma \rangle \) there exist \( s_0, \ldots, s_n \) such that \( s_0 = s \),

\[
\langle s_0, \sigma \rangle \leftrightarrow \cdots \leftrightarrow \langle s_{n-1}, \sigma \rangle \leftrightarrow \langle s_n, \sigma \rangle,
\]

\( \langle r, \sigma \rangle \overset{\text{st}}{\sim} \langle s_i, \sigma \rangle \) for all \( i \leq n - 1 \), and \( \langle r', \sigma' \rangle \overset{\text{st}}{\sim} \langle s_n, \sigma' \rangle \). Now, by applying Rule 21,

\[
\langle \partial(s_0), \sigma \rangle \leftrightarrow \cdots \leftrightarrow \langle \partial(s_{n-1}), \sigma \rangle \leftrightarrow \langle \partial(s_n), \sigma' \rangle.
\]

According to the definition of \( R \), \( \langle \partial(r), \sigma \rangle \overset{\text{st}}{\sim} \langle \partial(s_i), \sigma \rangle \) for all \( i \leq n - 1 \), and \( \langle \partial(r'), \sigma' \rangle \overset{\text{st}}{\sim} \langle \partial(s_n), \sigma' \rangle \).

To prove the divergence condition, suppose \( t_0 = \partial(r) \), \( \langle t_0, \sigma \rangle \leftrightarrow \langle t_1, \sigma \rangle \leftrightarrow \langle t_2, \sigma \rangle \leftrightarrow \cdots \) and \( \langle t_i, \sigma \rangle \overset{\text{st}}{\sim} \langle t_{i+1}, \sigma \rangle \) for all \( i \geq 0 \). Since Rule 21 is the final rule with \( \langle t_i, \sigma \rangle \leftrightarrow \langle t_{i+1}, \sigma \rangle \) as conclusion, we have \( t_0 = r \),

\[
\langle r_0, \sigma \rangle \leftrightarrow \langle r_1, \sigma \rangle \leftrightarrow \langle r_2, \sigma \rangle \leftrightarrow \cdots \n
\]

and \( t_i = \partial(r_i) \) for all \( i \geq 0 \). According to the definition of \( R \), \( \langle r_i, \sigma \rangle \overset{\text{st}}{\sim} \langle s, \sigma \rangle \) for all \( i \geq 0 \) and therefore there exist \( s' \) and \( j > 0 \) such that \( \langle s, \sigma \rangle \leftrightarrow \langle s', \sigma \rangle \) and \( \langle r_j, \sigma \rangle \overset{\text{st}}{\sim} \langle s', \sigma \rangle \). By applying Rule 21, \( \langle \partial(s), \sigma \rangle \leftrightarrow \langle \partial(s'), \sigma \rangle \).

According to the definition of \( R \), \( \langle \partial(r_j), \sigma \rangle \overset{\text{st}}{\sim} \langle \partial(s'), \sigma \rangle \).

With this we complete the proof that \( R \) is an interaction sensitive stuttering bisimulation relation.

5 Application

In [16], the translation from \( \chi \) to PROMELA is discussed informally. It is pointed out that the translation is straightforward for some constructs of \( \chi \) (e.g., for assignments and alternative composition), since they also exist in PROMELA. However, the translation of guards, nested scopes and nested parallelism is less straightforward, since they have no direct equivalents in PROMELA. In the pre-processing phase of the proposed translation, nested scopes and certain occurrences of nested parallelism are eliminated, and guards are pushed down to the level of atomic processes. In this section we indicate how this pre-processing phase can be proved correct using the notion of stuttering congruence proposed in this paper.

To be able to state some of the results pertaining to the scope operator, we first need to define the notion of free occurrence of a variable \( x \) in a process \( p \).

Definition 13. The set of free variables in \( p \in P \) is defined as:
Let
\[
\begin{align*}
\emptyset & \quad \text{if } p = \varepsilon, \delta \text{ or skip} \\
\{x\} \cup \text{dom}(e) & \quad \text{if } p = x := e \\
\text{dom}(e) & \quad \text{if } p = m\epsilon e \\
\{x\} & \quad \text{if } p = m?x \\
dom(b) \cup \text{free}(q) & \quad \text{if } p = b \rightarrow q \\
\text{free}(q) \cup \text{free}(r) & \quad \text{if } p = q \circ r, \text{ for all } o \in \{:, \|, \|\} \\
\text{free}(q) & \quad \text{if } p = q^* \\
\text{free}(q) \setminus \text{dom}(s) & \quad \text{if } p = [s | q] \\
\text{free}(q) & \quad \text{if } p = \partial(q)
\end{align*}
\]

The following theorem explains how guards can be distributed over all operators except parallel composition.

**Theorem 8.** Let \( p, q \in P \), let \( s, s_1, s_2 \in \Sigma \) and let \( b \in B \) such that \( \text{dom}(b) \cap \text{dom}(s) = \text{free}(p) \cap \text{dom}(s) = \emptyset \). Then:

1. \( b \rightarrow [s | p] \equiv_{st} [s | b \rightarrow p] \),
2. \( [s | p] : q \equiv_{st} [s | p ; q] \),
3. \( p ; [s | q] \equiv_{st} [s | p ; q] \),
4. \( [s | p] ; q \equiv_{st} [s | p \parallel q] \),
5. \( [s | p] \parallel q \equiv_{st} [s | p \parallel q] \),
6. \( [s_1 | s_2 | p] \equiv_{st} \gamma(s_1, s_2) | p] \),
7. \( [s | \partial(p)] \equiv_{st} \partial([s | p]) \).

In case of a repetition, we need to be careful: \([s | p]^*\) is not stuttering congruent with \([s | p^*]\), for in \([s | p]^*\) the state \( s \) is applied before each repetition of \( p \), while in \([s | p^*]\) it is only applied before the first repetition. The solution is to incorporate in \([s | p^*]\) the effect of the state \( s \) after each repetition of \( p \) by a sequential composition of assignments of the form \( x := s(x) \), one for every \( x \in \text{dom}(s) \). That is, if \( \text{dom}(s) = \{x_1, \ldots, x_n\} \), then we propose to replace \([s | p]^*\) by \([s | (p ; x_1 := s(x_1) ; \ldots ; x_n := s(x_n))^*]\). Note, however, that this only works if \( p \) does not have the option to terminate immediately (i.e., if \((p, \sigma) \not\rightarrow\) for all states \( \sigma \)). For, if \((p, \gamma(s, \sigma))\), then \(\langle [s | (p ; x := c)^*], \sigma \rangle\) can do the action \(aa(x, c)\), whereas \(\langle [s | p]^*, \sigma \rangle\) cannot do the same action (unless \( p \) can do it), and thus the root condition is violated.

Let \( \overrightarrow{P} \) be the set of processes that do not contain \( \varepsilon \) and in which every occurrence of the star operator is immediately followed by the sequential composition operator. Then \((p, \sigma) \not\rightarrow\) for all \( \sigma \in \Sigma \) and all \( p \in \overrightarrow{P} \), as is easily proved by structural induction on \( p \).

**Theorem 9.** Let \( p \in \overrightarrow{P} \), let \( x \in V \), and let \( e \in C \). If \( s \) is a state such that \( \text{dom}(s) = \{x_1, \ldots, x_n\} \), then
\[
\begin{align*}
[s | p]^* & \equiv_{st} [s | (p ; x_1 := s(x_1) ; \ldots ; x_n := s(x_n))^*].
\end{align*}
\]

The next theorem explains how guards can be distributed over all operators.
1. \( \text{true} :\rightarrow p \equiv_{st} p \)
2. \( b_1 :\rightarrow b_2 :\rightarrow p \equiv_{st} (b_1 \land b_2) :\rightarrow p \)
3. \( b :\rightarrow (p ; q) \equiv_{st} (b :\rightarrow p) ; q \)
4. \( b :\rightarrow (p \parallel q) \equiv_{st} (b :\rightarrow p) \parallel (b :\rightarrow q) \)
5. \( b :\rightarrow p^* \equiv_{st} (b :\rightarrow p) ; p^* \parallel (b :\rightarrow \varepsilon) \)
6. \( b :\rightarrow \partial(p) \equiv_{st} \partial(b :\rightarrow p) \)

Note that the process \( b :\rightarrow (p \parallel q) \) is not stuttering congruent with the process \( (b :\rightarrow p) \parallel (b :\rightarrow q) \). For suppose the processes are executed in a state in which \( b \) is \text{true} and an action from \( p \) changes the state in such a way that the value of \( b \) becomes \text{false}. Then the process \( b :\rightarrow (p \parallel q) \) proceeds as \( p^* \parallel q \) and the process \( (b :\rightarrow p) \parallel (b :\rightarrow q) \) as \( p^* \parallel (b :\rightarrow q) \), and in the latter process the option \( q \) is blocked.

We finish this section with a theorem that allows us to simplify nested parallelism in the context of sequential composition. For similar reasons as before it is formulated in terms of the set \( \overline{\mathcal{P}} \).

**Theorem 10.** Let \( p, q, r \in \overline{\mathcal{P}} \), and let \( w \in V \) such that \( w \notin \text{free}(p) \cup \text{free}(q) \cup \text{free}(r) \). Then,

1. \( (p \parallel q) ; r \equiv_{st} [w \mapsto 0 | p ; w := w + 1 \parallel q ; w := w + 1 \parallel (w = 2) :\rightarrow r] \)
2. \( p ; (q \parallel r) \equiv_{st} [w \mapsto 0 | p ; w := w + 1 \parallel (w = 1) :\rightarrow q \parallel (w = 1) :\rightarrow r] \)

6 Conclusion

In this paper we have proposed the notion of stuttering congruence for the modeling and simulation language \( \chi \). We have proved that if two \( \chi \) processes are stuttering congruent, then, with respect to any state \( \sigma \), they satisfy the same \( CTL^*_{\chi} \) formulas and either both have deadlock or neither of them. Stuttering congruence is a behavioural congruence for the constructs of discrete-event, untimed part of \( \chi \), i.e., it is defined directly on the operational semantics of the language and it is a congruence for its constructions. Therefore, it is suitable for establishing the correctness of syntactic transformations on \( \chi \) models.

We have illustrated the use of stuttering congruence in correctness proofs of syntactic transformations by indicating how (a part of) the preprocessing phase of the translation from \( \chi \) to Promela can be proved correct. It is explained in detail in [16] that if a \( \chi \) model satisfies a few general restrictions, then the preprocessing phase yields a \( \chi \) model that can be straightforwardly translated into Promela. The resulting Promela specification can then be verified with Spin. Incidentally, note that for Theorems 8 and 10 it is essential that stuttering congruence allows ‘stuttering’; the transformations in these theorems do not preserve the validity of full \( CTL^* \).

Currently, a translation from \( \chi \) to UPPAAL is being developed, and it also involves a preprocessing phase. The model checker of UPPAAL verifies \( CTL \) formulas, and since our stuttering congruence preserves \( CTL^*_{\chi} \) formulas, it will be suitable for proving the correctness of that preprocessing phase too.

As future work, we mention the extension of the results obtained in this paper to full timed \( \chi \) [15], proving the preservation of the validity of a timed variant of \( CTL^*_{\chi} \).
References