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Citation for published version (APA):

DOI:
10.1063/1.1627942

Document status and date:
Published: 01/01/2003

Document Version:
Publisher’s PDF, also known as Version of Record (includes final page, issue and volume numbers)

Please check the document version of this publication:
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Download date: 27. Jan. 2020
Digital alloy interface grading of an InAlAs/InGaAs quantum cascade laser structure studied by cross-sectional scanning tunneling microscopy

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(Received 21 July 2003; accepted 26 September 2003)

We have studied an InGaAs/InAlAs quantum cascade laser structure with cross-sectional scanning tunneling microscopy. In the quantum cascade laser structure digital alloy grading was used to soften the barriers of the active region. We show that due to alloy fluctuations, softening of the barriers occurs even without the digital grading. © 2003 American Institute of Physics. [DOI: 10.1063/1.1627942]

Unlike quantum well semiconductor lasers where the light originates from recombination of electrons and holes across the energy gap that exists between the conduction band and valence band of the crystal, in quantum cascade laser (QCL) structures, the wavelength is essentially determined by quantum confinement, i.e., by the thicknesses of the layers of the active region rather than by the band gap of the material. The injection and extraction efficiency to and from the active regions has been significantly improved by means of wave function engineering, which involves control over the barrier/well thickness and interface quality. Interface roughness scattering is one of the important nonradiative scattering mechanisms of QCL structures. To reduce interface roughness scattering, digital alloy grading was used to soften the barriers of the active region of a four-quantum-well (4QW) midinfrared quantum cascade laser structure. This structure showed a reduced threshold current density and a narrower luminescence linewidth in pulsed mode than a device with abrupt interfaces. However, this result could not be reproduced in a second growth run. We report cross-sectional scanning-tunneling microscopy (X-STM) measurements of 4QW QCL structures with and without graded interfaces. The aim of this letter is to show that interface roughness and grading occur due to fluctuations in the alloy concentration which are caused by indium segregation during growth.

The QCL structures were grown by molecular beam epitaxy on InP substrates using lattice-matched InAlAs/InGaAs active layers and an InP top cladding. Except for the interfaces, both types of devices are identical and based on a 4QW active region designed for a vertical lasing transition at 9.3 mm with a double phonon resonance.2 The layer sequence of the graded structure is as follows: 31/19/30/23/29/25/26/1/2/40/5/4/6/8/1(3/52/1/2)/(2/1/3/1/2)/(2/1/51/1/2)/(2/1/3/1/2)/(2/1/44/1/2)/(2/1/19)/34/14/33/13/32/15 Å. The structure with the abrupt interfaces has the following layer sequence: 31/19/30/23/29/25/29/40/19/7/58/9/57/19/50/22/34/14/33/13/32/15 Å. InGaAs wells are in roman, and InAlAs barriers in bold. Doped layers (Si, 3×10^{17} cm^{-3}) are underlined. The main differences between both structures are the design of injection barrier and the barrier/well interfaces of the active region which are graded by a digital InGaAs/InAlAs alloy of 1- and 2-Å-thick layers as indicated by parentheses.

STM measurements have been performed in an UHV chamber with base pressure <2×10^{-11} Torr on the UHV-cleaved (110) cross-sectional surface. Tips are prepared by electrochemical etching of polycrystalline tungsten wires and are treated in the vacuum with a self-sputtering technique.3

In Fig. 1 we show a STM image of the QCL structure with graded interfaces. Figure 1 was taken at a negative sample bias of −1.6 V, showing the filled states associated with the As sublattice. The bright regions are the InGaAs well layers and the dark regions are the InAlAs barrier layers. The observed constrast between the barrier and the well layers is due to the band offset between the well and the barrier layers which causes an electronic contribution to the tunnel current at low sample bias (\(V_{\text{sample}}\approx -2\) V). The

![Image](http://apl.aip.org/apl/)

**FIG. 1.** 60×60 nm^2 filled states STM image of the graded structure with layer sequence indicated at the bottom of the image, \(V_{\text{sample}}\approx -1.6\) V. The black layers are the InAlAs barriers and the white layers are the InGaAs wells.

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graded interfaces are indicated by arrows. The brighter and darker regions within and across the layers indicate the strong presence of alloy fluctuations which are caused, as we will show, by indium segregation. It is clear that the alloy fluctuations are causing strong interface roughness, which will influence the actual effect of the intentional grading of the interfaces.

In order to quantify the sharpness of the ungraded interfaces of both structures, we derive the distribution of the gallium and aluminum atoms across both interfaces of a 2.5 nm wide barrier by using averaged line profiles of the filled states image. Since we are imaging the filled states of the sample surface, we are not directly probing the aluminum and gallium atoms themselves but rather the electronic effects these two atomic species have on the surface arsenic.
atoms. The apparent height of an arsenic site should be dependent on the number of aluminum atoms in the nearest neighbor positions at the cleaved (110) surface. There are three nearest neighbor positions where an Al atom can reside. Two neighbor positions are in the surface layer and one neighbor position is in the subsurface layer. In our analysis of the apparent height profile, the average number of aluminum atoms of a single bilayer can be assigned to both the surface and subsurface layers, which will give a spread in the possible number of aluminum atoms in each monolayer. The final aluminum distribution is determined by fitting with a concentration profile in the form of:

$$c(x) = \frac{1}{2}[\text{erf}(L/2 + x)/2\sigma_x] + \text{erf}(L/2 - x)/2\sigma_y$$

where $L$ is the width of the barrier and $\sigma_x$ and $\sigma_y$ indicate the sharpness of the interfaces of the barrier. Figures 2(a) and 2(b) show for both QCL structures the aluminum distribution of the 2.5 nm wide InAlAs barrier with ungraded interfaces. The analysis was done for profiles taken at sample voltages of $-1.6$, $-2.0$, and $-2.4$ V. Although the observed contrast in the STM image depends on the applied voltage, the counted aluminum distribution does not. We find that for both structures the full width at half maximum is in agreement with the growth menu, however, in both cases the interfaces have a gradient of 4 ML. We propose that this grading is caused by large scale alloy fluctuations due to indium segregation during growth. This means that any additional digital grading of the interfaces of the active region will not have much effect.

In Figs. 3(a) and 3(b) we show detailed images the active region of the graded structure and the structure with the abrupt interfaces. The barriers of the active region are indicated by arrows. The injection barriers of both structures are clearly different, however, the thin barriers in the active region look the same. This can also be seen in Fig. 3(c) which shows the averaged lineprofiles of the active region of both structures.

Figure 4 shows the InGaAs region which was grown on top of the active layers. In the lower right corner part of the active layers can be seen. The image was taken at high positive voltage (+2.5 V) where electronic contrast is minimized. The topographical contrast is due to the outward relaxation of indium rich regions. The lateral scale variation of alloy fluctuations is about 10 nm, which is large enough to influence the device characteristics.

Figures 5(a) and 5(b) show images of exactly the same active region at low negative voltage (filled states) and high positive voltage (empty states), respectively. The filled states image clearly shows the electronic contrast between the barriers and wells. The empty states image shows the topographical contrast due to outward relaxation of indium rich regions. In both the barriers and the wells of the filled states image brighter and darker regions can be seen, which correspond to the indium rich and indium poor regions of the empty states image. This shows to our surprise that the indium clusters extend across the barrier and wells, which results, on average, in graded interfaces.

We have compared QCL structures with and without digitally alloyed interfaces on the atomic level with X-STM. We showed that due to indium segregation across the barrier and well layers, fluctuations in the width of the layers occur, which on average lead to graded interfaces of about 4 ML. We find that, because of the unintentional grading, the digital alloy graded interfaces are not significantly smoother than the ungraded interfaces. We propose that in order to reduce interface roughness scattering, fluctuations in the layer thicknesses due to indium segregation should be taken into account.