All-optical label extractor/eraser for in-band labels and 160-Gb/s payload based on microring resonators

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All-Optical Label Extractor/Eraser for In-Band Labels and 160-Gb/s Payload Based on Microring Resonators


Abstract—Photonic integration of an all-optical packet switch (AOPS) is a crucial issue since it leads to a great reduction of volume, power consumption, and costs. Recently, a $1 \times 4$ AOPS was successfully demonstrated by using an all-optical label extractor/eraser. The label extractor/eraser was implemented by means of fiber Bragg gratings and optical circulators that prevent the photonic integration of the AOPS. Here, we demonstrate a passive and asynchronous photonic integrated all-optical label extractor/eraser by using narrowband silicon-nitride add-drop ring-resonators. By exploiting the narrow bandwidth of the drop port and pass-through port error-free simultaneous separation/erasing of the in-band labels from the 160-Gb/s packet payload was demonstrated without noticeable pulse-shape distortions.

Index Terms—Optical processing, packet switching, ring resonators, wavelength filtering devices.

I. INTRODUCTION

All-optical packet switching based on all-optical signal processing is a promising technology to solve the mismatch between the fiber bandwidth and the router forwarding capacity [1], [2]. Moreover, it has been argued that all-optical technology can profit from the capability to integrate all the required switching functionalities in a photonic integrated chip to allow for a great reduction of volume, power consumption, and costs. It is then an essential issue to provide all-optical signal processing functionalities that are suitable for photonic integration.

Recently, an all-optical label extractor/eraser for in-band labeling addresses and 160-Gb/s data payload was successfully employed to demonstrate a $1 \times 4$ all-optical packet switch (AOPS) [3]. The AOPS schematic and the packet format are shown in Fig. 1. The optical address is encoded by combining the in-band labels (the wavelengths of the labels are located within the 20-dB bandwidth of the payload). The in-band labeling technique has the advantage that label extraction can be implemented by using passive optical bandpass filters centered at the labels wavelength and label erasing by using notch filters. In [3], the label extractor/eraser was implemented by exploiting the reflection and pass-through ports of two pigtailed fiber Bragg gratings (FBGs) centered at the label’s wavelength and two optical circulators (see Fig. 1). Thus, the labels, reflected by the FBGs, are extracted via the optical circulators, while the data payload passes through the FBGs and is fed into the switching fabric. The extracted labels are optically processed by the label processor, which produces an optical routing signal driving the switching fabric.

Despite the successful demonstration of the AOPS and the potential integration of the label processor and switching fabric [3], photonic integration of the entire AOPS is prevented by the integration of the label extractor/eraser. Moreover, the broad bandwidth of the FBGs caused a splicing and distortion of the spectrum of the payload which resulted in a pulse broadening of the 160-Gb/s payload. This leads to a closed eye and, therefore, to an extra power penalty. The pulse distortion can be practically avoided by using optical bandpass filters with narrow bandwidth and optical notch filters with narrow stopband and flat all-pass band.

In this work, we demonstrate a label extractor/eraser for in-band labeling addresses and 160-Gb/s payload by using an integrated microring resonator with pass-through and drop port. By exploiting the narrow bandwidth of the drop port and the

Fig. 1. (a) Packets format and in-band labeling technique. (b) AOPS configuration.
narrow bandwidth and flat all-pass band of the pass-through port, simultaneous error-free label extraction and label erasing has been performed without noticeable pulse distortion.

II. EXPERIMENTS AND RESULTS

The experimental setup employed to demonstrate the microring-based label extractor/eraser is shown in Fig. 2. The packet format is the same as the one shown in Fig. 1. Packet payload is generated by time-quadrupling a 40-Gb/s data stream consisting of 256 predefined return-to-zero bits (λp = 1551.6 nm) into a 160-Gb/s data stream using a passive pulse interleaver. Each pulse has duration of 1.5 ps making the 20-dB bandwidth of the payload to be 5 nm. The resulting packet payload consists of a 5.6-ns data burst. The guard-time between the packets is 800 ps. We employ in-band labeling; the packet address information is encoded with continuous-wave (CW) signals at wavelengths within the 5-nm payload band. Each label can have a binary value: “0” means no signal at the label wavelength, “1” means an optical signal at the label wavelength payload is output to the pass-through port. The two labels’ wavelengths are filter out at the drop-port, while the wavelengths matching the labels’ wavelengths. Therefore, the labels’ wavelengths are filter out at the drop-port, while the wavelength payload is output to the pass-through port. The two labels at wavelength λL1 and λL2 are output from the drop port and then separated by an arrayed waveguide grating (AWG) filter ready to be fed into the label processor. This is confirmed by spectral and time-domain measurements. In Fig. 3, the eye diagrams of the 160-Gb/s payload and the extracted label traces (after the AWG) are reported in Fig. 2(a). The measured optical spectra with resolution bandwidth of 0.06 nm at the drop and pass-through ports are reported. The optical spectra measurements with resolution bandwidth of 0.06 nm at the drop and pass-through ports are reported. The optical spectrum of the input packet before the label extractor is shown in Fig. 3(a). The optical spectrum at the output of the drop port and the pass-through output port are reported in Fig. 3(b) and (c). More than 25 dB of separation/erasing between the labels and the optical payload were measured.

We also report in Fig. 4(a)–(c) the time-domain traces of the 160-Gb/s payload and the extracted label traces (after the AWG) recorded at the pass-through port and drop port, respectively. Note that the two labels have the same pattern, because we employed a single amplitude modulator driven by a pattern generator to modulate the two CW signals at λL1 and λL2. The labels present no shape distortion and no visible crosstalk due to the payload.

The eye diagrams of the original input 160-Gb/s data payload and of the 160-Gb/s payload after the label extractor/eraser are reported in Fig. 4(a)–(c). An optical sample oscilloscope with 700 GHz of bandwidth was employed to record the eye diagrams of the 160-Gb/s pulses. Very small degradation and
broadening of the pulses is observed after the label extraction. The measured pulsewidth broadening was 0.4 ps. The measured root-mean-square (rms) time jitter was 212 fs for the pulses after the label extractor/eraser, which results in an increase of 40 fs compared to the 173 fs of the input 160-Gb/s payload pulses. For comparison, we also reported in Fig. 5(c) the eye diagram of the 160-Gb/s payload after the label extraction implemented by using the FBGs as in [3]. A much larger pulse broadening can be observed caused by the broad bandwidth of the FBGs (−3-dB bandwidth of 0.42 and 0.1 nm for FBG1 and FBG 2, respectively) compared to the bandwidth of the microring add–drop resonator.

To quantify the performance of the label extractor/eraser, bit-error-rate (BER) measurements are reported in Fig. 6. The BER measurements were performed in a static operation by time-quadrupling 40-Gb/s pseudorandom binary sequence $2^{31} - 1$ data payload. The resulting 160-Gb/s data payload after the label extractor/eraser is amplified and time-demultiplexed from 160 to 40 Gb/s by using an electrically clocked electroabsorption modulator that creates a 5-ps switching window with a periodicity of 25 ps. The resulting 40-Gb/s data is then detected by a 40-Gb/s detector and analyzed by using a BER tester. As reference, we report the BER curve of the back-to-back 160-Gb/s payload. The BER curve of the 160-Gb/s payload after the label extraction/eraser shows error-free operation with limited power penalty of around 0.5 dB. The power penalty can be ascribed to the additional amplified spontaneous emission noise introduced by the amplification stage required to compensate the total loss of the label extractor/eraser.

III. Conclusion

We have demonstrated error-free operation of a photonic integrated all-optical label extractor/eraser for in-band labeling addresses by using a passive silicon–nitride microring add–drop resonator. The in-band labels and the 160-Gb/s payload of the optical packets were successfully separated to the drop port and pass-through port, respectively. Due to the narrow stopband and flatness of the passband of the pass-through port of the microring, almost no distortion is experienced by 160-Gb/s payload spectrum. As a result, the 160-Gb/s payload pulses exhibit very limited broadening, in contrast with the visible broadening experienced by the payload pulses by using FBGs with broader stopband [3]. This resulted in a low power penalty of 0.5 dB. The negligible spectral distortion of the payload due to the utilization of microring with a narrow stopband is also very important in the perspective to operate the label extractor/eraser with a larger number of labels. We are currently designing for the realization of a microring resonator capable of extracting/erasing a larger number of in-band labels. Moreover, novel design will benefit from the achievement in technology to realize polarization independent and low insertion loss microring add–drop resonator as demonstrated in [5], [6]. Finally, it is worth to note that since the previous implementation of the label extractor/eraser employed in [3] was not suitable for photonic integration, the photonic integration of the AOPS node was prevented. This work demonstrates the feasibility for photonic integration of the label extractor/eraser, which may allow for photonic integration of the entire AOPS configuration.

REFERENCES