Abstract—During voltage dips continuous power delivery from distributed generation systems to the grid is desirable for the purpose of grid support. In order to facilitate the control of inverter-based distributed power generation adapted to the expected change of grid requirements, generalized power control strategies based on symmetric-sequence components are proposed in this paper, aiming to manipulate the delivered instantaneous power under unbalanced voltage dips. It is shown that active power and reactive power can be independently controlled with two individually adaptable parameters. By changing these parameters, the relative amplitudes of oscillating power can be smoothly regulated, as well as the peak values of three-phase grid currents. As a result, the power control of grid-interactive inverters becomes quite flexible and adaptable to various grid requirements or design constraints. Furthermore, two strategies for simultaneous active and reactive power control are proposed that preserve flexible controllability; and an application example is given to illustrate the simplicity and adaptability of the proposed strategies for on-line optimization control. Finally, experimental results are provided that verify the proposed power control.

Index Terms—Distributed power generation, grid-interactive converter, power control, grid fault, voltage dip.

I. INTRODUCTION

Voltage dips, usually caused by remote grid faults in the power system, are short-duration decreases in rms voltage. Most voltage dips are due to unbalanced faults, while balanced voltage dips are relatively rare in practice [1] [2]. Conventionally, a distributed generation (DG) system, as shown in Fig. 1, would be required to disconnect from the grid when voltage dips occur and to reconnect to the grid when faults are cleared. However, this requirement is changing. With the increasing application of renewable energy sources, more and more DG systems actively deliver electricity into the grid. In particular, wind power generation is becoming an important electricity source in many countries. Consequently, in order to maintain active power delivery and reactive power support to the grid, grid codes now require wind energy systems to ride through voltage dips without interruption [3] [4]. For the future scenario of a grid with significant DG penetration, it is necessary to investigate the ride-through control of wind turbine systems and other DG systems as well. Disregarding various upstream distributed sources and their controls, this paper will focus on the grid-side DG inverters.

Concerning the control of DG inverters under voltage dips, especially unbalanced situations, two aspects should be noticed. Firstly, fast system dynamics and good reference tracking are necessary. Current controllers must be able to deal with all the symmetric-sequence components and to have fast feedback signals for closed-loop control. Secondly, in case of unbalanced voltage dips, the employment of different power control strategies, i.e., the generation of reference currents, is also very important. Because this paper mainly concentrates on the second aspect, the control structure of such inverters will be presented in the section dedicated to experimental verifications.

Under unbalanced voltage dips, current reference generation is constrained by trade-offs. Considering the power-electronics converter constraints, a constant dc-link voltage is desirable, [5] and [6]. However, a constant dc bus is achieved at the cost of unbalanced grid currents, and this results in a decrease of maximum deliverable power. In [7], a power reducing scheme is used to limit the current during grid faults. On the other hand, the effects of the grid currents on the utility side should also be taken into account when assigning reference currents for DG inverters. As presented in [8] and [9], several specific strategies are possible in order to get different power quality levels at the grid connection point in terms of instantaneous power oscillation and current distortion. One of the methods in [8], which is based on instantaneous power theory [10], obtains zero instantaneous power oscillation but generates distorted grid currents due to the asymmetry of grid voltages. Other methods in [8] lead to sinusoidal output currents. These strategies show flexible control possibilities of DG systems under grid faults. However, they only cope with specific objectives, such as zero instantaneous active power, balanced current outputs, or unity power factor.

Since voltage dips are as diverse as grid faults, there
are always multiple objectives constrained by converters or by grid requirements for grid-fault ride-through control; and these objectives have to be compromised and be adapted under different grid faults so as to optimize system overall performance. Therefore, a generalized power control strategy, which can be easily adapted to fulfill different objectives under voltage dips, is research area of interest. Starting from the ideas in [8] and [9], this paper proposes generalized active and reactive power control strategies that enable DG inverters to be optimally designed under voltage dips.

This paper is organized as follows. In Section II, instantaneous power calculation is presented along with notation definition. The derivation of proposed strategies for independent active and reactive power control are described in Section III. Then, two strategies for simultaneous active and reactive power control are derived and compared in Section IV, which preserve the adaptive controllability. In Section V, an application example illustrates the simplicity and adaptability of the proposed strategies for on-line optimization control. Finally, experimental verifications of the proposed power control strategies are presented in Section VI.

II. INSTANTANEOUS POWER CALCULATION

To investigate power control strategies, the instantaneous power theory [10] [11] is revisited in this section. Then instantaneous power calculation based on symmetric sequences is presented, and the notation for the reference current design in the next sections is defined.

A. Instantaneous Power Theory

For a three-phase DG inverter, instantaneous active power and reactive power at the grid connection point are given by, respectively,

\[ p = \mathbf{v} \cdot \mathbf{i} = v_a i_a + v_b i_b + v_c i_c \]  
\[ q = \mathbf{v}_\perp \cdot \mathbf{i} = \frac{1}{\sqrt{3}} [(v_a - v_b) i_c + (v_b - v_c) i_a + (v_c - v_a) i_b] \]  

with \( \mathbf{v}_\perp = \frac{1}{\sqrt{3}} \begin{bmatrix} 0 & 1 & -1 \\ -1 & 0 & 1 \\ 1 & -1 & 0 \end{bmatrix} \mathbf{v} \)

where \( \mathbf{v} = [v_a \ v_b \ v_c]^T, \mathbf{i} = [i_a \ i_b \ i_c]^T \), bold symbols represent vectors, and the operator "\( \cdot \)" denotes the dot product of vectors. Note that the subscript "\( \perp \)" is used to represent a vector derived from the matrix transformation in (2), although vectors \( \mathbf{v}_\perp \) and \( \mathbf{v} \) are orthogonal only when the three-phase components in vector \( \mathbf{v} \) are balanced.

B. Symmetric-sequence Based Instantaneous Power

Symmetric-sequence transformation is a proven way to decompose unbalanced multi-phase quantities [12]. Note that in the experiment these symmetric-sequence components are detected in the time domain. Consequently, instantaneous quantities for unbalanced \( abc \)-voltages are represented by

\[ \mathbf{v} = \mathbf{v}_+ + \mathbf{v}_- + \mathbf{v}_0 \]  

where \( \mathbf{v}_+ = [v_a^+ \ -v_b^- \ v_c^+ \ -v_b^- \ v_c^+ \ -v_a^-]^T \), and subscripts "\( + \)", "\( - \)", and "\( 0 \)" denote positive, negative, and zero sequences, respectively.

Similarly, current quantities can also be represented in terms of symmetric sequences, i.e.

\[ \mathbf{i} = \mathbf{i}_+ + \mathbf{i}_- + \mathbf{i}_0 \]  

where \( \mathbf{i}_+ = [i_a^+ \ -i_b^- \ i_c^+ \ -i_b^- \ i_c^+ \ -i_a^-]^T \). As a result, the calculation of instantaneous power in (1) and (2) can be rewritten as

\[ p = \mathbf{v} \cdot \mathbf{i} = (\mathbf{v}_+ + \mathbf{v}_- + \mathbf{v}_0) \cdot (\mathbf{i}_+ + \mathbf{i}_- + \mathbf{i}_0) \]  
\[ q = \mathbf{v}_\perp \cdot \mathbf{i} = (\mathbf{v}_+ \mathbf{v}_- + \mathbf{v}_0) \cdot (\mathbf{i}_+ + \mathbf{i}_- + \mathbf{i}_0) \]  

With respect to the definitions of the symmetric-sequence vector in (3), corresponding orthogonal vectors in (6) can be derived by using the matrix transformation in (2). Note that \( \mathbf{v}_+ \) lags \( \mathbf{v}_- \) by \( 90^\circ \), \( \mathbf{v}_- \) leads \( \mathbf{v}_+ \) by \( 90^\circ \), and \( \mathbf{v}_0 \) is always equal to zero. Because the dot products between \( \mathbf{i}_0 \) and positive-sequence or negative-sequence voltage vectors are also always zero (due to symmetry of the components in \( \mathbf{v}_+ \) and \( \mathbf{v}_- \)), equation (5) and (6) can be simplified by

\[ p = \mathbf{v} \cdot \mathbf{i} = (\mathbf{v}_+ + \mathbf{v}_-) \cdot (\mathbf{i}_+ + \mathbf{i}_-) \]  
\[ q = \mathbf{v}_\perp \cdot \mathbf{i} = (\mathbf{v}_+ \mathbf{v}_- + \mathbf{v}_0) \cdot (\mathbf{i}_+ + \mathbf{i}_-) \]  

Because the calculation of instantaneous power and current references is carried out in terms of vectors, it can also be used in other reference frames, simply by substituting the vectors in the \( abc \)-frame with vectors derived in other frames, for example, the stationary \( \alpha \beta \gamma \)-reference frame.

In next sections, current control based only on positive-sequence and negative-sequence components is investigated. Because zero-sequence voltages of unbalanced voltage dips do not exist in three-wire systems, nor can they propagate to the secondary side of star-grounded or delta connected transformers in four-wire systems, most case-studies only consider positive and negative sequences. Even for unbalanced systems with zero-sequence voltage, four-leg inverter topologies can eliminate zero-sequence current with appropriate control. Simplifying assumptions we will use:

- Only positive-sequence and negative-sequence currents are present;
- Only fundamental voltages exist, in practice they can be extracted out;
- The amplitude of the positive-sequence voltage is higher than the negative sequence one.
III. STRATEGIES FOR INDEPENDENT P&Q CONTROL

In order to separately analyze the contribution of currents to independent active and reactive power control, sequence currents $i^+_p$ and $i^-_q$ can be decoupled into two orthogonal quantities, i.e., $i^+_p$ and $i^-_q$, as depicted in Fig. 2. The subscript “$p$” is related to active power control, and “$q$” is related to reactive power control.

A. Reactive Power Control

For reactive power control, only $i^+_q$ and $i^-_q$ are present, which are defined in phase with $v^+_q$ and $v^-_q$, respectively, in order to generate reactive power only. Rewriting (7) and (8) in terms of $i^+_q$ and $i^-_q$, we obtain

$$p = v^+_q \cdot i^+_q + v^-_q \cdot i^-_q$$

$$q = v^+_q \cdot i^+_q + v^-_q \cdot i^-_q + v^+_q \cdot i^+_q + v^-_q \cdot i^-_q$$

(9)

where $Q^+$ and $Q^-$ denote the constant reactive power introduced by positive and negative sequences, respectively, $\hat{p}_{2q}$ represents oscillating active power, and $\hat{q}_{2q}$, oscillating reactive power. It can be found that the two terms of $\hat{p}_{2q}$ are in-phase quantities oscillating at twice the fundamental frequency. A similar property can be found for the two terms of $\hat{q}_{2q}$.

Because oscillating active power can reflect a variation on the dc-link current, and high dc voltage variation may cause over-voltage problems, output distortion, or even control instability, it is desirable to eliminate $\hat{p}_{2q}$. On the other hand, the oscillating reactive power $\hat{q}_{2q}$ also causes power losses and operating current rise, and therefore it is advantageous to mitigate $\hat{q}_{2q}$ as well. A trade-off between $\hat{p}_{2q}$ and $\hat{q}_{2q}$ is not straightforward and depends on practical requirements. In the following, strategies to achieve controllable oscillating active and reactive power are derived from two considerations.

1) Controllable oscillating active power:

For a desired level of injection into the grid of reactive power $Q$, which, for the DG system under consideration, should be determined in advance in agreement with the grid operator by taking also into account the inverter power ratings, the first two terms of (10) are designed to meet

$$Q = v^+_q \cdot i^+_q + v^-_q \cdot i^-_q.$$  

(11)

Since the other two terms of $\hat{q}_{2q}$ in (10) are in-phase quantities, it is convenient that these two terms can easily compensate each other. Therefore, by setting intentionally

$$v^+_q \cdot i^-_q = -k_q v^-_q \cdot i^+_q, \quad 0 \leq k_q \leq 1,$$

(12)

where $k_q$ is a scalar coefficient used as a weighting factor for the elimination of $\hat{q}_{2q}$; the subscript “$q$” is related to reactive power ($Q$) control. After some manipulations the negative-sequence current $i^-_q$ is derived from (12) as

$$i^-_q = \frac{-k_q v^+_q \cdot i^+_q}{\|v^+_q\|^2} v^-_q.$$  

(13)

where $\|v^+_q\|^2 = \|v^+_q\|^2 = v^+_q \cdot v^+_q$, operator “$\| \cdot \|$” means the norm of a vector.

Substituting (13) into (11), and using $\|v^+_q\|^2 = \|v^-_q\|^2$, we obtain

$$Q \|v^+_q\|^2 = (\|v^+_q\|^2 - k_q \|v^-_q\|^2) (v^+_q \cdot i^+_q).$$  

(14)

Then, based on (13) and (14), currents $i^+_q$ and $i^-_q$ can be calculated as

$$i^+_q = \frac{Q}{\|v^+_q\|^2 - k_q \|v^-_q\|^2} v^+_q$$

$$i^-_q = \frac{-k_q Q}{\|v^+_q\|^2 - k_q \|v^-_q\|^2} v^-_q.$$  

(15)

(16)

Finally, the total current reference $i^*_q$ is the sum of $i^+_q$ and $i^-_q$, that is

$$i^*_q = \frac{Q}{\|v^+_q\|^2 - k_q \|v^-_q\|^2} v^+_q,$$

$$0 \leq k_q \leq 1.$$  

(17)

2) Controllable oscillating active power:

Instead of compensating the oscillating reactive power in (10), we can similarly control the oscillating active power $\hat{p}_{2q}$ in (9). For this purpose negative-sequence currents are imposed to meet

$$v^+_q \cdot i^-_q = -k_q v^-_q \cdot i^+_q, \quad 0 \leq k_q \leq 1.$$  

(18)

where the scalar coefficient $k_q$ is now a weighting factor for the elimination of $\hat{p}_{2q}$. Note that the subscript “$q$” in $k_q$ remains consistent as used in (12), representing reactive power ($Q$) control.

By considering that $v^+ \cdot i^- = v^+_q \cdot i^-_q$ (because $v^+_q$ lags $v^+$ by 90° and $i^-_q$ leads $i^-$ by 90°), the left side of (18) can be rewritten as

$$v^+ \cdot i^-_q = v^+_q \cdot i^-_q = -k_q v^-_q \cdot i^+_q,$$

(19)

where $i^*_q$ denotes the orthogonal vector of $i^*_q$ according to (2). Then, it follows that

$$i^-_q = \frac{-k_q v^+_q \cdot i^+_q}{\|v^+_q\|^2} v^-_q.$$  

(20)

Hence the negative-sequence current $i^-_q$ follows directly from (20) as

$$i^-_q = \frac{k_q v^+_q \cdot i^+_q}{\|v^+_q\|^2} v^-_q.$$  

(21)

Solving (21) and (11), the positive-sequence current and negative-sequence current are derived as

$$i^+_q = \frac{Q}{\|v^+_q\|^2 + k_q \|v^-_q\|^2} v^+_q,$$

(22)

$$i^-_q = \frac{-k_q Q}{\|v^+_q\|^2 + k_q \|v^-_q\|^2} v^-_q.$$  

(23)

Again, the total current reference $i^*_q$ is the sum of $i^+_q$ and $i^-_q$, that is

$$i^*_q = \frac{Q}{\|v^+_q\|^2 + k_q \|v^-_q\|^2} (v^+_q + k_q v^-_q), \quad 0 \leq k_q \leq 1.$$  

(24)
3) **Merging strategies 1) and 2):**

Simple analysis reveals that (17) and (24) can be put together as

\[ i_q^* = \frac{Q}{\|v^+\|^2 + k_q \|v^-\|^2} (v^+_q + k_q v^-_q), \quad -1 \leq k_q \leq 1. \] (25)

Further, by substituting (25) into (9) and (10), it follows that

\[ p = \frac{Q(1 - k_q) (v^+_p - v^-_q)}{\|v^+\|^2 + k_q \|v^-\|^2}, \] (26)

\[ q = \frac{Q(1 + k_q) (v^+_q + v^-_p)}{\|v^+\|^2 + k_q \|v^-\|^2}. \] (27)

It can be seen that the variant terms of (26) and (27), i.e. oscillating active power and reactive power, are controlled by the coefficient \( k_q \). These two parts of oscillating power are orthogonal and equal in maximum amplitude.

To observe the controllability of the strategy represented by (25), simulation results are shown in Fig. 3 by sweeping parameter \( k_q \), from -1 to 1, where the unbalanced voltage dips are assumed to persist. It is illustrated that either the oscillating active power or the oscillating reactive power can be controlled smoothly and even can be eliminated at the two extremes of the \( k_q \) curve. It is pointed out that the strategies presented in [9], namely positive-negative-sequence compensation (PNSC), average active-reactive control (AARC), and balanced positive-sequence (BPS) are equivalent to the results of the proposed strategy when \( k_q \) equals -1, 1, and 0, respectively. Other than only three specific points, there is a wide choice of possibilities in between with the proposed method, and moreover, strategies from one to another operation point can be simply shifted with the value of the coefficient \( k_q \). This controllable and smooth characteristic allows to enhance system control flexibility and facilitates system optimization. An application example will be presented in Section V.

**B. Active Power Control**

For a given level of injection into the grid of active power \( P \), which is determined by the power availability of the DG source and the converter ratings, the current reference for active power control can be derived similarly, as calculated from

\[ i_p^* = \frac{P}{\|v^+\|^2 + k_p \|v^-\|^2} (v^+_p + k_p v^-), \quad -1 \leq k_p \leq 1, \] (28)

where \( k_p \) is a similar adjustable coefficient as \( k_q \), but denotes a weighting factor for the elimination of oscillating active power or reactive power related to the active power \( (P) \) control. Hence, the subscript “\( p \)” in \( k_p \) denotes active power control. Detailed derivation of (28) has been presented in [13].

IV. **STRATEGIES FOR COMBINED P&Q CONTROL**

As already mentioned, some grid codes also require DG systems to contribute with reactive power [3] [4]. For example, with respect to the amplitude drop of voltages, DG systems having agreements with grid operators are expected to deliver both active power and reactive power during grid faults. Hence the reference currents for this case, named \( i_{pq}^* \), can be derived by adding (25) and (28), as expressed by

\[ i_{pq}^* = i_p^* + i_q^* = \frac{P}{\|v^+\|^2 + k_p \|v^-\|^2} (v^+_p + k_p v^-) + \frac{Q}{\|v^+\|^2 + k_q \|v^-\|^2} (v^+_q + k_q v^-), \] (29)

with \(-1 \leq k_p \leq 1, -1 \leq k_q \leq 1\). \( k_p \) and \( k_q \).

It can be seen that there are infinite combinations for (29) with independent coefficients \( k_p \) and \( k_q \). This also implies that the linear controllability benefiting from previous independent power control strategies does not really exist. In order to preserve the controllability, two joint strategies are proposed to simplify (29) by linking the two coefficients. To differ from the symbol \( k_p \) used in active power control and \( k_q \) used in reactive power control, the coefficient used for combined active and reactive power control in the following is defined as \( k_{pq} \).
A. Joint Strategy with Same-Sign Coefficients

By setting \( k_p = k_q = k_{pq} \) in (29), reference current calculation is simplified and rewritten as

\[
i_{pq}^* = \frac{S}{\sqrt{P^2 + Q^2}} \mathbf{R}(\varphi) \left( v^+ + k_{pq} v^- \right)
\]

where \( S = \sqrt{P^2 + Q^2} \) is the apparent power and \( \varphi \) the power factor angle, with

\[
\varphi = \cos^{-1}\left( \frac{P}{\sqrt{P^2 + Q^2}} \right).
\]

Since the \( \alpha\beta \)-reference frame is used in the experiment, it can be derived that

\[
\mathbf{R}(\varphi) = \begin{bmatrix}
\cos \varphi & \sin \varphi \\
-\sin \varphi & \cos \varphi
\end{bmatrix}.
\]

Note that \( \mathbf{R}(\varphi) \) will be different in the \( abc \)-reference frame.

On the basis of (30), the resulting currents and oscillating powers can now be adjusted by \( k_{pq} \). To help understanding, a vector diagram, representing voltage and current trajectories, and the relationship between oscillating powers are plotted with \( k_{pq} \) as an adjustable parameter under an unbalanced voltage dip and a certain \( \varphi \), for instance, \( 30^\circ \).

As shown in Fig. 4(a), when \( k_{pq} \) changes from 1 to -1, the length of current vectors changes and reaches a minimum value at \( k_{pq} = 0 \). In other words, the reference currents given by (30) become balanced when \( k_{pq} = 0 \). In Fig. 4(b), the amplitudes of the oscillating powers, which are calculated by substituting (30) into (7) and (8), also vary with the change of \( k_{pq} \). Because \( \varphi \) is not \( 0^\circ \) or \( 90^\circ \), i.e., active power or reactive power is not zero, \( \tilde{p}_{2\omega} \) and \( \tilde{q}_{2\omega} \) cannot be eliminated since either active power or reactive power delivery always introduces oscillating power at the two extremes of \( k_{pq} \).

B. Joint Strategy with Opposing-Sign Coefficients

By setting \( k_p = -k_q = k_{pq} \) in (29), the reference current is represented by

\[
i_{pq}^* = \frac{S}{\sqrt{P^2 + Q^2}} \left( v^+ + k_{pq} v^- \right)
\]

\[
+ \frac{S}{\sqrt{P^2 + Q^2}} \left( v^+ - k_{pq} v^- \right).
\]

Illustrative plots are drawn in Fig. 5. It can be seen from (33) that this joint strategy requires almost twice the computation time of joint strategy A. Fortunately, zero \( \tilde{p}_{2\omega} \) or \( \tilde{q}_{2\omega} \) can be achieved at the two extremes of \( k_{pq} \), as shown in Fig. 5 (b). Similar to joint strategy A, when shifting \( k_{pq} \) towards zero it implies that the length of the current vectors decreases and reaches a minimum value at \( k_{pq} = 0 \), achieving by this way balanced grid currents. For this value, the current trajectory becomes a circle, as shown in Fig. 5 (a).

The main aspects of the two joint strategies are tabulated in Table I. Strategy A can only remove oscillating power when \( \varphi = 0^\circ \) or \( 90^\circ \), whereas Strategy B allows a more flexible controllability. In summary, the simple adaptive controllability of independent power control strategies is preserved in the joint strategies above. This enables DG systems to be optimized under unbalanced voltage dips and make them flexible to meet the upcoming grid codes which might allow:

- Constant active/reactive power (Strategy B)
- Balanced grid currents (Strategy A or B)
- Unbalanced currents with limited unbalanced factor [14] (Strategy A or B)
- Average power delivery with limited oscillating power (Strategy A or B).

V. APPLICATION EXAMPLE: CONFINED OSCILLATING ACTIVE POWER

To illustrate the simplicity and adaptability of the proposed methods for on-line optimization control, an application example is given in this section, together with the design procedure. The joint strategy B presented in the previous section is
Fig. 5. Graphic representation of (a) grid voltage and current trajectories under unbalanced voltage dips in the stationary frame, and (b) relationship between oscillating active power $\tilde{p}_{2\omega}$ and reactive power $\tilde{q}_{2\omega}$ with $k_{pq}$ as an adjustable parameter under the joint strategy of (c), where $k_p = -k_q = k_{pq}$.

TABLE I

<table>
<thead>
<tr>
<th>Description</th>
<th>Strategy A</th>
<th>Strategy B</th>
</tr>
</thead>
<tbody>
<tr>
<td>Active power control</td>
<td>yes</td>
<td>yes</td>
</tr>
<tr>
<td>Reactive power control</td>
<td>yes</td>
<td>yes</td>
</tr>
<tr>
<td>Current symmetry control</td>
<td>yes</td>
<td>yes</td>
</tr>
<tr>
<td>Null $q_{2\omega}$ control</td>
<td>no$^*$</td>
<td>yes</td>
</tr>
<tr>
<td>Null $p_{2\omega}$ control</td>
<td>no$^*$</td>
<td>yes</td>
</tr>
</tbody>
</table>

1) only possible when $\varphi = 0^\circ$ or $90^\circ$.

Fig. 6. Control scheme of $k_{pq}$ for confining oscillating active power.

applied to confine the oscillating active power of DG inverters that deliver active power in normal grid conditions and support the grid with reactive power under voltage dips.

Firstly, the control range and regulating directions of $k_{pq}$ should be determined. Considering the power-electronics inverter-bridge constraints, a serious problem for the inverters is the second-order ripple on the dc bus, which reflects to the ac side and creates distorted grid currents during unbalanced voltage dips [15]. Either for facilitating dc-bus voltage control or for minimizing the dc-bus capacitors, it is preferred to make $k_{pq}$ in (33) close to -1 so that the dc voltage variations can be kept very small. However, the maximum deliverable power has to decrease because unbalanced phase currents reduce the operating margin of the inverters. Therefore, for a maximum power-tracking system it is preferable to shift $k_{pq}$ towards zero to get balanced currents as long as the oscillating active power is within a certain limit. It is concluded that the $k_{pq}$ should start from 0, and shifted towards -1 when the amplitude of the oscillating active power is more than a maximum threshold, denoted as $\tilde{p}_{2\omega,\text{pk}}$.

For a DG inverter, the maximum oscillating active power can be expressed as a function of dc-link capacitance and dc voltage variations, that is

$$\tilde{p}_{2\omega,\text{pk}} = 2\pi f_1 V_{dc} C_{dc} \Delta V_{dc}$$

(34)

where $f_1$ is the grid fundamental frequency, $V_{dc}$ is the average dc voltage, $C_{dc}$ is the dc-link capacitance and $\Delta V_{dc}$ the peak-peak value of the specified dc voltage variation.

According to (7), the oscillating active power is expressed by

$$\tilde{p}_{2\omega} = v^+ \cdot i^- + v^- \cdot i^+.$$  

(35)

Following that, Fig. 6 shows a simple control scheme for regulating $k_{pq}$. When the oscillating active power is bigger than the $\tilde{p}_{2\omega,\text{pk}}$, a proportional plus integral controller ramps up the value of $k_{pq}$ towards -1. Note that the integrator should be reset when the grid gets back to normal conditions. Therefore, the oscillating active power will be limited by the adaptive control of $k_{pq}$, while the output currents are controlled to be as balanced as possible. In similar manners, the proposed strategies can be adapted for optimizing other control objectives.

Note that the oscillating power on account of output filters are not considered. The effects of output inductors are well-known and therefore are shortly presented here. The instantaneous power control discussed so far has been treated at the grid connection point. When the oscillating power on account of the output inductors cannot be neglected (in case of large and unbalanced currents), the fundamental components of the inverter-bridge output voltages should be used for calculations instead of the grid voltages. Indirect derivation based on grid voltages and currents or direct measurement on the output of inverter bridge can be used to get the required voltages, as presented in [5] and [6].

VI. EXPERIMENTAL VERIFICATIONS

To verify the proposed strategy, experiments are carried out on a laboratory experimental system constructed from a four-leg inverter that is connected to the grid through LCL filters, as shown in Fig. 7. The system parameters are listed

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in Table II, and the grid impedances $Z_{ga,b,c}$ are assumed to be combined with the output inductors. By using a four-leg inverter, zero-sequence currents can be eliminated when the grid has zero-sequence voltages. For the cases where the zero-sequence voltage of unbalanced grid dips is isolated by transformers, a three-leg inverter can be applied. A 15kVA three-phase programmable ac power source is used to emulate the unbalanced utility grid, and the distributed source is implemented by a dc power supply. The controller is designed on a dSPACE DS1104 setup by using Matlab / Simulink.

A. Control Realization

As shown in Fig. 7, the proposed current controller is realized with a double-loop current controller, which consists of an outer control loop with proportional + resonant (PR) controllers for eliminating the steady-state error of the fundamental-frequency currents, and an inner inductor current control loop with simple proportional gain to improve system dynamics and stability. In addition, a feed-forward loop from the grid voltages is used to improve system response to voltage disturbances. Note that the current control used here is only for the laboratory test, but it is not the key point of this paper; other current controllers can also be applied [16]-[18].

The control for both positive-sequence and negative-sequence components would be much too complicated and computation-time consuming when conventional PI control with coordinate transformation is used. Therefore, it is preferred to choose a PR controller in the stationary frame. A quasi-proportional-resonant controller with a high gain at the fundamental frequency is used

$$G_1(s) = K_p + \frac{2K_r \omega_{br}s}{s^2 + 2\omega_{br}s + \omega_1^2}$$  \hspace{1cm} (36)

where $K_p$ is the proportional gain, $K_r$ is the resonant gain, $\omega_1$ denotes the fundamental radian frequency, and $\omega_{br}$ the equivalent bandwidth of the resonant controller. A detailed design for the PR controller has been presented in [19] and [20], it is not duplicated here. Through optimizing, the parameters used in the experiment are $K_p=2$, $K_r=100$, and $\omega_{br}=10$ rad/s.

Since the whole controller is designed in the stationary frame, the sequence detection of grid voltages is also realized based on a stationary frame filter cell in the $\alpha\beta$-frame [21]. As shown in Fig. 7, the outputs $v_{\alpha,\beta1}$ and $v_{\alpha,\beta2}$ denote the $\alpha$, $\beta$ quantities of fundamental positive-sequence and negative-sequence voltages, respectively. The basic filter cell can be easily implemented using a multi-state-variable structure. A high performance output can still be achieved under distorted grid voltages. Because of its robustness for small frequency variations, a phase locked loop (PLL) is avoided in the experiment since the ac power supply only emulates the magnitude drop of the grid voltage. Otherwise, a PLL should be added to the filter for adapting to large frequency changes [21].

---

**TABLE II
SYSTEM PARAMETERS**

<table>
<thead>
<tr>
<th>Description</th>
<th>Symbol</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output filtering inductor</td>
<td>$L_{ga,b,c}$</td>
<td>2mH</td>
</tr>
<tr>
<td>Output filtering capacitor</td>
<td>$C_{ga,b,c}$</td>
<td>5μF</td>
</tr>
<tr>
<td>Output filtering inductor</td>
<td>$L_{gb,c}$</td>
<td>2mH</td>
</tr>
<tr>
<td>Neutral filtering inductor</td>
<td>$L_n$</td>
<td>0.6/2mH</td>
</tr>
<tr>
<td>dc-link voltage</td>
<td>$V_{dc}$</td>
<td>750V</td>
</tr>
<tr>
<td>dc-link capacitors</td>
<td>$C_{dc}$</td>
<td>4400μF</td>
</tr>
<tr>
<td>Switching frequency</td>
<td>$f_{sw}$</td>
<td>10kHz</td>
</tr>
<tr>
<td>System rated power</td>
<td>$S_{out}$</td>
<td>15kVA</td>
</tr>
<tr>
<td>Tested apparent power</td>
<td>$S$</td>
<td>2500VA</td>
</tr>
</tbody>
</table>

---

Fig. 7. Circuit diagram and control structure of experimental four-leg inverter system.

Fig. 8. Emulated grid voltages to be faulty at $t=0.03$s, where two phases dip to 70%.
Concerning the power factor angle \( \varphi \), defined in (31), two values are tested in the experiment. Firstly, a slightly modified approach is used here to calculate the angle \( \varphi \) according to the grid code in [3]. Specifically, the DG system should inject at least 2\% of the rated current as reactive current for each percent of the fundamental-sequence voltage dip. Therefore, if the desired angle \( \varphi \) is calculated from

\[
\varphi = \sin^{-1}\left(2 \left| V^+ - V_N \right| / V_N \right)
\]  

(37)

where \( V_N \) is nominal voltage amplitude, and \( V^+ = \sqrt{(v^+_a)^2 + (v^+_b)^2} \) is the positive-sequence voltage amplitude, then the requirements in [3] will be satisfied. Furthermore, it is also required in [3] that a reactive power output of at least 100\% of the rated current should be possible when necessary. Hence also \( \varphi = 90^0 \) will be assigned directly to test a complete range from active power to reactive power.

Note that dc-link voltage control is not added here. Usually, a dc-link voltage control loop is included in the control structure, for instance, in a rectifier system [6] or for a wind turbine inverter [7]. The dc bus in the experimental system is only controlled by the dc power supply with a quite low bandwidth to maintain a stable dc bus in an average sense. Since the experiment intends to investigate the effects of the proposed strategies when choosing different \( k_{pq} \), it is convenient to leave out the dc voltage control in order to only observe the performance of the power control strategies.

B. Tests of the Joint Strategies at Specific Operating Points

By shifting the controllable parameter \( k_{pq} \) to specific values, the system is tested under unbalanced voltage dips with the joint strategies. In order to capture the transient reaction of the system, three situations are intentionally tested under equivalent voltage dips.

As shown in Fig. 8, grid voltages are emulated to be faulty at \( t = 0.03s \) where two phases dip to 70\%. Consequently, the power factor angle \( \varphi \) derived in the control by (37) is 23\(^0\) and the corresponding results of joint strategy \( A \) are shown in Fig. 9. In order to allow clear observation of the low-order oscillating power, high-order components are filtered out. It can be seen that the reactive power support starts within half a cycle after voltage dips. As analyzed in Section IV, the instantaneous active power and reactive power always have oscillating ripples, and the injected grid currents get balanced only when \( k_{pq} \) becomes near to zero. In case \( \varphi \) equals 90\(^0\) (intentionally imposed), the joint strategy \( A \) turns out to be a reactive power control strategy as expressed by (25). Comparing with the simulation results in Fig. 3 at the point of \( k_q = -1, 0 \) and 1, it can be seen that the results in Fig. 10 show the same effects on the regulation of oscillating power and reference currents.
Besides, it is noticed that the delivered active power and reactive power have a certain ramp slope when changing from one average value to the other, and this is because a low-pass filter is used to smooth the output of the power factor angle $\varphi$ as calculated in (37). To detect accurate symmetric-sequence voltages under unbalanced and/or distorted voltage dips, the sequence detection filter used in Fig. 7 was designed with a settling time of 20ms, which copes well with a test condition up to 40% voltage dip. As a result, the outputs from the sequence detection filter after the moment of voltage dips will cause low-frequency ripple on the magnitude value of $V^+$ in (37), due to unequal amplitudes of $v^+_{d1}$ and $v^+_{b1}$ at the transient. An extra low-pass filter with also a settling time of 20ms was used to smooth further the ripple effects on the power factor angle. For other unbalance conditions, the settling time of the sequence detection filter and the low-pass filter should be optimized. Other fast detection techniques [22] can also be used, thereby improving the design.

Under the same test conditions, experimental results are also measured for joint strategy B. As shown in Fig. 11, nearly zero oscillating reactive power and active power are achieved at $k_{pq} = 1$ and -1, respectively. When $k_{pq} = 0$, the results of joint strategy B are the same as the results of joint strategy A, since both joint strategies only depend on positive-sequence components in this case. The results with $\varphi = 90^0$ are given in Fig. 12. Comparing with the results in Fig. 10 of joint strategies A, it is clear that the joint strategies with opposite signs of $k_{pq}$ turn out to produce the same results.

It is noticed that the grid currents are slightly distorted especially after a grid fault. There are two reasons that cause the low frequency harmonic currents. The first one is the dead-time effect of the IGBT inverter bridges used in the lab system. Harmonic distortion is introduced into the output voltage of the inverter when the dead time is large to a certain extent (in this work, a fixed dead time reaches 4% of the switching period), resulting in distorted currents through small grid impedances. This distortion will be less when increasing the fundamental currents. The second reason is the dc-link voltage variations. In the experimental verifications, a dc-link voltage control was not implemented in order to allow observing the behavior of the power control strategies. In this case, variations are present in the dc voltage when changing the delivered power after grid faults and/or when oscillating active power exists. Therefore, the voltage variations reflect to the grid side, leading to low frequency current harmonics.

C. Test of Adaptive Control with Joint Strategy B

Next, the joint strategy B is applied to confine the oscillating active power of the inverter under unbalanced voltage dips with the control scheme presented in Section V. The PI parameters are $K_P = 0.1$ and $K_I = 50 \text{ s}^{-1}$. Because the tested power level is around 2.5kW, a value of 200W is assigned to...
active power and reactive power control of distributed generation inverters operating under unbalanced voltage dips. The impact of symmetric-sequence components on the instantaneous power and the interactions between symmetric sequences have been explained in detail. Furthermore, for simultaneous control of active and reactive power, two joint strategies have been proposed, yielding an adaptive controllability that can cope with multiple constraints in practical applications. Application examples together with experimental results have been given, which verify the validity of the proposed ideas.

VII. CONCLUSION

This paper has proposed methods for independent active and reactive power control of distributed generation inverters operating under unbalanced voltage dips. The impact of symmetric-sequence components on the instantaneous power and the interactions between symmetric sequences have been explained in detail. Furthermore, for simultaneous control of active and reactive power, two joint strategies have been proposed, yielding an adaptive controllability that can cope with multiple constraints in practical applications. Application examples together with experimental results have been given, which verify the validity of the proposed ideas.

REFERENCES


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