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A Hairpin Antenna-in-Package Concept for RFID Tag Applications
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Abstract — Antenna integration is a relative new concept for the miniaturization of Radio Frequency (RF) transceivers where a key problem is to achieve a small size at low cost. Traditional solutions, using ceramic materials, are too costly. Additionally, the current trend in RFID applications is the development of smaller and cheaper tags. In this paper, we present an elegant and low-cost Antenna-in-Package (AiP) solution for RFID tag applications. A printed dipole antenna that integrates the balun function with improved filtering characteristics is designed on a low-cost dielectric material. Simulation and measurement results verify the performance of the proposed antenna.

Index Terms — Antenna-in-Package (AiP), antenna integration, balun, Radio Frequency Identification (RFID) tags.

I. INTRODUCTION

Radio Frequency Identification (RFID) technology has become a mainstream application used in several fields such as: public transport, ticketing, access control, animal identification, and industrial automation [1]. RFID technology enables the identification and tracking of a distant object using radio waves. Moreover, it can incorporate more data, or even measure ambient conditions, such as temperature [2]. These possibilities reveal the importance of introducing this technology in the logistic chains of perishable goods (i.e., fruit, flower, medicine). Therefore, RFID technology could greatly improve the efficiency during shipment and guarantee the product quality when compared to the existing logistic chains that result in large amounts of waste. However, the mass introduction has not been realized yet due to the high-cost solutions that exist today.

Perishable mAnagement through Smart Tracking of lifEtime and qUality by RFID (PASTEUR) is the topic of a European project that involves the collaboration of universities and industries from the Netherlands, Belgium, Spain, and Austria. The goal of the project is the investigation and realization of an RFID-based sensor platform. The platform will track ambient conditions in order to guarantee product quality and establish a more efficient quality control in the logistic chains of perishable goods. The main technological drivers are the miniaturization of electronic systems together with low-cost and energy efficient system integration. Part of this work is also the investigation and integration of the RFID tag antenna for the development of highly integrated RF transceivers.

Moving to the 5.8 GHz band has enabled the design of smaller RFID antenna structures. Moreover, the development of system-in-package (SiP) modules has offered the possibility to integrate a complete system with the necessary passive components into a single packaging module [3]. However, the antenna size is comparable to the wavelength of operation (~52 mm in air at 5.8 GHz) and the antenna is usually external to the integrated circuit (IC). Therefore, in order to achieve a higher degree of integration, a relative new, elegant solution in antenna design, the Antenna-in-Package (AiP), has been investigated. The AiP technology integrates an antenna or multiple antennas in the chip package [4], saving board area and cost, and maximizing the performance of a RF module. Several antenna concepts have been proposed in the literature. In [5]-[8] microstrip patch antennas and Printed Inverted-F Antennas (PIFA) are realized on ceramic materials, resulting in thin packages. The low temperature co-fired ceramic (LTCC) substrate technology offers high dielectric constants and an arbitrary number of layers for the miniaturization of microwave and millimeter wave circuits [9]. However, it is a costly solution, preventing the mass production of the antenna designs.

In this paper, we present an elegant and low-cost AiP solution operating around 6 GHz. A printed dipole is realized on top of a ground plane on a thin, low-cost material. The proposed concept can be used for realizing plastic packages using molding techniques. Moreover, the antenna solution integrates the balun function and achieves a sharp resonance with improved filtering characteristics, which can be beneficial for certain RFID applications. Finally, the proposed antenna has a significant smaller size as compared to the structure reported in literature [4].

II. ANTENNA DESIGN

Previous AiP studies mostly used antenna concepts based on the microstrip patch antenna and the PIFA. Common to both designs is that they require a ground plane. The presence of the ground plane is of vital importance in AiP designs since it serves as the shielding mechanism that protects the active circuitry from the
antenna radiation. For the case of a microstrip line antenna the presence of a ground plane in a very close distance results in a high quality factor. Consequently, the line behaves as a microstrip resonator with very low radiation losses. Zhang et al. designed a microstrip line AiP in LTCC technology for single-chip RF transceivers in the 5.8 GHz frequency band [4]. In this study, they proposed an easily configurable structure for adjusting the resonance frequency, in close proximity to the ground plane. The antenna achieved high radiation efficiency by the appropriate geometrical configuration of the line. The achievements in [4] with respect to AiP designs served as inspiration for our design. However, we consider the use of LTCC substrate technology as a drawback since it increases the cost of the final solution.

The goal of this study is to realize a smaller and cheaper AiP solution, which is compatible with plastic packaging technologies. Therefore, as a first step in our approach, we transferred the design of [4] onto a dielectric material (RO3003) with a dielectric constant of 3 [10]. An AiP with a board area equal to 25 x 25 mm$^2$ and a simulated radiation efficiency of 73.3% was achieved. Fig. 1 shows a photograph and the measured input impedance of the antenna. Hence, the realization of an AiP on substrates of lower permittivity is proven. However, the lower $\varepsilon_r$ has resulted in a larger element size.

In order to reduce the size of the antenna solution, the design of a half-wavelength printed dipole is investigated. The distance from the ground plane affects the dipole’s resonant resistance. The input impedance and, thus, the radiation resistance of a dipole requires a specific value for matching purposes. For thin substrates the radiation resistance of the first resonance is very small since the dipole’s first resonance is of series-type and the power that is radiated is also small [11]. In addition, dipoles are differential structures. Targeting a single-ended chip, it is necessary to connect a balun to the antenna. This will result in additional space requirements, more losses, and would increase the production yield time and assembly cost. In order to overcome these inherent problems, a new feeding structure is designed. The structure integrates the balun function and results in a higher resonant resistance that can be matched to the input impedance of the IC at the desired frequency. It is based on parallel-coupled, half-wavelength resonator hairpin-line filters [12] and improves the frequency selectivity of the antenna. Fig. 2 shows the top and side views of the proposed AiP. As shown, it is composed of three microstrip lines that are closely coupled. The third line is a half-wavelength dipole for operation at 6 GHz. The substrate material used is NelcoNX9 with a dielectric constant of 3 [13]. The resonance frequency of the antenna can be easily adjusted by changing the length $l_4$ of the dipole (see Fig. 2).

The commercial software HFSS was used for optimizing the antenna [14]. The design parameters are summarized in Table I. The resulting board area is 9.58 x 17.8 mm$^2$ for operation at 6 GHz. Furthermore, the substrate height is selected to be $h = 0.5$ mm in order to prevent the excitation of the TE$_1$ surface wave mode [11], and for the total volume of the structure to remain small. The simulated resonance frequency is 5.991 GHz and the impedance bandwidth for which $S_{11}<-10$ dB is 38 MHz from 5.972 GHz to 6.01 GHz. A simulated radiation efficiency and gain of 61.5% and 3.27 dBi, respectively,
are achieved. In Fig. 3 the magnitude of the current density on the structure is depicted.

Finally, Fig. 4 shows our proposed antenna integration concept in a ball grid array (BGA) package that carries the RF chip. Three dielectric layers, two buried, and a top metal layer are depicted (see also [5]). The radiating element is realized in the top metal layer. The bottom buried metal layer is used for the chip base and the signal traces, while the upper buried layer provides the antenna ground plane. The antenna is excited with a via through a circular aperture in the ground, which connects to the chip with bond wires and traces. Finally, external solder balls connect to the chip through bond wires, signal traces and vias.

![Fig. 3. Magnitude of current density of the hairpin antenna.](image1)

![Fig. 4. Side view of the antenna integrated in a BGA package.](image2)

| TABLE I |
| --- | --- |
| **ANTENNA DESIGN PARAMETERS** | |
| **Parameters** | **Values (in mm)** |
| $l_1$ | 2 |
| $l_2$ | 11 |
| $l_3$ | 2 |
| $l_4$ | 15.6 |
| $w$ | 1.2 |
| $s$ | 0.14 |

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III. MEASUREMENT RESULTS

The proposed AiP was fabricated in Nelco NX9. A photograph of the manufactured prototype with the attached connector is shown in Fig. 5. In order to allow space for the connector, the feed line was extended ($l_1$=6 mm). For comparison purposes, simulations that included the extra line length and a model of the connector were performed in HFSS [14]. In Fig. 6, both measurement and simulation results of the input impedance are shown. Fig. 7 depicts the simulated and measured S11. A frequency shift of 88 MHz can be observed. Additionally, the measured imaginary part gives higher values. This is considered to be the result of the connector since a simplified simulation model has been analyzed. Also,

![Fig. 5. Photograph of manufactured hairpin AiP.](image3)

![Fig. 6. Simulated and measured input impedance of the hairpin AiP.](image4)

![Fig. 7. Simulated and measured S11 of the hairpin AiP.](image5)
manufacturing and permittivity tolerances can result in a lower resonance frequency [13]. Finally, the higher inductive imaginary part results in a deteriorated S11. The measured resonance frequency is 5.926 GHz and the bandwidth for which S11< -6 dB is 50 MHz while the simulated bandwidth is 80 MHz for resonance at 6.014 GHz.

The radiation patterns for the two principal planes at 5.93 GHz are shown in Fig. 8. The measurements were performed in an anechoic chamber and a close agreement with the simulations was found. The resulted radiation pattern has a maximum radiation towards 0° in the E-plane and towards 22° in the H-plane. This is considered to be the result of the printed dipole being at the edge of the ground plane. A low backside radiation is also achieved, serving the purpose of effectively shielding the die from the antenna radiation. The low signal level in the H-plane around 270° is the result of the connector that blocks the signal path. Also, the differences of the signal level in the lower hemisphere of the E-plane can be seen as the result of the signal strength being close to the noise floor. Finally, the measured peak gain in the H-plane is equal to 2.84 dBi.

IV. CONCLUSION

In this paper a small, low-cost AiP design for RFID applications around 6 GHz was presented. A dipole antenna with an integrated balun function and improved filtering characteristics was designed on a thin, low-cost material with low dielectric constant. Finally, the performance of the antenna was measured and verified. The antenna achieved good operation around 6 GHz while providing a single-ended solution with improved filtering characteristics that is beneficial for RFID applications. Additionally, it is a low cost antenna that can be integrated with plastic packages resulting in a reduced size and cost of the final product.

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