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PACKAGED PHASAR-BASED WAVELENGTH DEMULTIPLEXER WITH INTEGRATED DETECTORS


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ABSTRACT

For the first time, PHASAR-based wavelength demultiplexers with integrated detectors have been packaged in an industry-standard Butterfly-type module. 8-Channel nearly polarisation-independent operation is demonstrated. In addition, 4nm tuning is realised.

I. INTRODUCTION

The quickest growing market segment in optical telecommunications is that of Wavelength Division Multiplexing (WDM) systems. Presently, these systems find are deployed in high-end point-to-point links, and are constructed from discrete components such as wavelength-specific DFB lasers, modulators, (de-)multiplexers, and receiver photodiodes. As the number of installed WDM systems grows, this technology will penetrate further into the network, which will call for a reduction of the number of separate components, in favour of cost-effective integrated solutions.

On the receiver side of a WDM link, a single module containing a wavelength demultiplexer, photodetectors, and transimpedance amplifiers, is an attractive component. Recently, a number of wavelength demultiplexers with integrated photodetectors have been reported [1], [2], [3], [4], [5]. In addition, both hybrid [6] and monolithic [7] integration of a PHASAR and preamplifiers have been shown, as well as a packaged WDM receiver module containing a pigtailed spectrograph and a separately pigtailed pin-JFET array [8].

In this paper, the first packaged InP PHASAR-based demultiplexer with integrated detectors is presented, using an industry-standard Butterfly module. This is made possible by the compact dimensions of the opto-electronic chip. Nearly polarisation-independent CW operation is demonstrated for 8 channels with 200GHz spacing. In addition tuning of the channels is realised over 4nm.

II. DESIGN

Polarisation-independent operation is obtained by making use of non-birefringent waveguides [9]. The $8 \times 8$ PHASAR chip, shown in Fig. 1, is designed to have a central wavelength of 1535nm, a 200GHz channel spacing, and a 50nm free spectral range. Its size is only $1.4 \times 1.3\text{mm}^2$. To transfer light from the output waveguides to the InGaAs absorption layer, evanescent coupling is employed in the detectors. Using the optimised layer stack shown in Fig. 2, simulation work predicts a better than 99% coupling efficiency in the $200\mu\text{m}$ long, $8\mu\text{m}$ wide detectors.

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After Low-Pressure MOVPE growth, processing starts with definition of the detector mesas using selective wet chemical etching. In the same step, the detector layers are removed from the area designated for the PHASAR. Outside this area the detector layers are left in place to absorb scattered light. Next the p-metallisation is fabricated, which consists of 6μm wide strips on top of the detector mesas, connected to 100 × 100μm wide bond pads by 10μm wide leads. Prior to deposition of the metallisation, a Si3N4 isolation layer is deposited. Fabrication of the PHASAR is performed by means of direct electron-beam writing techniques and reactive ion etching. Finally, the wafer is thinned and the n-metallisation is deposited.

The wafer is cleaved into dies of 3.5 × 3mm², and an AR coating is applied to the front facet. Subsequently, the device is mounted on a Si submount and a 6 × 6mm² carrier with 8 contacts. Fibre-chip coupling is accomplished by means of a lensed fibre, having a 12.5μm radius of curvature allowing less than 1dB coupling loss. The fibre is actively aligned to the appropriate input waveguide, and fixed by soldering it on a stud mounted on a base plate in front of the carrier. Finally, the fibre-chip assembly is mounted on a Peltier cooler inside a 14-pin industry-standard Butterfly package (see Fig. 3).
from the array of detectors cleaved off the PHASAR). The low efficiency of the detectors has been tracked down to a problem with the Si$_3$N$_4$ isolation layer, which breaks down at reverse voltages above 1V. In addition, this problem is responsible for the large background of one of the channels. Separately fabricated reference photodiodes having a good Si$_3$N$_4$ layer exhibit an external efficiency of -3dB at a reverse voltage of 5V, including coupling and waveguide loss.

In addition to the 8-channel polarisation independent device, one of the devices reported in Ref. [3] has been packaged as well. As shown in Fig. 5, tuning over more than 4nm has been obtained with virtually no performance degradation, by changing the chip temperature using the built-in Peltier cooler.

V. CONCLUSIONS

First results have been presented of PHASAR-based wavelength demultiplexers with integrated photodetectors which are packaged in industry-standard Butterfly-type modules. 8-Channel nearly polarisation-independent operation has been demonstrated, as well as tuning of the channels over more than 4nm.
The contributions of T. van Dongen (crystal growth), M. Vermeulen-Hartjes (device coating), and A. van Leerdam (device mounting) are gratefully acknowledged. Part of this work has been supported by the ACTS AC028 TOBASCO project.

REFERENCES


