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Novel Concept for an Integrated Optical Waveguide Isolator for Picosecond Pulse Operation

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Using current photonic integration technology different active and passive optical components can be combined on a single chip. The operation of active components, such as semiconductor optical amplifiers (SOAs) and in particular lasers, is disturbed by optical feedback down to the -50dB level. Such feedback can be caused by reflections of the device output signal on components further downstream in the circuit. Thus some form of integrated optical isolation is required to prevent destabilization of active devices.

Most attempts to obtain integrated optical isolators are based on magneto-optic effects [1] to break the symmetry between the forward and backward propagation direction in a waveguide. Such work is however targeted towards a general purpose optical isolation. In many practical applications the forward traveling signal is a train of short optical pulses such as those generated by a mode-locked laser. The feedback, which is the backward traveling signal, is typically amplified spontaneous emission (ASE) from another active component or a reflection of the forward traveling signal. In both cases the backward traveling signal will be lower in peak power by about two to three orders of magnitude. This fact can be utilized to achieve optical isolation.

In this paper we propose the use of a concatenated array of SOAs and saturable absorbers (SAs, reversely biased SOAs) as an integrated optical waveguide isolator for picosecond pulses (Fig. 1(a)). The saturation energy of the SA is smaller than the saturation energy of the SOA. An asymmetry in the forward and backward gain, i.e. isolation, can be created by operating the SOA/SA pairs at a point where the pulse energy of the forward propagating signal can saturate (open) the SAs as opposed to the backward propagating signal, which has a lower power or pulse energy. When the forward traveling signal opens one of the SAs, the backward traveling signal is not attenuated by that SA. To ensure that all parts of the backward going signal are attenuated, multiple SOA/SA pairs are used. As the forward signal is pulsed this configuration ensures that the backward traveling signal always propagates through several unsaturated (closed) SAs. The periodicity of the pairs of the SOA/SA pairs can be optimized for a specified pulse rate.

Simulations of these devices have been performed using SOA and SA rate equation models as presented in [2], extended with an ASE term. The parameters used are valid for the InP/InGaAsP material. Figure 1(b) shows the simulated isolation for a 2mm device, consisting of ten SOA/SA pairs. Simulated isolation levels are 20dB-35dB for the devices with the longer SAs. With typical reflection levels of 0.1% to 1% on chip, this keeps the feedback below -50dB. As such the device is a very promising option for integration with a MLL. Furthermore generated peak ASE levels are low, below 15μW in both directions, for the given injection currents. A first series of experimental devices has been realized and we expect to be able to present first results.

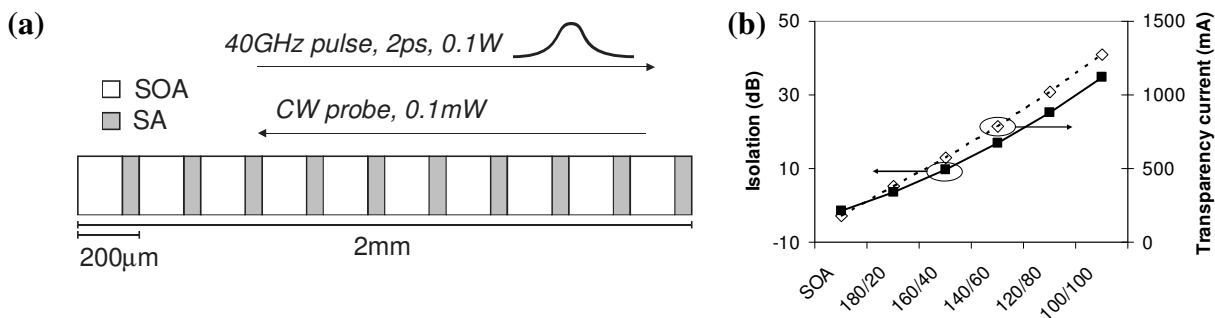


Fig. 1(a) Simulated device configurations: each SOA/SA pair has a combined length of 200μm, with a varying length ratio. The forward signal is a 40GHz train of pulses with a duration of 2ps and a peak power of 0.1W, typical for integrated MLLs [2]. The backward signal is a 0.1mW continuous wave probe signal.

(b) Simulated absorption (isolation) for different device configurations (SOA/SA lengths are given (μm), 2mm SOA for reference) of the backward probe signal when the device is operated at transparency for the forward signal. The total device injection current (i.e. for the ten SOAs) needed to obtain transparency is shown. The SA recovery time is 5ps.

References

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