Towards standardization of compact data center switches✩,✩✩

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Received 28 February 2017; received in revised form 10 May 2017; accepted 23 May 2017
Available online 16 June 2017

Abstract

Data Centers, the essential infrastructure for the increasing amount of cloud applications, are required to expand in number of devices even further. However, the scaling is limited by both space and power constraints. It is therefore essential to shrink the size and power consumption of the devices. This work explores how On-Board Optics provides a solution for data center switches. We experimentally demonstrate a proof-of-concept data center switch with integrated On-Board Optics modules, power consumption below 150W and measuring only 0.25U. We further discuss the standards required to fully exploit this technology, and the future of On-Board Optics. © 2017 The Korean Institute of Communications Information Sciences. Publishing Services by Elsevier B.V. This is an open access article under the CC BY-NC-ND license (http://creativecommons.org/licenses/by-nc-nd/4.0/).

1. Introduction

Society is increasingly relying on cloud applications such as Amazon, Google, or Facebook. These cloud applications are commonly executed in large data centers, which are a collection of switches and servers optically interconnected with a chosen topology [1,2]. For instance, a data center implementing a 3-level fat-tree topology requires 5120 128-ports switches to interconnect 131072 servers with full bi-section bandwidth [3]. Assuming one rack unit (1U) devices, 50 U/rack, and 300 W/device [4], this example requires above 2700 racks and 40 MW only for the devices. It is clear that adding more devices with similar characteristics, in order to continue expanding the computational capacity of these data centers, is reaching the point where it is unpractical in terms of space and power.

The solution to this problem is to reduce the size and power consumption of the devices. For instance, by cutting by one half the size and power of the switches and servers, it is possible to duplicate the amount of devices in the data center without requiring extra space or power. For the servers, there is a clear trend towards more modular and compact solutions [5]. However, the dominant design consideration for switches is that optical interfaces are front panel pluggable transceivers. The form factor of these standard optical modules leads to switches which are 1U in size having a front panel fully populated with transceivers, and consuming around 300 W [4]. On-Board Optics (OBO) transceivers are a promising alternative to achieve smaller data center switches with reduced power consumption. They are compact and not limited by the front panel bottleneck to increase the bandwidth density of the rack unit. Besides, OBO modules are more power efficient because they are located closer to the network switch chip. However, OBO modules lack an updated standard [6].

This work explores, experimentally, how the future release of updated standards for OBO transceivers could influence the design of data center switches. As a proof-of-concept, we have designed and implemented a $128 \times 10$ Gbps switch exploiting OBO. With a size of less than one fourth of the standard 19" rack case, it achieves power consumption of less than one half of the traditional switches. In addition, in this work we examine the need for new standards to maximize the benefits of this technology and future developments of OBO.
2. Data center switches with front panel transceivers

Fig. 1 shows a typical 1U rack case layout of a data center switch [4]. The main components of the Printed Circuit Board (PCB) are one single switching ASIC (Application-Specific Integrated Circuit), the required receptacles for the front panel transceivers, and the socket for the CPU control board. In addition, the rack unit includes a redundant power supply, and hot air extracting fans located in the back.

Regarding transceivers choice, industry seems to favor 4-ports front panel transceivers that lead to 1U devices. For instance, the 10G version of the 128-ports switching ASIC [7] is packaged with 32 QSFP (4 × 10G) transceivers, and the 25G version [8] with 32 QSFP28 (4 × 25G) transceivers. And the recently announced 50G (2 × 25G) version of the 128-ports switching ASIC will be probably assembled with 32 DD-QSFP transceivers, whose standard has not been released yet. While it is clear that pluggable modules offer ease of use, they cannot compete on density and power consumption with OBO as it is shown in the following sections.

3. On-Board Optics transceivers

On-Board Optics is a promising alternative to front panel transceivers. Both types of transceivers provide the same functionality. They are responsible for the electro-optical conversion (E/O and O/E) required to electronically switch the data being transmitted through optical fibers. For instance, a basic VCSEL-based (Vertical-Cavity Surface-Emitting Laser) transceiver includes in the transmitter side the VCSEL driver and the VCSEL array, and in the receiver side, the PD (Photodiode) array, and the TIA/LA (TransImpedance Amplifier/Limiting Amplifier) [9].

However, OBO modules can be designed to be more compact and power efficient than the corresponding front panel transceivers. The main reason for this is that, being placed closer to the switching ASIC, OBO modules deal with shorter PCB channels (and reduced losses), which simplifies its design. For instance, a QSFP (4 × 10G) front panel transceiver requires approximately the same power but double area than the commercial 12 × 10G OBO module [10] integrated in the prototype introduced in the next section. Even more compact devices have been reported, such as a 24 × 20G transceiver in just 14 mm × 14 mm area [11].

4. Data center switches with On-Board Optics

4.1. Experimental prototype and packaging

Fig. 2 presents the proof-of-concept design of a data center switch with OBO transceivers in the lower left corner. It integrates the 10G version of a 128-ports switching ASIC [7], covered by a fan in the figure. Eleven 12-ports OBO transceivers [10] surround the switching ASIC, providing the optical interfaces through MPO (Multi-Fiber Push On) connectors. The top left corner of the board includes the interface connectors, and the voltage converter blocks. A total of nine phases generate the internal voltages required by the board from a single 12 V power input. The 12 V input is suggested by the Open Compute Project (OCP) to enable power distribution engineers to optimally design the amount and placement of power supplies in the data center, and also, to fully exploit the rack unit space. The top right corner of the board includes the CPU control board, with an orange heatsink, that runs the operating system, and converts the openflow messages received from the data center controller into adequate instructions for the data plane switching ASIC. Below the CPU board are all the rest of the components. The final size is 200 mm × 200 mm, and makes it one of the most compact, fully integrated, single 128-ports switches ever implemented.

Fig. 2 also shows the packaging proof-of-concept of four of those switches in a single rack unit. We did not attempt to build a larger switch in agreement with [12].

Each one of the compact boards requires only half of the rack width. Besides, thanks to the single 12 V power supply input, it is possible to remove the power supplies from the rack unit, and fully populate the space available with four boards. The front panel, free from transceivers, accommodates the MPO connectors array. While this design provides four times the bandwidth density, compared with a switch implementation using QSFP modules, there is still enough room in the front
panel for air openings. This, together with the additional space in the back panel for extra fans, improves the heat removal from the case.

4.2. Power characterization of a single switch

The implemented switch shown in Fig. 2 has a maximum power consumption of 150 W by design. It is distributed in approximately 100 W for the switching ASIC, 40 W for the OBO transceivers, and 10 W for the CPU control board.

Fig. 3 shows the measured results for the power required by a single switch. The minimum power consumption, around 7 W, is achieved by turning off the data plane (ASIC and optical transceivers), and leaving the CPU control board on. Potentially, since the switches are SDN (Software Defined Network) enabled, it is possible to power down a part of the system to greatly reduce the power consumption of the network. Once the ASIC and transceivers are switched on, there is a sharp rise in power consumption. Then, a set of tests transmitting packets are executed with the ports loopbacked. With 24/48/72/96 ports enabled the power consumption increases to 83.3 W/89.7 W/97.9 W/105.6 W, respectively.

The maximum load test with only 96 ports is due to limitations imposed by the normal operation mode of the ASIC. The gap between the maximum designed power consumption, and the experimentally measured, can be associated to the loading of the CPU, the setting of the ASIC in oversubscription mode, and transceivers. Thus, we can conclude that our OBO based design can operate at a power consumption far below 150 W which is 50% less than a standard 1U switch.

5. Discussion

5.1. Front panel transceivers vs. On-Board Optics

Up to the present day, front panel transceivers dominate data center switches market. They are easy to replace, and have standards allowing modules interchangeability. However, they usually lead to 1U, power hungry devices that make difficult further scaling of data centers. In contrast, OBO transceivers allow compact, and low power consumption data center switches. Our prototype has a size of 0.25U, and requires less than 150 W to operate. With these results, the amount of devices in a data center such as the one of the introduction could be duplicated without requiring extra space or power, assuming that similar results are possible with servers [5].

5.2. Standards for compact data center switches with On-Board Optics transceivers

In order to take full advantage of using OBO as optical interface for electronic data center switches, we believe that three aspects require new up-to-date standardization. First, is needed an updated standard packaging of the modules to allow devices interoperability and interchangeability in a similar manner to pluggable transceivers, and benefit from economies of scale. The main transceivers’ manufacturers are joining efforts in the Consortium for On-Board Optics (COBO) [13] with this goal in mind. Second, is needed a standard chip-to-chip electrical interface for very short distances to lower down the power consumption even further. Nowadays, switching ASICs are optimized for front panel transceivers with longer paths, and bigger losses, with standards like KR4. They include blocks like Decision Feedback Equalizers (DFE) that can be removed with OBO [14]. The Optical Internetworking Forum is working on the definition of a standard for OBO, the Extra Short Reach (XSR) for 56G chip-to-chip interconnects [15]. The release and adoption of such standard will enable further reduction in the power consumption of ASIC and transceivers. Third, the introduction of a standard for a single power supply input (potentially) frees precious space in the rack unit to fully populate it with switches (or servers). Our prototype switch and packaging adopts the 12 V standard suggested by OpenCompute [16], and achieves the integration of four switches in a single rack unit. In contrast, many commercial products still include a redundant power supply inside the rack case (and a single switch). Although they may provide some of the voltages required in the board (3 V, 5 V, and/or 12 V), they do not include all the voltages that present low power ICs may need. For instance, 1.0 V, 1.2 V, 1.5 V, and 1.8 V are becoming popular, and it is necessary to generate them in the board.

5.3. Future of On-Board Optics

The cost and power consumption of OBO drops with the number of lanes, and eventually, with the data rate [11]. The introduction of 56G standards will enable even cheaper (in power and cost) modules by the improved CDR (Clock Data Recovery) circuits. For instance, the energy per bit with four (twelve) channels increases from 25 (20) pJ/bit at 10G to 35 (22) pJ/bit at 25G, and it is predicted that will lower down to 19 (15) pJ/bit at 50G. The cost per Gbps will decrease with four (twelve) channels from 3.0 (1.5) $/Gbps at 10G, to 1.8 (1.2) $/Gbps at 25G, and to 1.2 (1.0) $/Gbps at 50G.
6. Conclusions

Adding more devices to data centers without requiring additional space and power is challenging. The solution passes through shrinking switches and servers in size and power. Regarding switches, OBO is a promising technology. In this work, we have experimentally demonstrated a 0.25U data center switch with power consumption below 150 W. These results, together with similar efforts in servers, enable duplicating the size of data centers without additional needs of space and power. However, to fully exploit the benefits of OBO, new standards for module interchangeability and very short chip-to-chip electrical interfaces are required.

Acknowledgment

This work has been supported by the FP7 European Project COSIGN (FP7-619572).

References